

ANTREX Electronics

Electronic Components Sourcing Information

Product Reference Information

Part Number:	SQ2364EES-T1_GE3
Manufacturer:	Vishay Siliconix
Package:	Please confirm with sales
Availability:	Please confirm with sales

Product Page:

<https://antrexco.com/product/details/vishay-siliconix/sq2364ees-t1-ge3.html>

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Contact Information

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Quality & Trust

New & Original Components

Supplier verification and packaging condition review

CoC / RoHS / REACH documents available on request

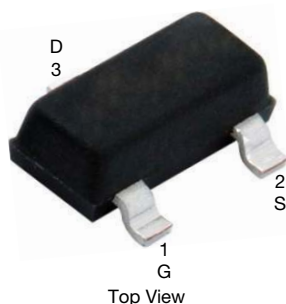
Secure sourcing support for OEM / EMS projects

Note:

This PDF includes an ANTREX product reference cover page.

The original manufacturer datasheet follows from the next page.

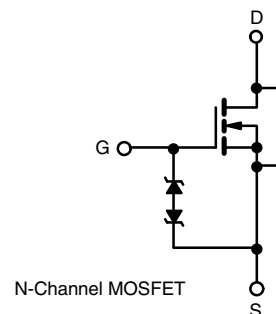
Automotive N-Channel 60 V (D-S) 175 °C MOSFET

SOT-23 (TO-236)

Marking code: 9Jxxx

FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified
- 100 % Rg and UIS tested
- Typical ESD protection 800 V
- Material categorization:
for definitions of compliance please see
www.vishay.com/doc?99912

AUTOMOTIVE
GRADE

RoHS
COMPLIANT
HALOGEN
FREE

PRODUCT SUMMARY

V_{DS} (V)	60
$R_{DS(on)}$ (Ω) at $V_{GS} = 1.5$ V	0.245
I_D (A)	2
Configuration	Single
Package	SOT-23

ORDERING INFORMATION

Package	SOT-23
Lead (Pb)-free and halogen-free	SQ2364EES-T1 (for detailed order number please see www.vishay.com/doc?79771)

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-source voltage	V_{DS}	60	V
Gate-source voltage	V_{GS}	± 8	
Continuous drain current	I_D	$T_C = 25$ °C	2
		$T_C = 125$ °C	1.3
Continuous source current (diode conduction)	I_S	2	A
Pulsed drain current ^a	I_{DM}	8	
Single pulse avalanche current	I_{AS}	5	
Single pulse avalanche energy	E_{AS}	1.25	mJ
Maximum power dissipation ^a	P_D	$T_C = 25$ °C	3
		$T_C = 125$ °C	1
Operating junction and storage temperature range	T_J, T_{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-ambient	R_{thJA}	166	°C/W
Junction-to-foot (drain)	R_{thJF}	50	

Notes

- a. Pulse test; pulse width ≤ 300 μ s, duty cycle ≤ 2 %
b. When mounted on 1" square PCB (FR4 material)

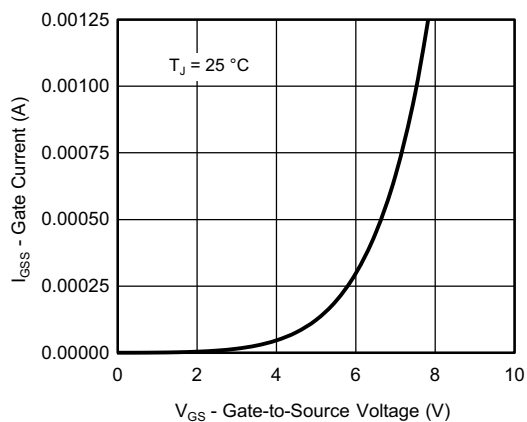
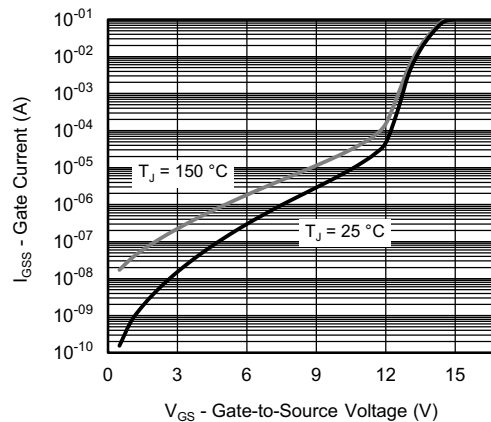
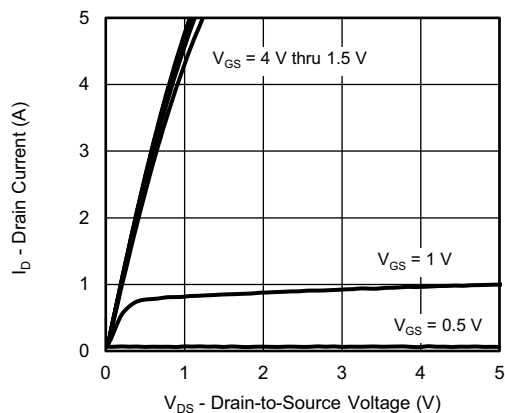
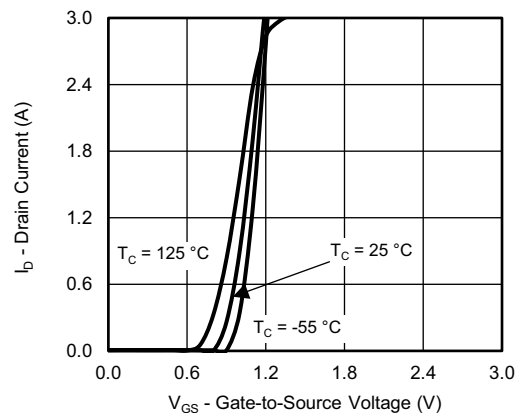
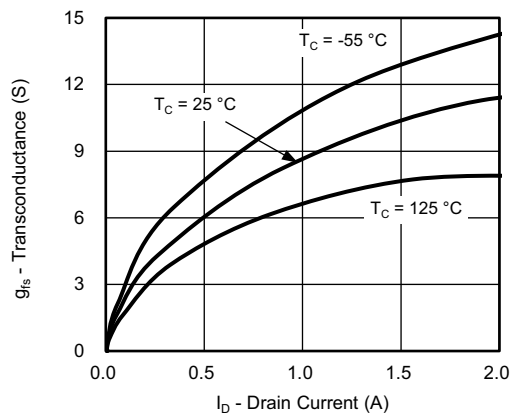
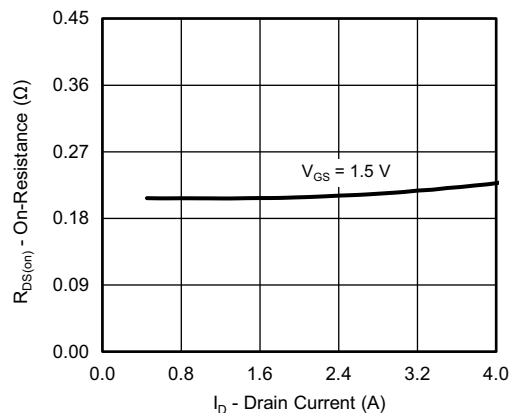


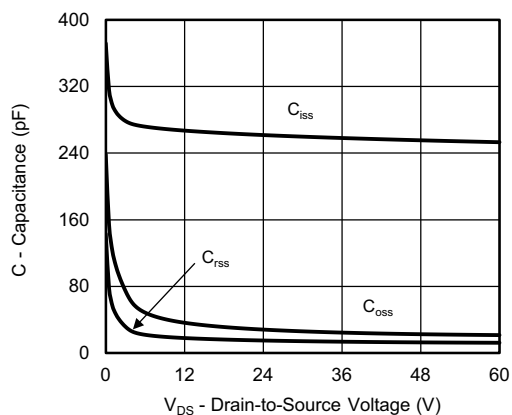
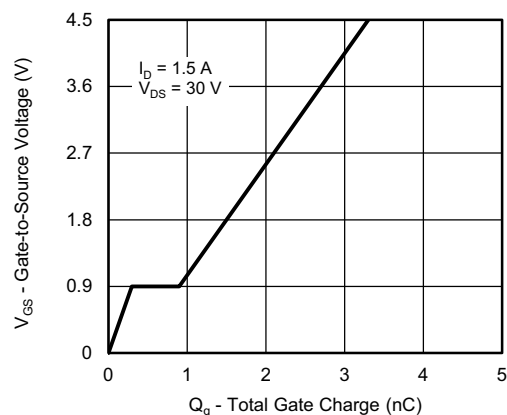
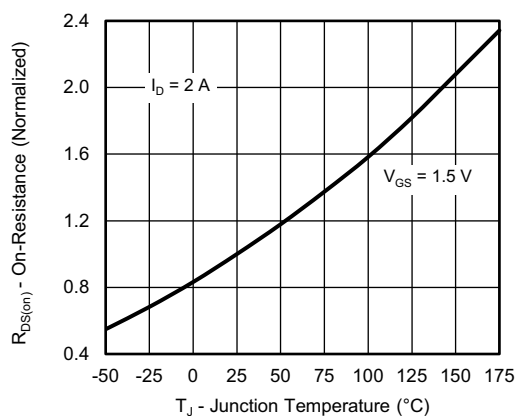
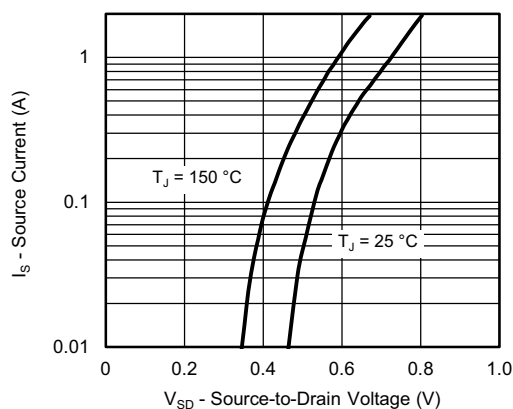
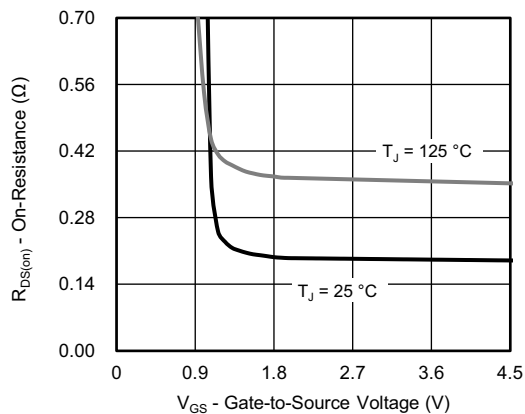
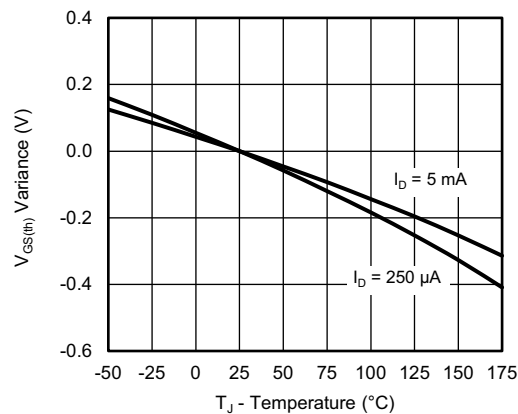
SPECIFICATIONS (T _C = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0, I _D = 250 μA		60	-	-	V
Gate-source threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA		0.46	0.6	1	
Gate-source leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 3 V		-	-	± 100	nA
		V _{DS} = 0 V, V _{GS} = ± 8 V		-	-	± 5.5	
Zero gate voltage drain current	I _{DSS}	V _{GS} = 0 V	V _{DS} = 60 V	-	-	1	μA
		V _{GS} = 0 V	V _{DS} = 60 V, T _J = 125 °C	-	-	50	
		V _{GS} = 0 V	V _{DS} = 60 V, T _J = 175 °C	-	-	150	
On-state drain current ^a	I _{D(on)}	V _{GS} = 4.5 V	V _{DS} ≥ 5 V	2	-	-	A
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V	I _D = 2 A, T _J = 25 °C	-	0.190	0.240	Ω
		V _{GS} = 4.5 V	I _D = 2 A, T _J = 125 °C	-	-	0.460	
		V _{GS} = 4.5 V	I _D = 2 A, T _J = 175 °C	-	-	0.600	
		V _{GS} = 1.5 V	I _D = 2 A	-	0.195	0.245	
Forward transconductance ^b	g _{fs}	V _{DS} = -15 V, I _D = 1 A		-	8.8	-	S
Dynamic ^b							
Input capacitance	C _{iss}	V _{GS} = 0 V	V _{DS} = 25 V, f = 1 MHz	-	263	330	pF
Output capacitance	C _{oss}			-	28	35	
Reverse transfer capacitance	C _{rss}			-	15	19	
Total gate charge ^c	Q _g	V _{GS} = 4.5 V	V _{DS} = 30 V, I _D = 1.5 A	-	2	2.5	nC
Gate-source charge ^c	Q _{gs}			-	0.3	-	
Gate-drain charge ^c	Q _{gd}			-	0.6	-	
Gate resistance	R _g	f = 1 MHz		2.5	4.1	6.6	Ω
Turn-on delay time ^c	t _{d(on)}	V _{DD} = 30 V, R _L = 15 Ω I _D ≅ 1.5 A, V _{GEN} = 10 V, R _g = 1 Ω		-	6	7.2	ns
Rise time ^c	t _r			-	11	14	
Turn-off delay time ^c	t _{d(off)}			-	26	32	
Fall time ^c	t _f			-	13	16	
Source-Drain Diode Ratings and Characteristics ^b							
Pulsed current ^a	I _{SM}			-	-	8	A
Forward voltage	V _{SD}	I _F = 2 A, V _{GS} = 0		-	0.8	1.2	V

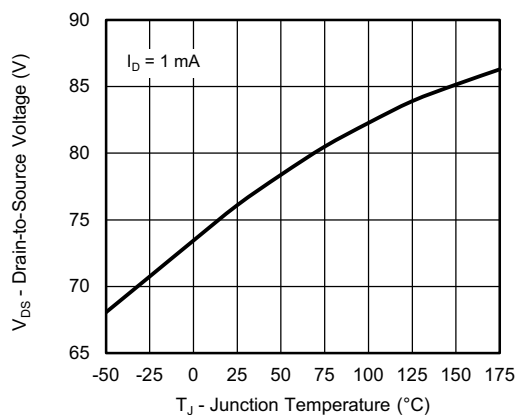
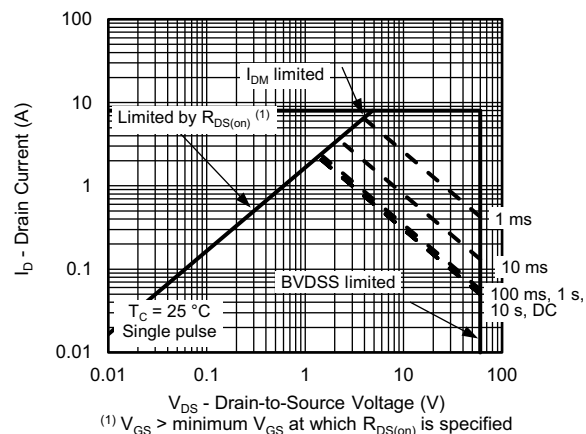
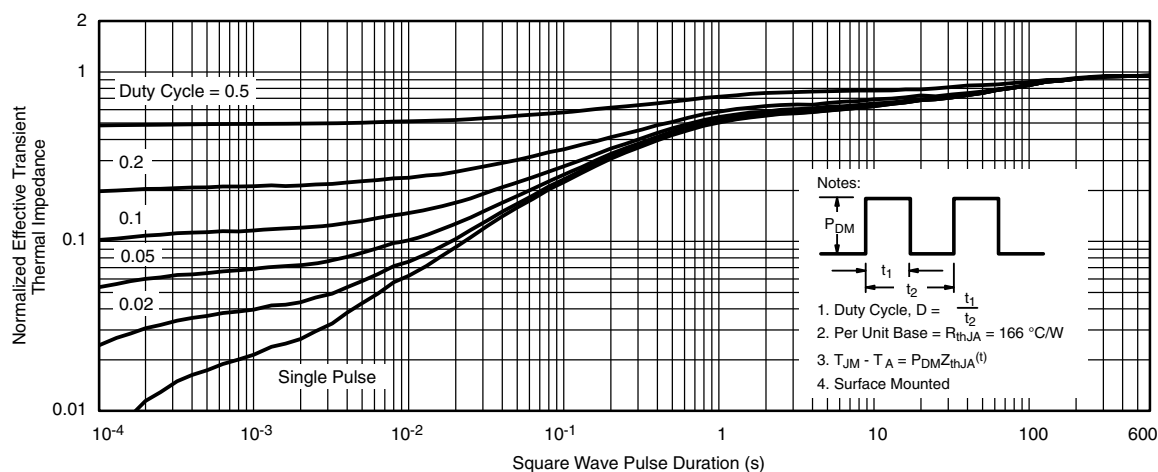
Notes

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$
b. Guaranteed by design, not subject to production testing
c. Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

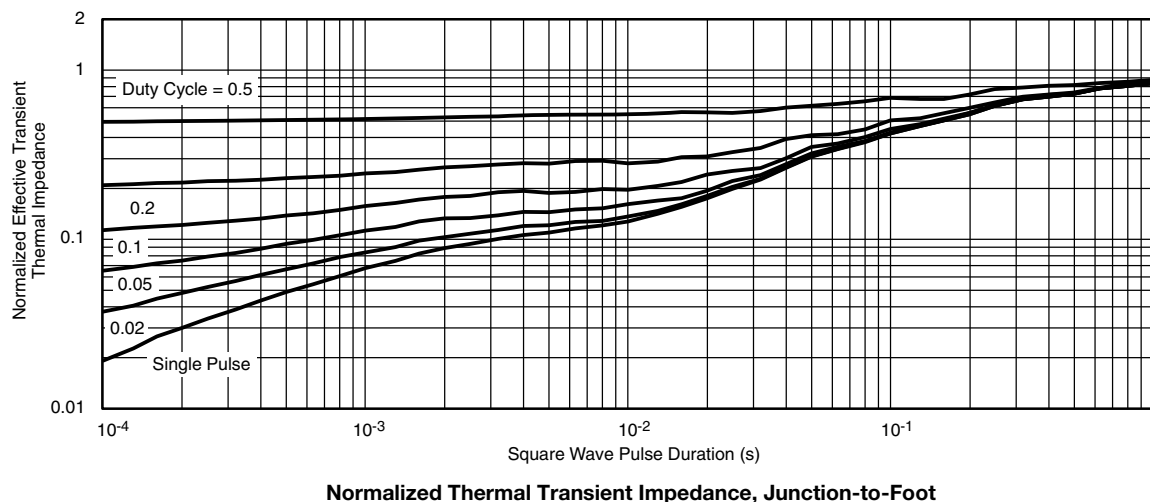
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Gate Current vs. Gate-Source Voltage

Gate Current vs. Gate-Source Voltage

Output Characteristics

Transfer Characteristics

Transconductance

On-Resistance vs. Drain Current

TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Capacitance

Gate Charge

On-Resistance vs. Junction Temperature

Source-Drain Diode Forward Voltage

On-Resistance vs. Gate-Source Voltage

Threshold Voltage

TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Drain-Source Breakdown vs. Junction Temperature

Safe Operating Area

Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient ($25\text{ }^{\circ}\text{C}$)
 - Normalized Transient Thermal Impedance Junction-to-Foot ($25\text{ }^{\circ}\text{C}$)are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

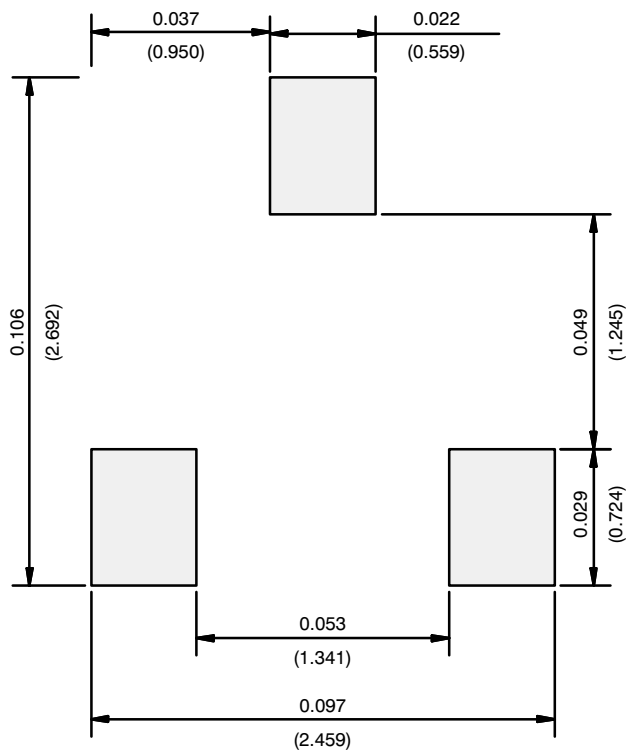
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?75975.

SOT-23 (TO-236): 3-LEAD



Dim	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	0.89	1.12	0.035	0.044
A ₁	0.01	0.10	0.0004	0.004
A ₂	0.88	1.02	0.0346	0.040
b	0.35	0.50	0.014	0.020
c	0.085	0.18	0.003	0.007
D	2.80	3.04	0.110	0.120
E	2.10	2.64	0.083	0.104
E ₁	1.20	1.40	0.047	0.055
e	0.95 BSC		0.0374 Ref	
e ₁	1.90 BSC		0.0748 Ref	
L	0.40	0.60	0.016	0.024
L ₁	0.64 Ref		0.025 Ref	
S	0.50 Ref		0.020 Ref	
q	3°	8°	3°	8°
ECN: S-03946-Rev. K, 09-Jul-01 DWG: 5479				

RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads
Dimensions in Inches/(mm)

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