

ANTREX Electronics

Electronic Components Sourcing Information

Product Reference Information

Part Number:	SI1480BDH-T1-GE3
Manufacturer:	Vishay Siliconix
Package:	Please confirm with sales
Availability:	Please confirm with sales

Product Page:

<https://antrexco.com/product/details/vishay-siliconix/si1480bdh-t1-ge3.html>

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Contact Information

Email: info@antrexco.com

WhatsApp: +86-186-8066-5326

Website: <https://antrexco.com>

Quality & Trust

New & Original Components

Supplier verification and packaging condition review

CoC / RoHS / REACH documents available on request

Secure sourcing support for OEM / EMS projects

Note:

This PDF includes an ANTREX product reference cover page.

The original manufacturer datasheet follows from the next page.

N-Channel 100 V (D-S) MOSFET


Marking Code: AVXX

PRODUCT SUMMARY	
V_{DS} (V)	100
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 10$ V	0.212
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 4.5$ V	0.270
Q_g typ. (nC)	1.86
I_D (A) ^a	2.38
Configuration	Single

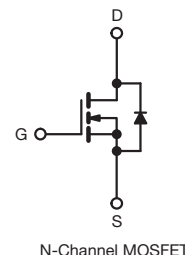
FEATURES

- TrenchFET® Gen IV power MOSFET
- 100 % R_g and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

- Load switches
- DC/DC converters
- Power management
- LED backlighting



N-Channel MOSFET

ORDERING INFORMATION	
Package	SOT-363
Lead (Pb)-free and halogen-free	Si1480BDH-T1-GE3

ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C, unless otherwise noted)				
PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-source voltage	V_{DS}	100	V	
Gate-source voltage	V_{GS}	± 20		
Continuous drain current ($T_J = 150$ °C) ^a	$T_C = 25$ °C	2.38 ^a	A	
	$T_C = 70$ °C	1.9		
	$T_A = 25$ °C	1.8 ^{b, c}		
	$T_A = 70$ °C	1.4 ^{b, c}		
Pulsed drain current ($t = 300$ μ s)	I_{DM}	7		
Avalanche current	I_{AS}	3		
Repetitive avalanche energy	E_{AS}	0.45	mJ	
Continuous source-drain diode current	$T_C = 25$ °C	2.3	A	
	$T_A = 25$ °C	1.3 ^{b, c}		
Maximum power dissipation ^a	$T_C = 25$ °C	2.6	W	
	$T_C = 70$ °C	1.7		
	$T_A = 25$ °C	1.5 ^{b, c}		
	$T_A = 70$ °C	0.97 ^{b, c}		
Operating junction and storage temperature range	T_J, T_{stg}	-55 to +150	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient ^{b, d}	$t \leq 5$ s	R_{thJA}	62	82	°C/W
Maximum junction-to-foot (drain)	Steady state	R_{thJF}	37	47	

Notes

- Package limited
- Surface mounted on 1" x 1" FR4 board
- $t = 5$ s
- Maximum under steady state conditions is 130 °C/W

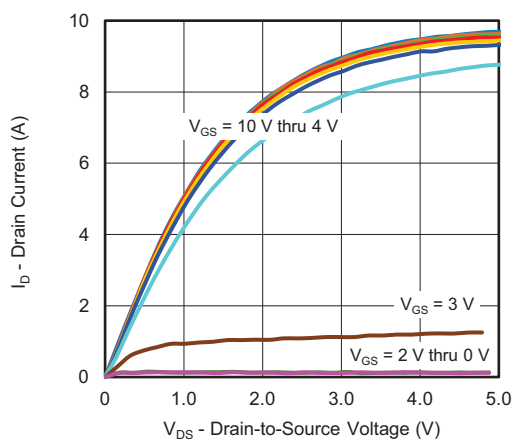
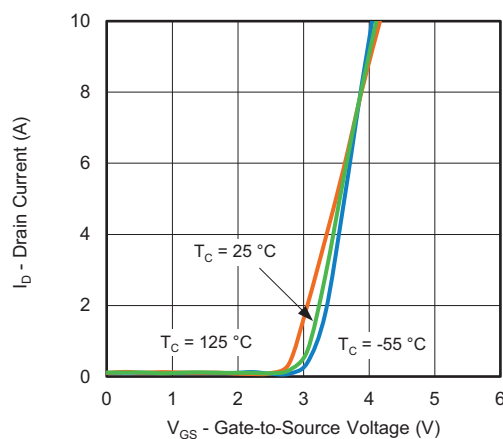
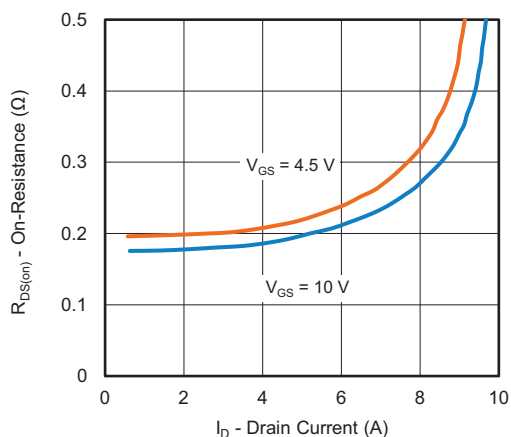
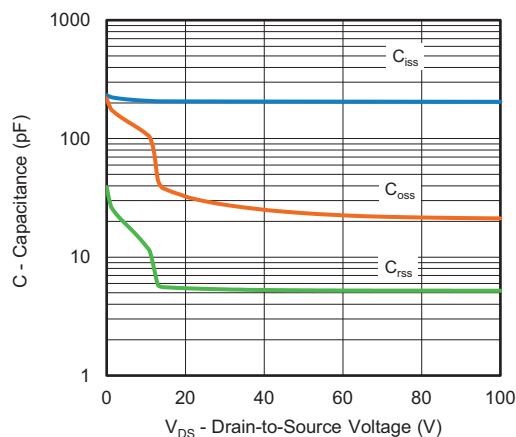
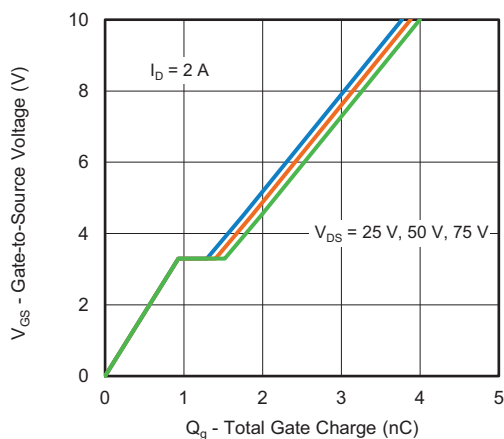
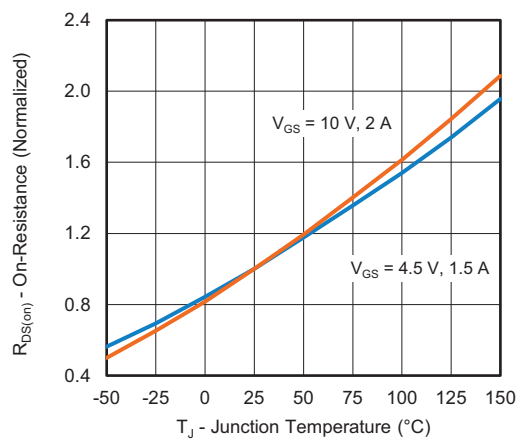


SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	100	-	-	V
V _{DS} temperature coefficient	ΔV _{DS} /T _J	I _D = 250 μA	-	87	-	mV/°C
V _{GS(th)} temperature coefficient	ΔV _{GS(th)} /T _J		-	-4.3	-	
Gate-source threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.6	-	3	V
Gate-source leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V	-	-	± 100	nA
Zero gate voltage drain current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V	-	-	1	μA
		V _{DS} = 100 V, V _{GS} = 0 V, T _J = 55 °C	-	-	10	
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 2 A	-	0.176	0.212	Ω
		V _{GS} = 4.5 V, I _D = 1.5 A	-	0.196	0.270	
Forward transconductance	g _{fs}	V _{DS} = 10 V, I _D = 2 A	-	9	-	S
Dynamic ^b						
Input capacitance	C _{iss}	V _{DS} = 50 V, V _{GS} = 0 V, f = 1 MHz	-	206	-	pF
Output capacitance	C _{oss}		-	24	-	
Reverse transfer capacitance	C _{rss}		-	5	-	
Total gate charge	Q _g	V _{DS} = 50 V, V _{GS} = 10 V, I _D = 2 A	-	3.9	6.0	nC
		V _{DS} = 50 V, V _{GS} = 4.5 V, I _D = 2 A	-	1.86	3.0	
Gate-source charge	Q _{gs}		-	0.93	-	
Gate-drain charge	Q _{gd}		-	0.5	-	
Gate resistance	R _g	f = 1 MHz	0.5	2.0	3.5	Ω
Turn-on delay time	t _{d(on)}	V _{DD} = 50 V, R _L = 25 Ω I _D ≅ 2 A, V _{GEN} = 4.5 V, R _g = 1 Ω	-	11	22	ns
Rise time	t _r		-	25	50	
Turn-off delay time	t _{d(off)}		-	10	20	
Fall time	t _f		-	12	24	
Turn-on delay time	t _{d(on)}	V _{DD} = 50 V, R _L = 25 Ω I _D ≅ 2 A, V _{GEN} = 10 V, R _g = 1 Ω	-	6	12	ns
Rise time	t _r		-	4	8	
Turn-off delay time	t _{d(off)}		-	10	20	
Fall time	t _f		-	3	6	
Drain-Source Body Diode Characteristics						
Continous source-drain diode current	I _S	T _C = 25 °C	-	-	2.3	A
Pulse diode forward current ^a	I _{SM}		-	-	7	
Body diode voltage	V _{SD}	I _S = 2 A, V _{GS} = 0 V	-	0.85	1.2	V
Body diode reverse recovery charge	Q _{rr}	I _F = 2 A, dI/dt = 100 A/μs, T _J = 25 °C	-	22	44	nC
Body diode reverse recovery time	t _{rr}		-	20	40	ns
Reverse recovery fall time	t _a		-	18	-	
Reverse recovery rise time	t _b		-	3	-	

Notes

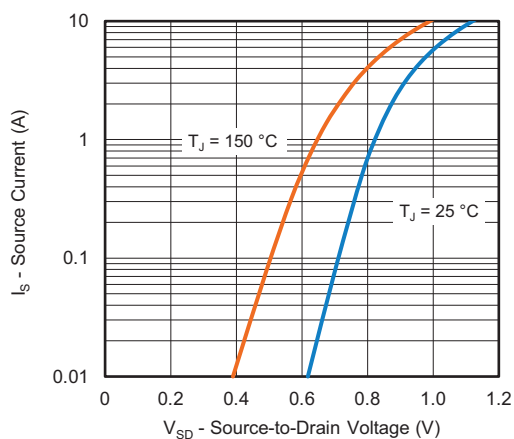
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$
b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

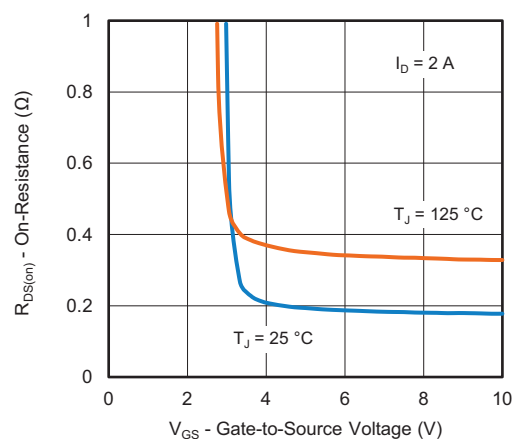
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Output Characteristics

Transfer Characteristics Curves vs. Temperature

On-Resistance vs. Drain Current

Capacitance

Gate Charge

On-Resistance vs. Junction Temperature



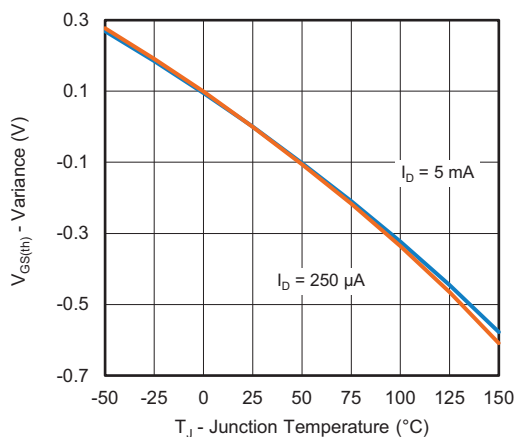
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



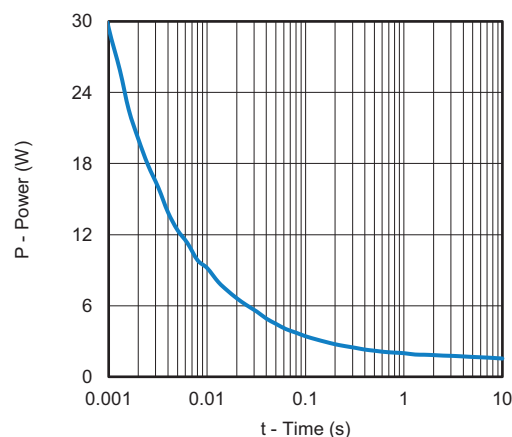
Source-Drain Diode Forward Voltage



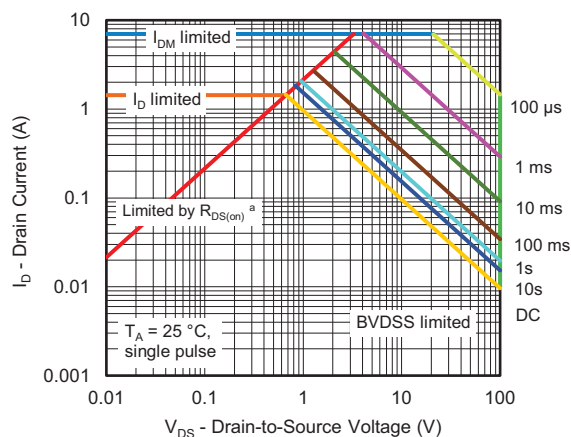
$R_{DS(on)}$ vs. V_{GS} vs. Temperature



Threshold Voltage



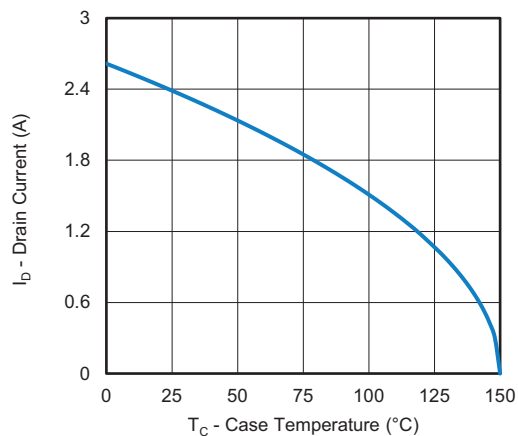
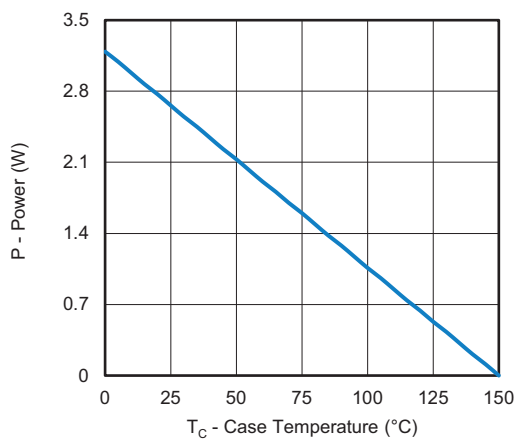
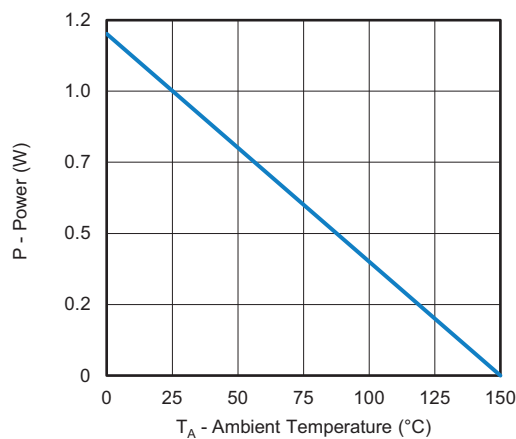
Single Pulse Power



Safe Operating Area, Junction-to-Ambient

Note

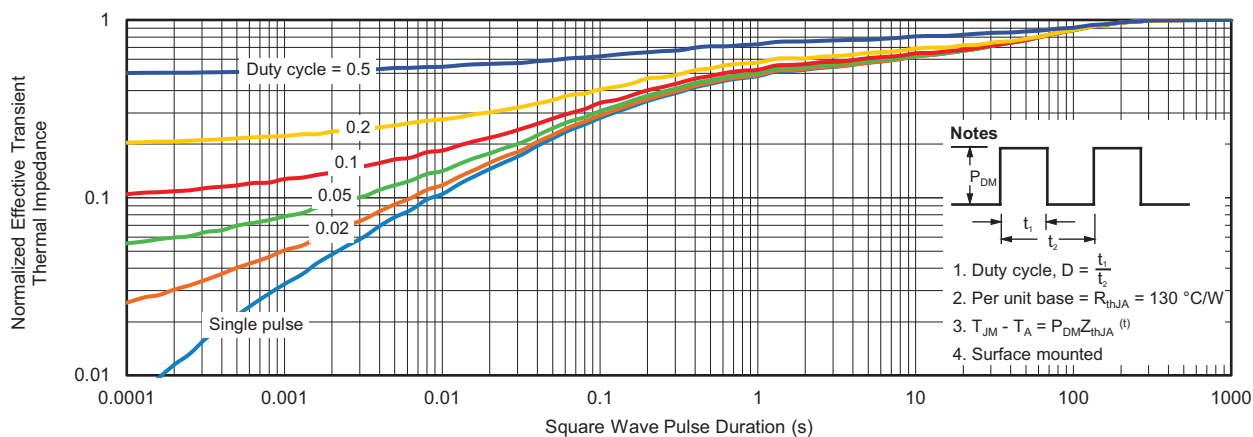
a. $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)

Current Derating ^a

Power, Junction-to-Case

Power, Junction-to-Ambient
Note

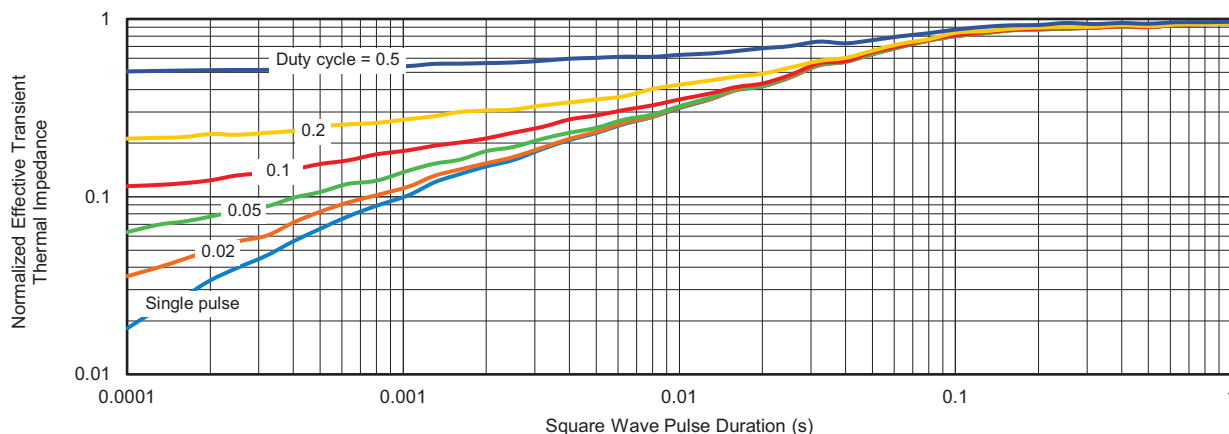
- a. The power dissipation P_D is based on T_J max. = 150°C , using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit



TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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