

ANTREX Electronics

Electronic Components Sourcing Information

Product Reference Information

Part Number: VS-ENV020M120M

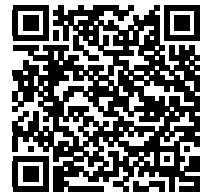
Manufacturer: Vishay General Semiconductor - Diodes Division

Package: -

Availability: Please confirm with sales

Product Page:

<https://antrexco.com/product/details/vishay-general-semiconductor-diodes-division-vs-env020m120m.html>



Scan for product page

Request a Quote:

[Submit RFQ for this part](#)

Contact Information

Email: info@antrexco.com

WhatsApp: +86-186-8066-5326

Website: <https://antrexco.com>

Quality & Trust

New & Original Components

Supplier verification and packaging condition review

CoC / RoHS / REACH documents available on request

Secure sourcing support for OEM / EMS projects

Note:

This PDF includes an ANTREX product reference cover page.

The original manufacturer datasheet follows from the next page.

EMIPAK 1B PressFit Power Module 1200 V AC Line Input Rectification, Flexible Configuration, 20 A



EMIPAK 1B
(package example)

FEATURES

- MOAT standard recovery diode
- Exposed Al_2O_3 substrate with low thermal resistance
- Very low forward voltage drop
- Low internal inductances
- Qualified using AQG324 guideline as reference
- PressFit pins locking technology
PATENT(S): www.vishay.com/patents
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT

PRIMARY CHARACTERISTICS

D1 - D12	
V_{RRM}	1200 V
V_{FM} typical at 20 A	1.29 V
I_O at $T_{SINK} = 97^\circ\text{C}$	20 A
Package	EMIPAK 1B
Circuit configuration	6 x independent diodes legs for AC line input rectification
Type	Modules - diode, high voltage

DESCRIPTION

The EMIPAK 1B package is easy to use thanks to the PressFit pins. The exposed substrate provides improved thermal performance.

The optimized layout also helps to minimize stray parameters, allowing for better EMI performance.

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Operating junction temperature	T_J		150	$^\circ\text{C}$
Storage temperature range	T_{Stg}		-40 to +150	
RMS isolation voltage	V_{ISOL}	$T_J = 25^\circ\text{C}$, all terminals shorted, $f = 50\text{ Hz}$, $t = 1\text{ s}$	3500	V
D1 - D12				
Maximum DC output current	$I_{F(AV)}$	$T_{SINK} = 25^\circ\text{C}$	33	A
		$T_{SINK} = 80^\circ\text{C}$	23	
Power dissipation	P_D	$T_{SINK} = 25^\circ\text{C}$	87	W
		$T_{SINK} = 80^\circ\text{C}$	49	
Maximum peak one cycle forward non-repetitive surge current	I_{FSM}	10 ms sine or 6 ms rectangular pulse, $T_J = 150^\circ\text{C}$, no voltage reapplied	230	A
		8.3 ms sine, $T_J = 150^\circ\text{C}$, no voltage reapplied	241	A
Maximum I^2t capability for fusing	I^2t	No voltage reapplied, $t = 10\text{ ms}$	264	A^2s
		No voltage reapplied, $t = 8.3\text{ ms}$	241	
Maximum $I^2\sqrt{t}$ capability for fusing	$I^2\sqrt{t}$	$t = 0.1\text{ ms}$ to 10 ms , no voltage reapplied	2647	$\text{A}^2\sqrt{\text{s}}$
Repetitive peak reverse voltage	V_{RRM}		1200	V
Low level value of threshold voltage	$V_{F(TO)1}$	$(16.7\% \times I_{F(AV)}) < I < I_{F(AV)}$, $T_J = T_J$ maximum	0.92	V
High level value of threshold voltage	$V_{F(TO)2}$	$I > I_{F(AV)}$, $T_J = T_J$ maximum	1.10	
Low level value of forward slope resistance	r_{f1}	$(16.7\% \times I_{F(AV)}) < I < I_{F(AV)}$, $T_J = T_J$ maximum	51.3	$\text{m}\Omega$
High level value of forward slope resistance	r_{f2}	$I > I_{F(AV)}$, $T_J = T_J$ maximum	50.8	

PATENT(S): www.vishay.com/patents

This Vishay product is protected by one or more United States and international patents.

**ELECTRICAL SPECIFICATIONS** ($T_J = 25\text{ }^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
D1 - D12						
Forward voltage drop	V_{FM}	$I_F = 20\text{ A}$	-	1.29	1.90	V
		$I_F = 20\text{ A}, T_J = 150\text{ }^{\circ}\text{C}$	-	1.26	-	
Breakdown voltage	V_{BR}	$I_R = 500\text{ }\mu\text{A}$	1200	-	-	V
Reverse leakage current	I_{RM}	$V_R = 1200\text{ V}$	-	1.0	100	μA
		$V_R = 1200\text{ V}, T_J = 150\text{ }^{\circ}\text{C}$	-	900	-	

INTERNAL NTC - THERMISTOR SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUE	UNITS
Resistance	R_{25}	$T_C = 25\text{ }^{\circ}\text{C}$	5000	Ω
	R_{100}	$T_C = 100\text{ }^{\circ}\text{C}$	$493 \pm 5\%$	
B-value	$B_{25/50}$	$R_2 = R_{25} \exp. [B_{25/50}(1/T_2 - 1/(298.15\text{K}))]$	$3375 \pm 5\%$	K
Maximum operating temperature			220	$^{\circ}\text{C}$
Dissipation constant			2	$\text{mW}/^{\circ}\text{C}$
Thermal time constant			8	s

THERMAL AND MECHANICAL SPECIFICATIONS

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNITS
D1 - D12 - thermal resistance junction to sink (per diode) ⁽¹⁾	R_{thJS}	-	1.19	-	$^{\circ}\text{C}/\text{W}$
Case to sink thermal resistance (per module) ⁽¹⁾		-	0.1	-	
Mounting torque (M4)		2	-	3	Nm
Weight		-	28	-	g

Note

⁽¹⁾ Mounting surface flat, smooth, and greased, $\lambda_{\text{grease}} = 0.67\text{ W/mK}$

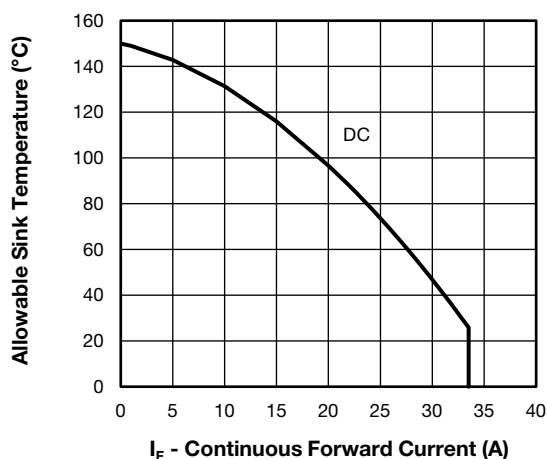


Fig. 1 - Allowable Sink Temperature vs. Continuous Forward Current (Forward Current vs. Sink Temperature)

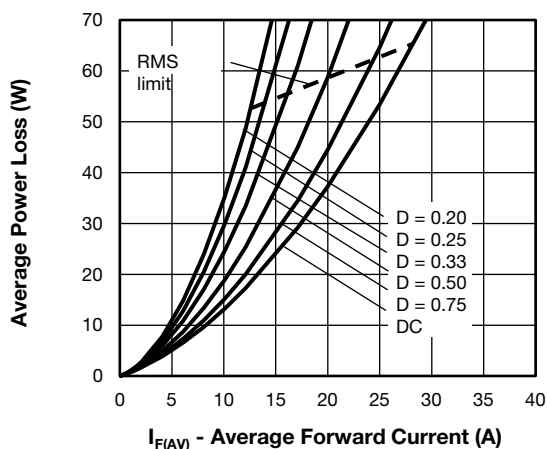


Fig. 2 - Average Power Loss vs. Average Forward Current (Forward Power Loss Characteristics)

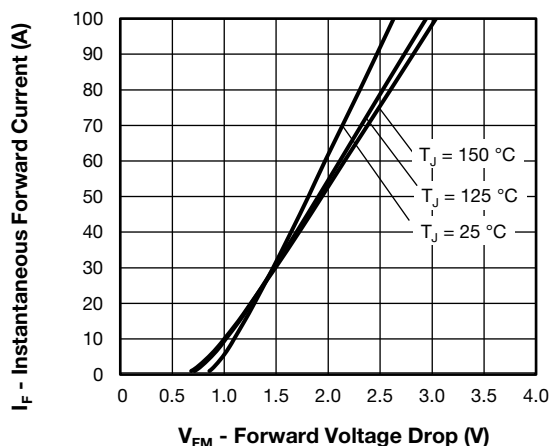


Fig. 3 - Typical Forward Voltage Drop vs. Instantaneous Forward Current (Per Diode)

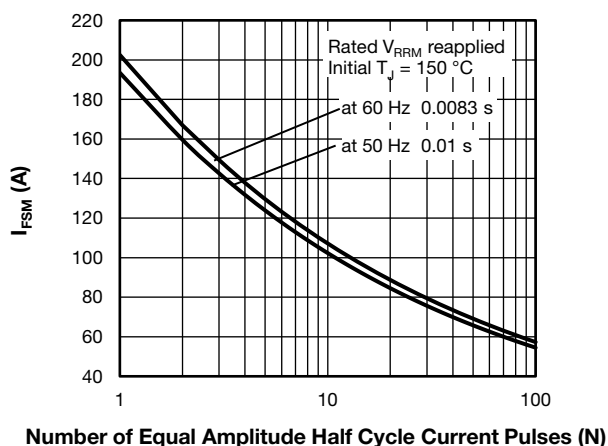


Fig. 5 - I_{FSM} vs. N
(Non-Repetitive Peak Forward Surge Current vs. Number Pulses)

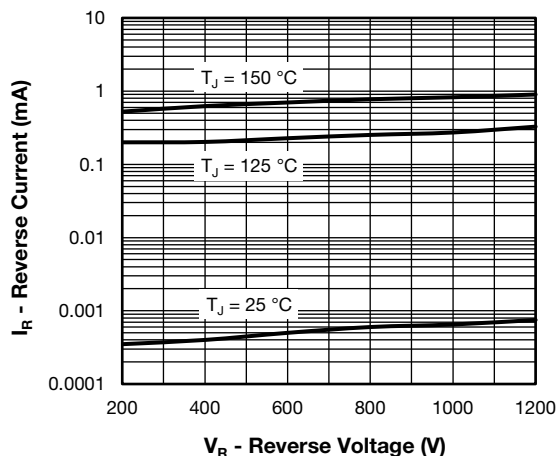


Fig. 4 - Typical Reverse Current vs. Reverse Voltage (Per Diode)

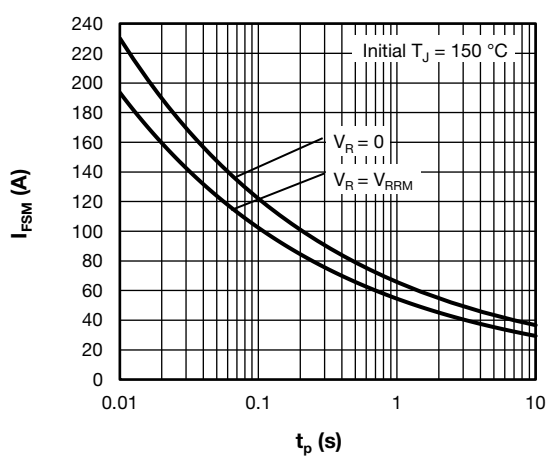


Fig. 6 - I_{FSM} vs. t_p
(Non-Repetitive Peak Forward Surge Current vs. Pulse Duration)

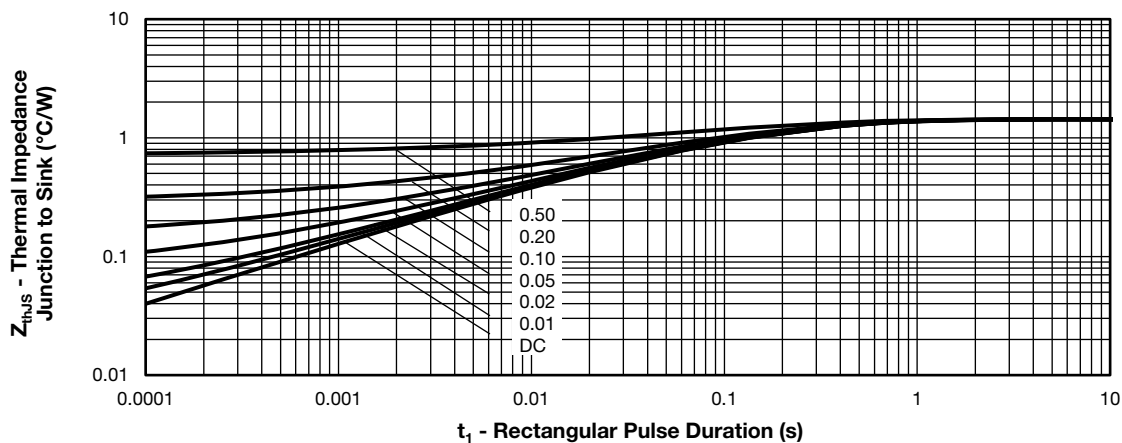
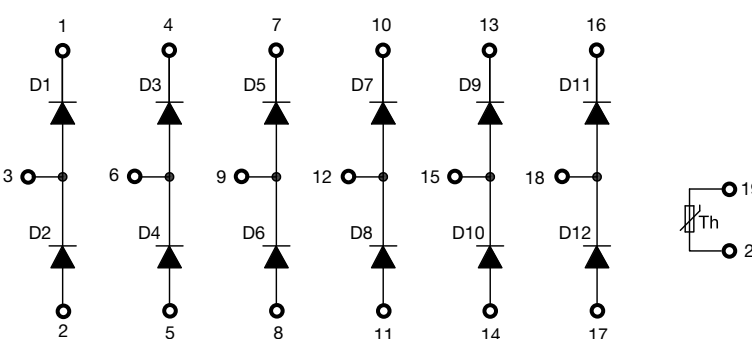


Fig. 7 - Z_{thJS} Thermal Impedance Junction to Sink vs. t_1 Rectangular Pulse Duration
(Maximum Thermal Impedance Z_{thJS} Characteristics Per Diode)

ORDERING INFORMATION TABLE

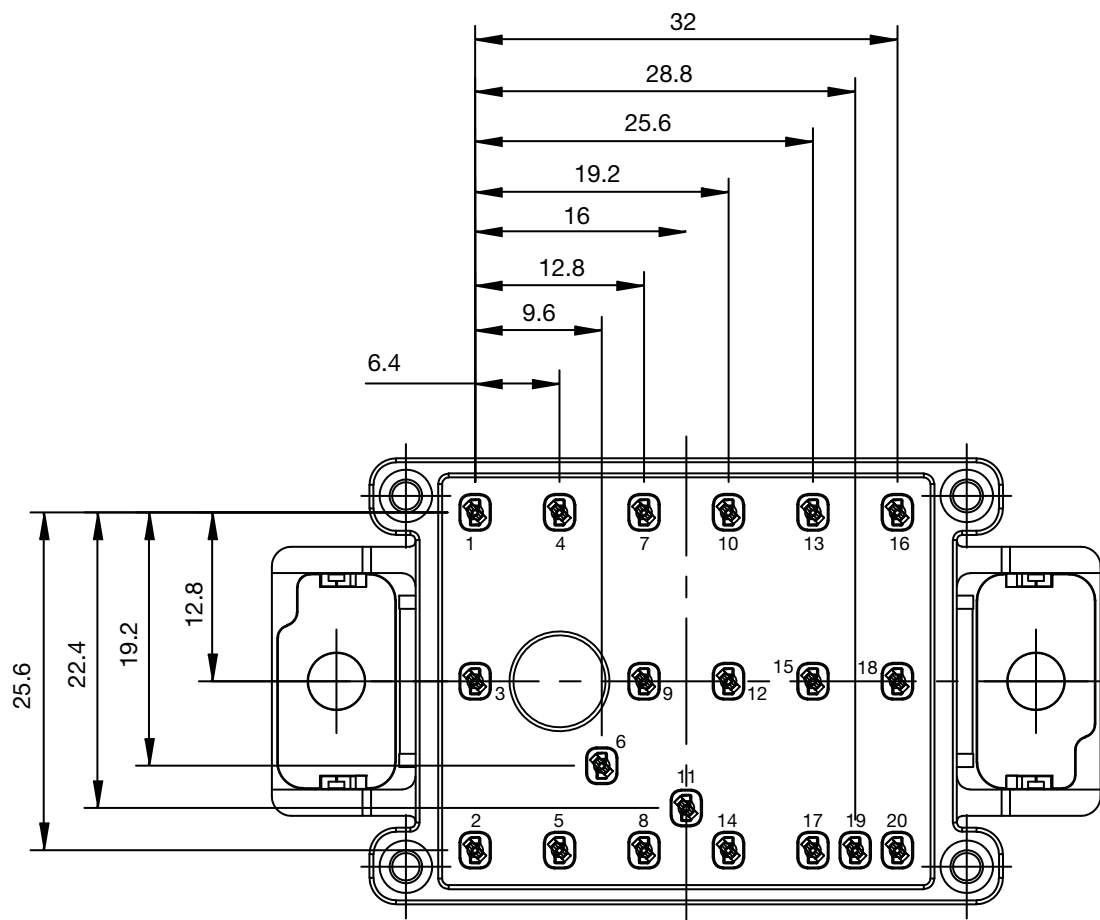
Device code	VS-	EN	V	020	M	120	M
	1	2	3	4	5	6	7

- 1** - Vishay Semiconductors product
- 2** - Package indicator (EN = EMIPAK 1B)
- 3** - Circuit configuration (V = 6 x independent diodes legs for AC line input rectification)
- 4** - Current rating (020 = 20 A)
- 5** - Switch die technology (M = MOAT standard recovery diode)
- 6** - Voltage rating (120 = 1200 V)
- 7** - Diode technology (M = MOAT standard recovery diode)

CIRCUIT CONFIGURATION		
CIRCUIT DESCRIPTION	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING
6 x independent diodes legs for AC line input rectification	V	



PACKAGE

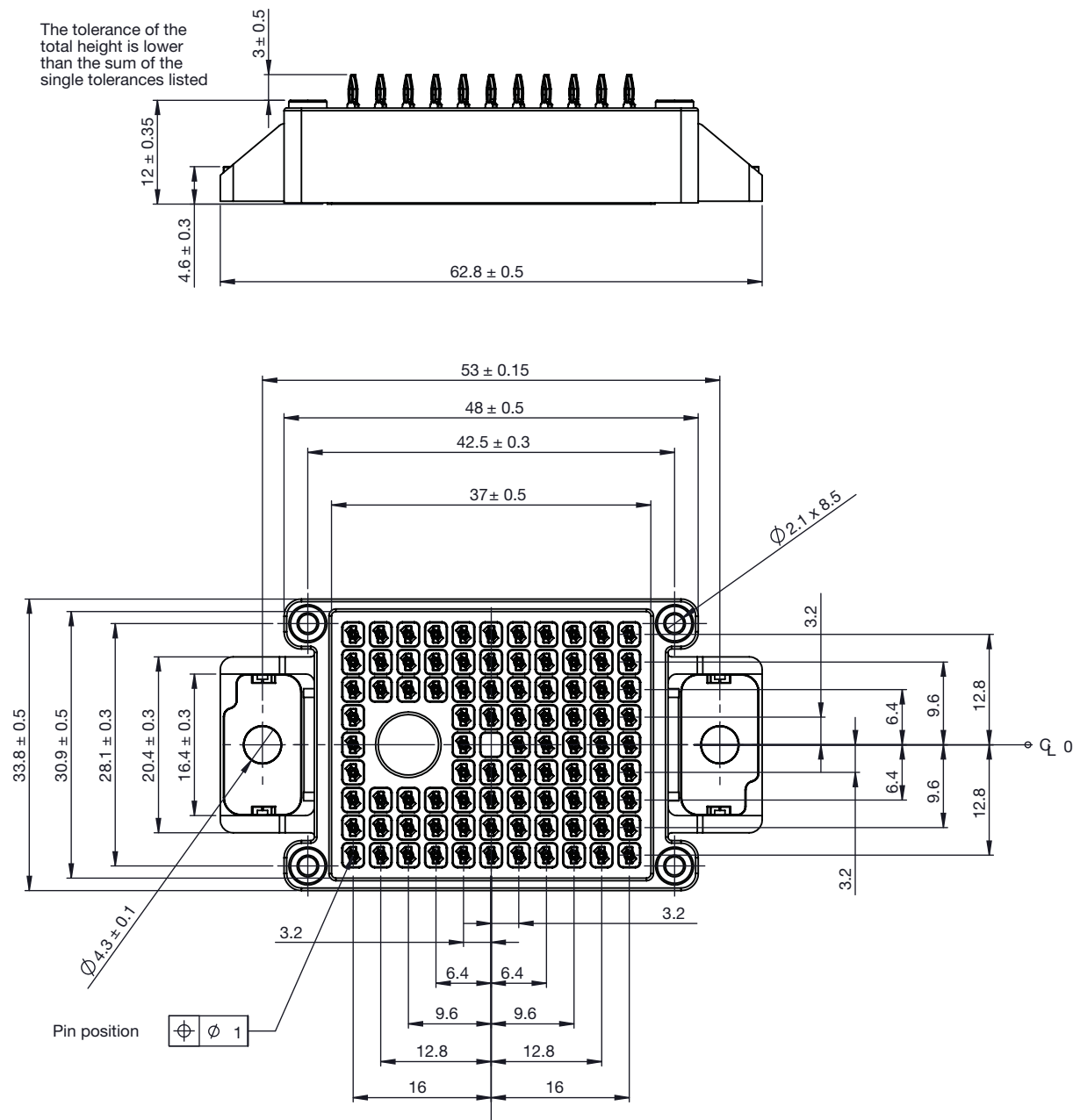


LINKS TO RELATED DOCUMENTS

Dimensions	www.vishay.com/doc?95558
Application Note	www.vishay.com/doc?95580

EMIPAK 1B

DIMENSIONS in millimeters





Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.