

ANTREX Electronics

Electronic Components Sourcing Information

Product Reference Information

Part Number: 8ETH03-1

Manufacturer: Vishay General Semiconductor - Diodes Division

Package: TO-262-3 Long Leads, I2PAK, TO-262AA

Availability: Please confirm with sales

Product Page:

<https://antrexco.com/product/details/vishay-general-semiconductor-diodes-division/8eth03-1-139414.html>



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New & Original Components

Supplier verification and packaging condition review

CoC / RoHS / REACH documents available on request

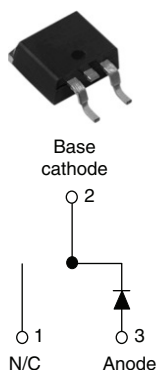
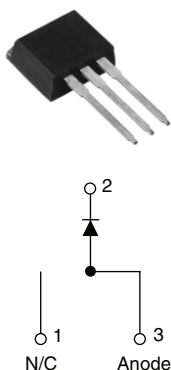
Secure sourcing support for OEM / EMS projects

Note:

This PDF includes an ANTREX product reference cover page.

The original manufacturer datasheet follows from the next page.

Hyperfast Rectifier, 8 A FRED P_t[™]

8ETH03S

D²PAK
8ETH03-1

TO-262

FEATURES

- Hyperfast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Designed and qualified for industrial level

DESCRIPTION/APPLICATIONS

300 V series are the state of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

PRODUCT SUMMARY

t_{rr}	35 ns
$I_{F(AV)}$	8 A
V_R	300 V

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Repetitive peak reverse voltage	V_{RRM}		300	V
Average rectified forward current	$I_{F(AV)}$	$T_C = 155\text{ °C}$	8	A
Non-repetitive peak surge current	I_{FSM}	$T_C = 25\text{ °C}$	100	
Operating junction and storage temperatures	T_J, T_{Stg}		- 65 to 175	°C

ELECTRICAL SPECIFICATIONS ($T_J = 25\text{ °C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_R	$I_R = 100\text{ }\mu\text{A}$	300	-	-	V
Forward voltage	V_F	$I_F = 8\text{ A}$	-	1.0	1.25	
		$I_F = 8\text{ A}, T_J = 125\text{ °C}$	-	0.83	1.00	
Reverse leakage current	I_R	$V_R = V_R\text{ rated}$	-	0.02	20	μA
		$T_J = 125\text{ °C}, V_R = V_R\text{ rated}$	-	6.0	200	
Junction capacitance	C_T	$V_R = 300\text{ V}$	-	31	-	pF
Series inductance	L_S	Measured lead to lead 5 mm from package body	-	8	-	nH

DYNAMIC RECOVERY CHARACTERISTICS ($T_C = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t_{rr}	$I_F = 1\text{ A}$, $dI_F/dt = -50\text{ A}/\mu\text{s}$, $V_R = 30\text{ V}$	-	-	35	ns
		$T_J = 25\text{ }^{\circ}\text{C}$	-	27	-	
		$T_J = 125\text{ }^{\circ}\text{C}$	-	40	-	
Peak recovery current	I_{RRM}	$T_J = 25\text{ }^{\circ}\text{C}$	-	2.2	-	A
		$T_J = 125\text{ }^{\circ}\text{C}$	-	5.3	-	
Reverse recovery charge	Q_{rr}	$T_J = 25\text{ }^{\circ}\text{C}$	-	30	-	nC
		$T_J = 125\text{ }^{\circ}\text{C}$	-	106	-	

THERMAL - MECHANICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T_J , T_{Stg}		- 65	-	175	$^{\circ}\text{C}$
Thermal resistance, junction to case per leg	R_{thJC}		-	1.45	2.5	$^{\circ}\text{C}/\text{W}$
Thermal resistance, junction to ambient per leg	R_{thJA}	Typical socket mount	-	-	70	
Thermal resistance, case to heatsink	R_{thCS}	Mounting surface, flat, smooth and greased	-	0.2	-	
Weight			-	2.0	-	g
			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style D ² PAK	8ETH03S			
		Case style TO-262	8ETH03-1			

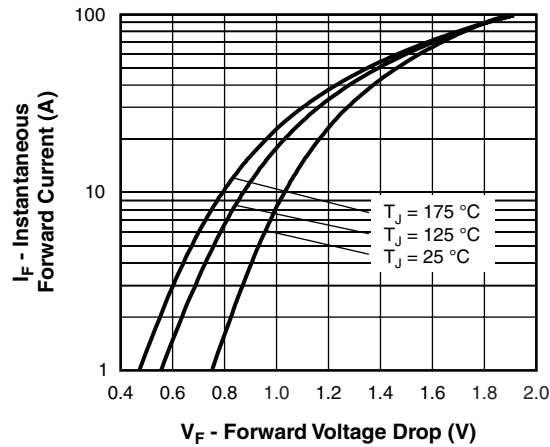


Fig. 1 - Typical Forward Voltage Drop Characteristics

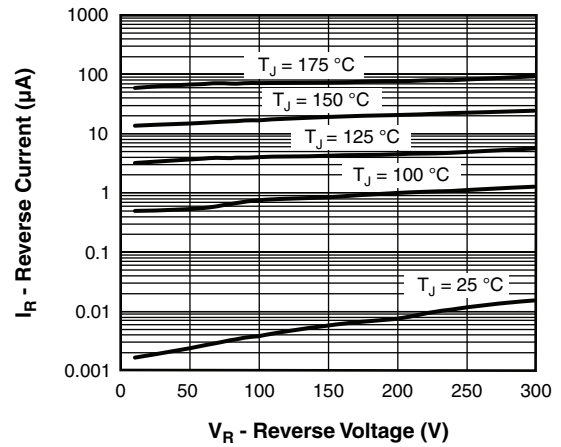


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

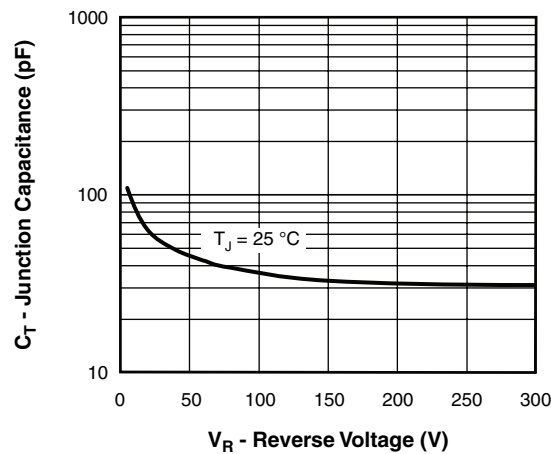
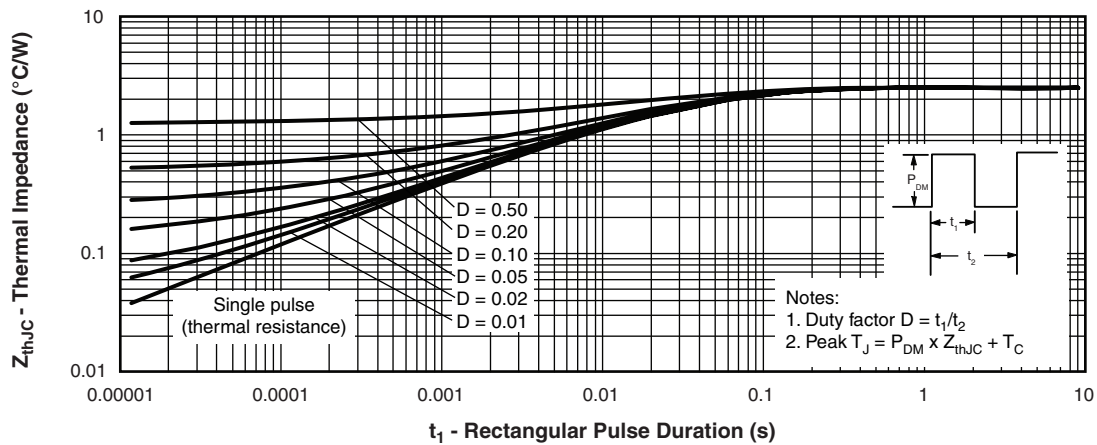


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage


 Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

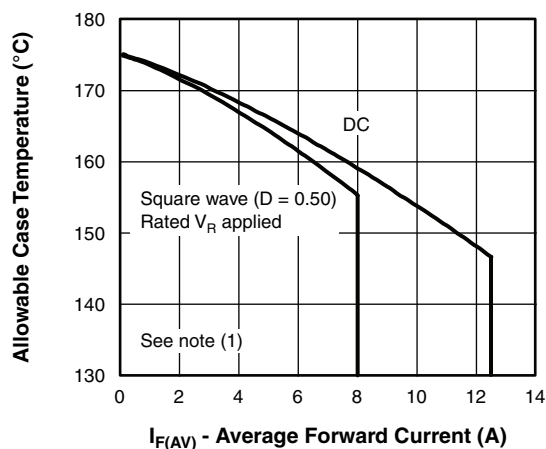


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

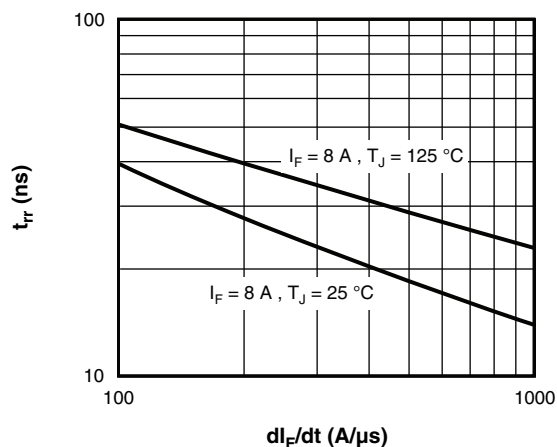


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

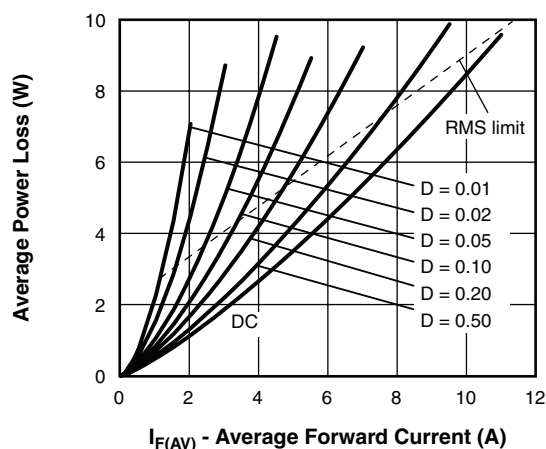


Fig. 6 - Forward Power Loss Characteristics

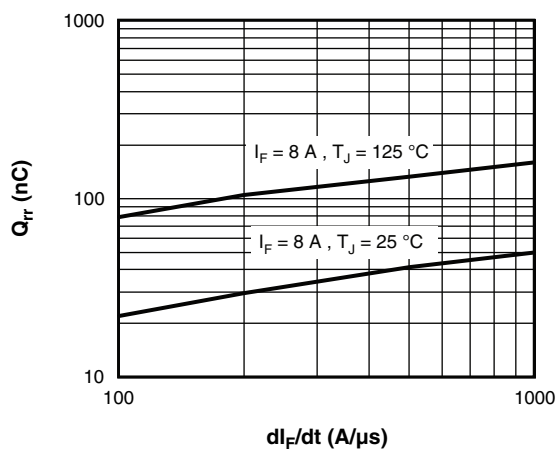


Fig. 8 - Typical Stored Charge vs. dI_F/dt

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;
 P_d = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 P_{dREV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = Rated V_R

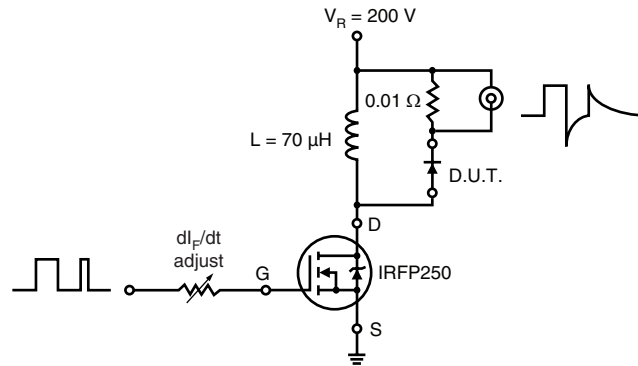


Fig. 9 - Reverse Recovery Parameter Test Circuit

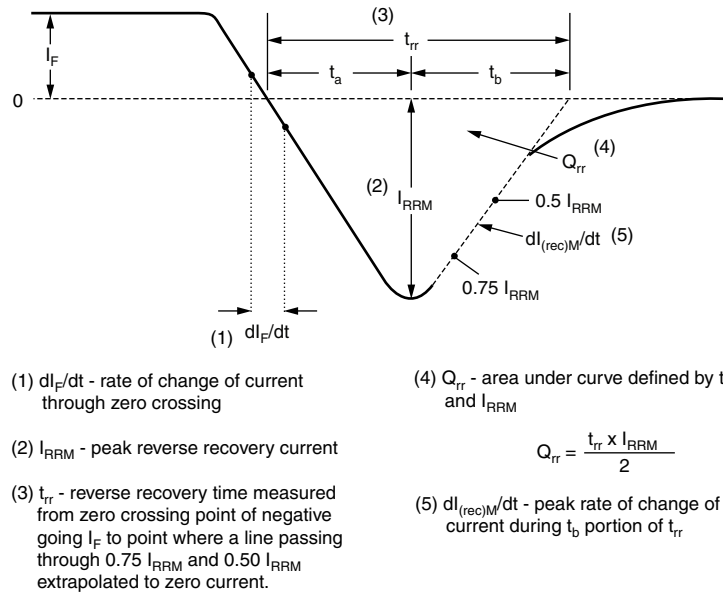


Fig. 10 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code	8	E	T	H	03	S	TRL	-
	1	2	3	4	5	6	7	8

- 1** - Current rating (8 A)
- 2** - E = Single diode
- 3** - T = TO-220, D²PAK
- 4** - H = Hyperfast rectifier
- 5** - Voltage rating (03 = 300 V)
- 6** -
 - S = D²PAK
 - -1 = TO-262
- 7** -
 - None = Tube (50 pieces)
 - TRL = Tape and reel (left oriented, for D²PAK package)
 - TRR = Tape and reel (right oriented, for D²PAK package)
- 8** -
 - None = Standard production
 - PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS	
Dimensions	http://www.vishay.com/doc?95014
Part marking information	http://www.vishay.com/doc?95008
Packaging information	http://www.vishay.com/doc?95032



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