

ANTREX Electronics

Electronic Components Sourcing Information

Product Reference Information

Part Number: NJM2521D#
Manufacturer: Nisshinbo Micro Devices Inc.
Package: 8-DIP (0.300" , 7.62mm)
Availability: Please confirm with sales

Product Page:

<https://antrexco.com/product/details/nisshinbo-micro-devices-inc/njm2521d.html>

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Supplier verification and packaging condition review

CoC / RoHS / REACH documents available on request

Secure sourcing support for OEM / EMS projects

Note:

This PDF includes an ANTREX product reference cover page.

The original manufacturer datasheet follows from the next page.

3-INPUT 1-OUTPUT AUDIO SWITCH

■ GENERAL DESCRIPTION

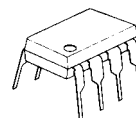
The NJM2521 is 58k Ω input impedance 3-input 1-output audio switch.

It contains two bias-type inputs and one buffer-type output.

■ FEATURES

- Operating Voltage (+4.75V~+13V)
- Crosstalk (-70dB typ.)
- Input Impedance (58k Ω typ.)
- 3-Input, 1-Output
- Bipolar Technology
- Package Outline DIP8,DMP8,SSOP8

■ PACKAGE OUTLINE



NJM2521D

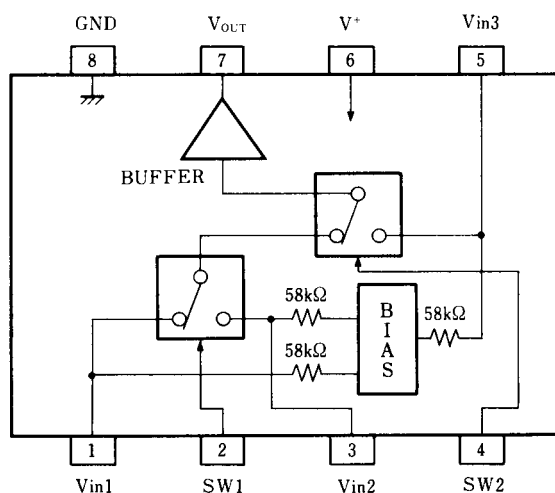


NJM2521M



NJM2521V

■ PIN CONFIGURATION



NJM2521D
NJM2521M
NJM2521V

PIN FUNCTION

1. Vin1
2. SW1
3. Vin2
4. SW2
5. Vin3
6. V⁺
7. Vout
8. GND

NJM2521

■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V^+	+15	V
Power Dissipation	P_D	(DIP8) 500 (DMP8) 300 (SSOP8) 250	mW
Operating Temperature Range	T_{opr}	-20~+75	°C
Storage Temperature Range	T_{stg}	-40~+125	°C

■ ELECTRICAL CHARACTERISTICS

($V^+=5V, T_a=25^\circ C$)

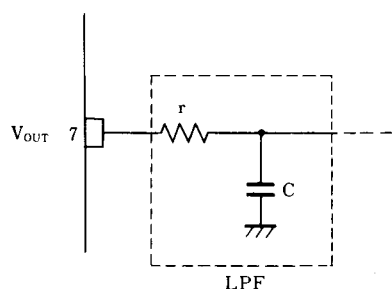
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Operating Voltage	V^+		+4.75	-	+13.0	V
Operating Current	I_{CC}		-	11.0	14.5	mA
Frequency Characteristics	G_f	$V_{IN}=2V_{P-P}, V_O=10MHz / 100kHz$	-1.0	0	+1.0	dB
Voltage Gain	G_V	$V_{IN}=2V_{P-P}, 100kHz$	-0.5	0	+0.5	dB
Total Harmonic Distortion	THD	$V_{IN}=2.5V_{P-P}, 1kHz$	-	0.03	-	%
Output Offset Voltage	V_{off}		-35	0	+35	mV
Switching Voltage	V_{CH}		2.4	-	-	V
	V_{CL}		-	-	0.8	V
Input Impedance	R_i		-	58	-	kΩ
Output Impedance	R_o		-	10	-	Ω

■ INPUT CONTROL SIGNAL-OUTPUT SIGNAL

SW1	SW2	OUTPUT SIGNAL
L	L	V_{IN1}
H	L	V_{IN2}
L/H	H	V_{IN3}

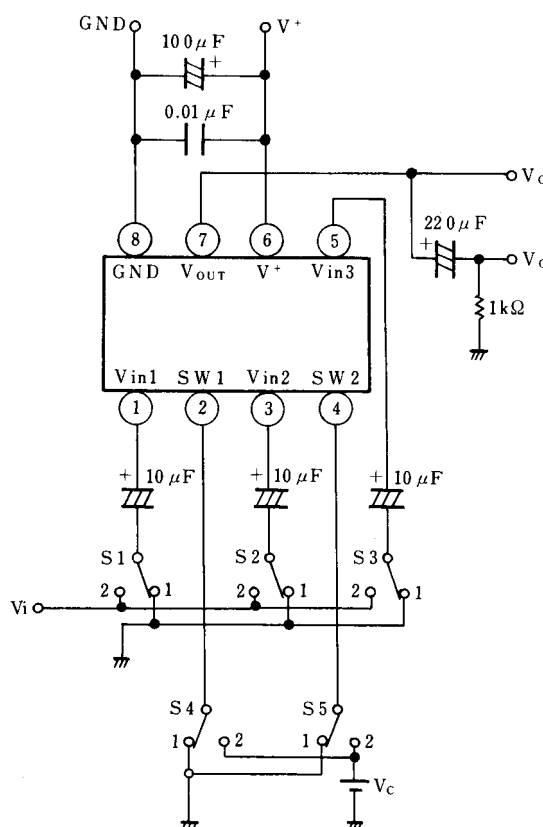
■ APPLICATION

Oscillation Prevention on light loading conditions
Recommended under circuit



$r \cong 100\Omega$
 $C \cong 20pF$

■ TEST CIRCUIT



[CAUTION]

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