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Electronic Components Sourcing Information

Product Reference Information

Part Number: PMEG2010BER,115
Manufacturer: Nexperia USA Inc.
Package: SOD-123W
Availability: Please confirm with sales

Product Page:

<https://antrexco.com/product/details/nexperia-usa-inc/pmeg2010ber-115.html>

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Note:

This PDF includes an ANTREX product reference cover page.

The original manufacturer datasheet follows from the next page.



PMEG2010BER

20 V, 1 A low VF Schottky barrier rectifier

1 January 2023

Product data sheet

1. General description

Planar Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD123W small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: $I_{F(AV)} \leq 1$ A
- Reverse voltage: $V_R \leq 20$ V
- Low forward voltage
- High power capability due to clip-bond technology
- Small and flat lead SMD plastic package

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- Reverse polarity protection
- Low power consumption applications

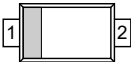
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$I_{F(AV)}$	average forward current	$\delta = 0.5$; $f = 20$ kHz; square wave; $T_{sp} \leq 140$ °C	-	-	1	A
V_R	reverse voltage	$T_j = 25$ °C	-	-	20	V
V_F	forward voltage	$I_F = 1$ A; $T_j = 25$ °C	-	395	450	mV
I_R	reverse current	$V_R = 20$ V; $T_j = 25$ °C	-	8	50	μ A

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]	 CFP3 (SOD123W)	 K $\rightarrow$ A sym001
2	A	anode		

[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMEG2010BER	CFP3	plastic, surface mounted package; 2 terminals; 2.6 mm x 1.7 mm x 1 mm body	SOD123W

7. Marking

Table 4. Marking codes

Type number	Marking code
PMEG2010BER	B6

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	20	V
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{amb} ≤ 125 °C	[1]	-	1	A
		δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 140 °C		-	1	A
I _{FSM}	non-repetitive peak forward current	t _p = 8.3 ms; half sine wave; T _{j(init)} = 25 °C		-	50	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[2]	-	0.57	W
			[3]	-	0.95	W
			[1]	-	1.8	W
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	220	K/W
			[1] [3]	-	-	130	K/W
			[1] [4]	-	-	70	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[5]	-	-	18	K/W

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [4] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.
- [5] Soldering point of cathode tab.

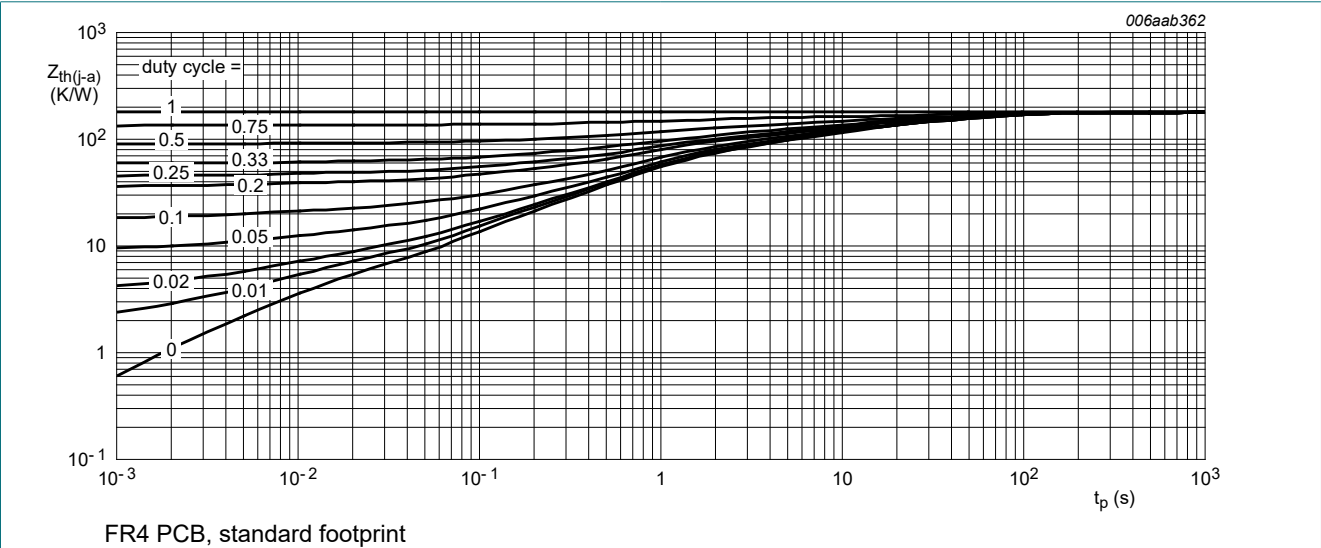


Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

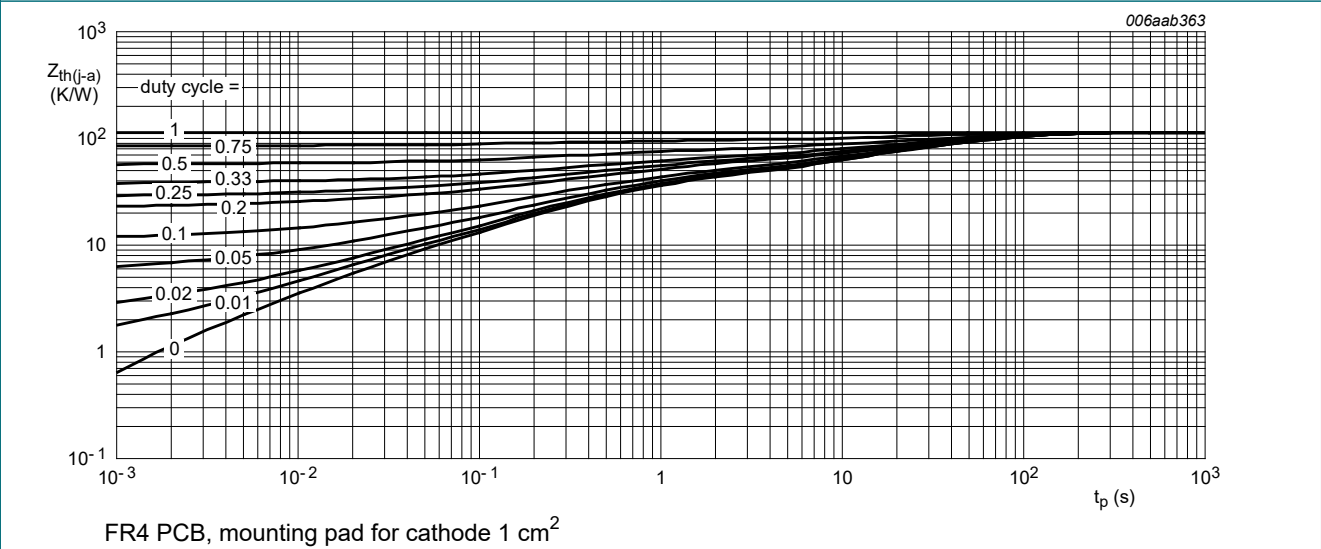
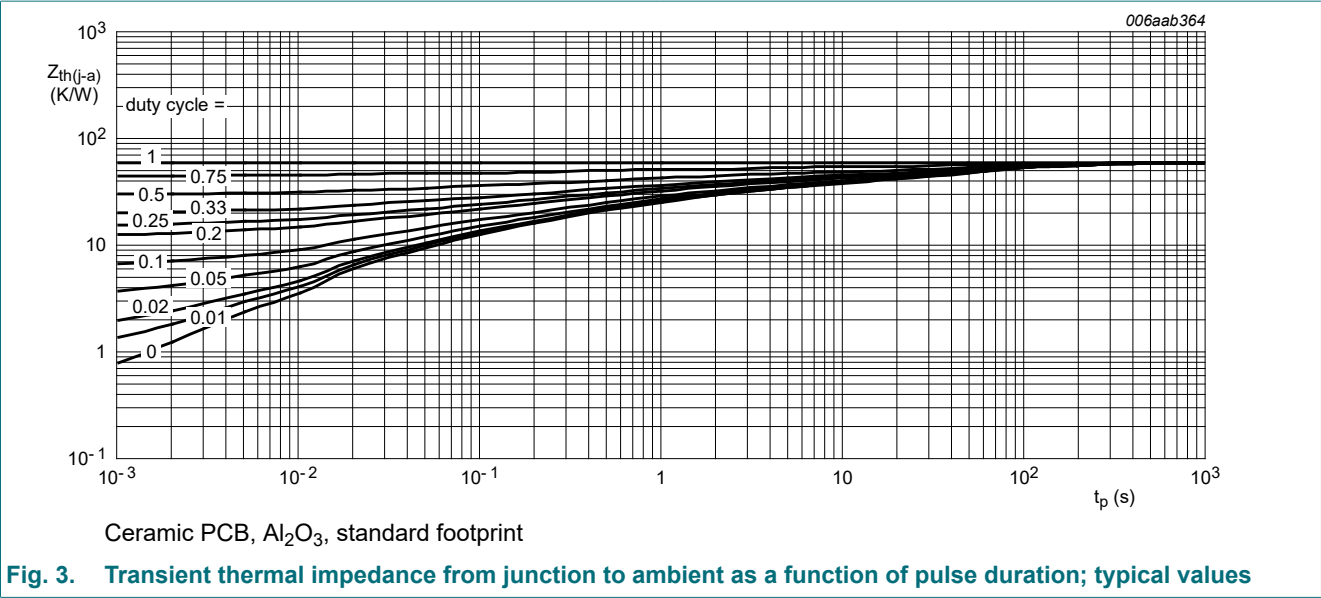


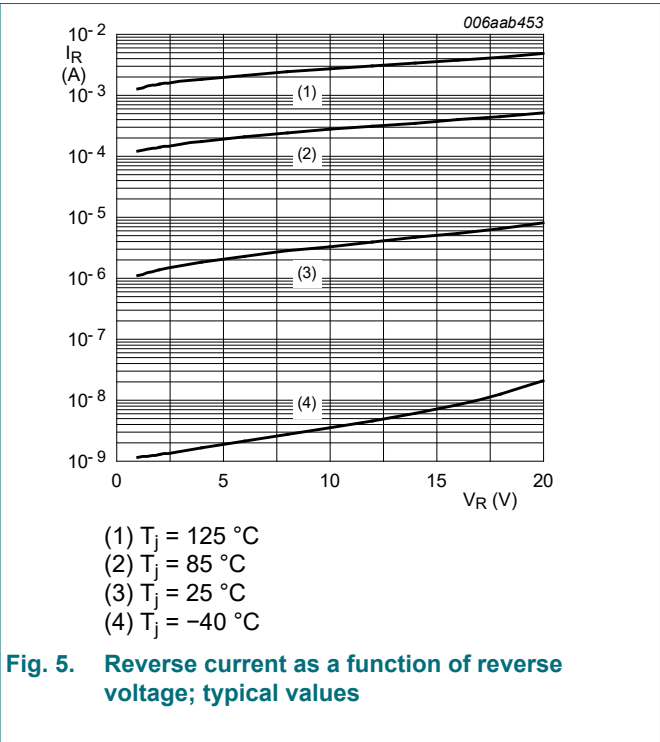
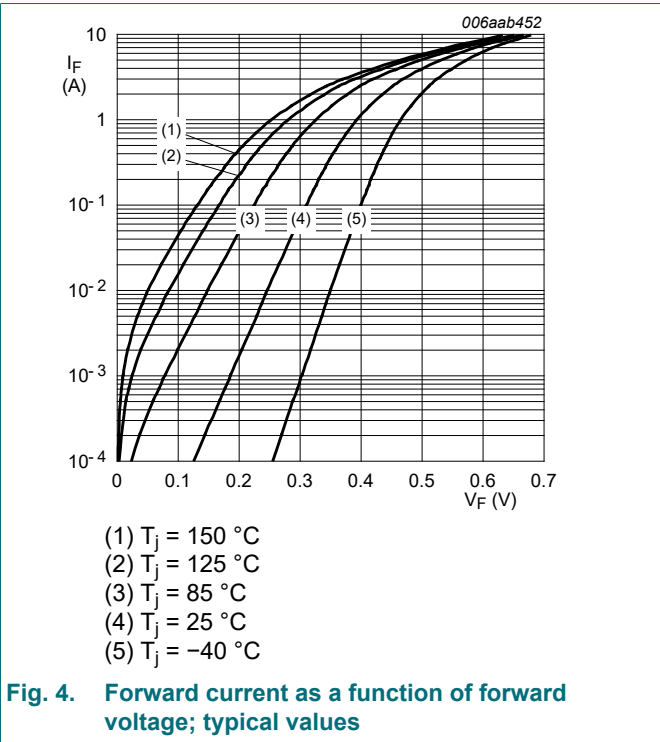
Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V _F	forward voltage	I _F = 0.1 A; T _j = 25 °C		-	310	340	mV
		I _F = 0.7 A; T _j = 25 °C		-	380	430	mV
		I _F = 1 A; T _j = 25 °C		-	395	450	mV
I _R	reverse current	V _R = 5 V; T _j = 25 °C		-	2	-	μA
		V _R = 10 V; T _j = 25 °C		-	3	-	μA
		V _R = 20 V; T _j = 25 °C		-	8	50	μA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C		-	185	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C		-	65	-	pF



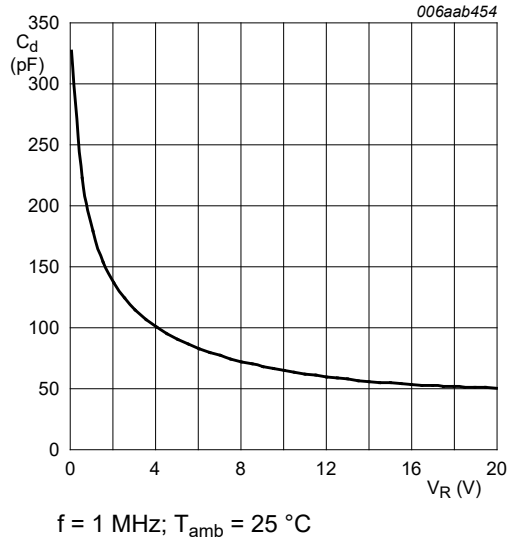


Fig. 6. Diode capacitance as a function of reverse voltage; typical values

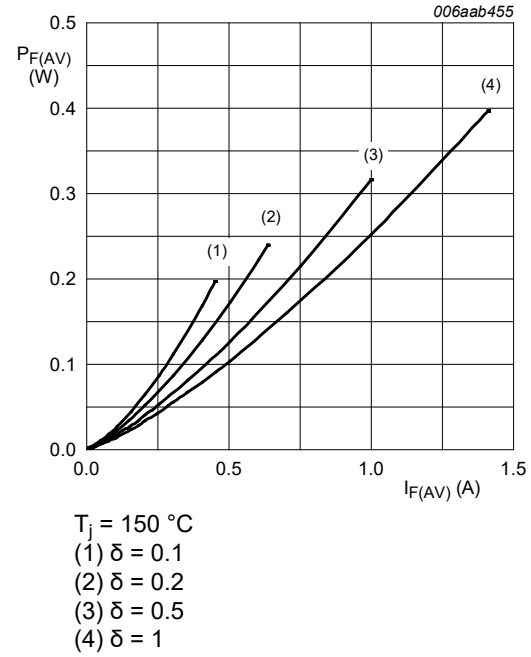


Fig. 7. Average forward power dissipation as a function of average forward current; typical values

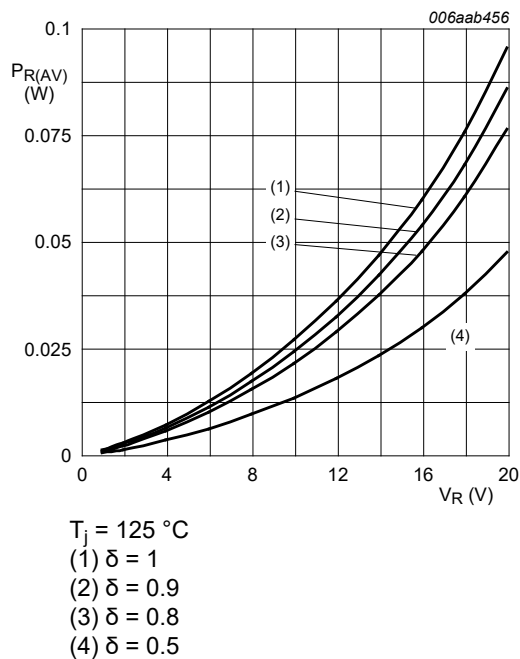


Fig. 8. Average reverse power dissipation as a function of reverse voltage; typical values

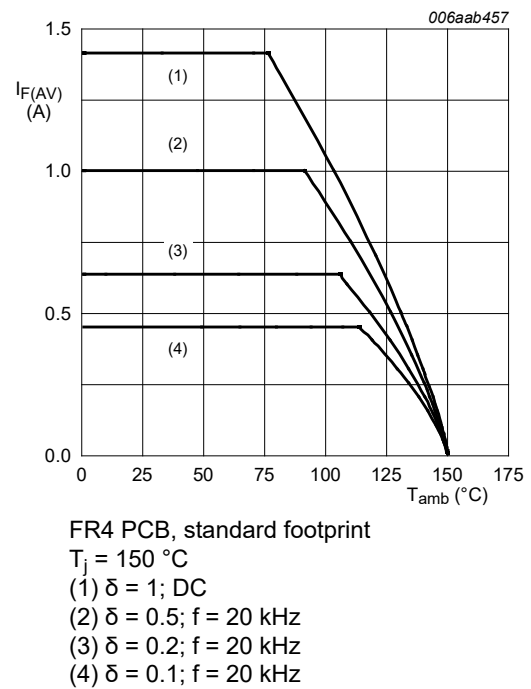
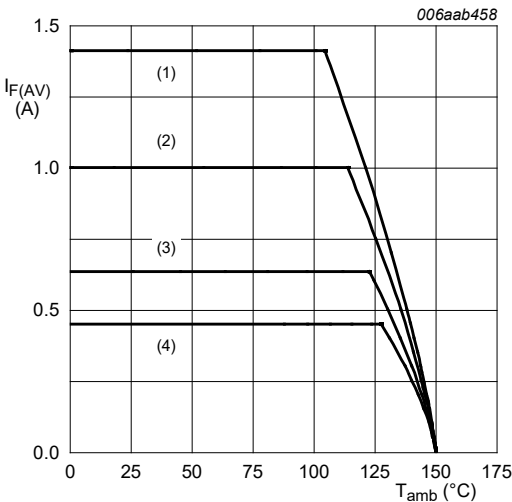
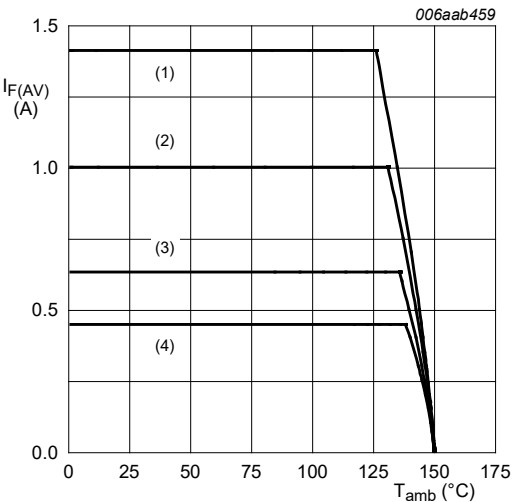


Fig. 9. Average forward current as a function of ambient temperature; typical values



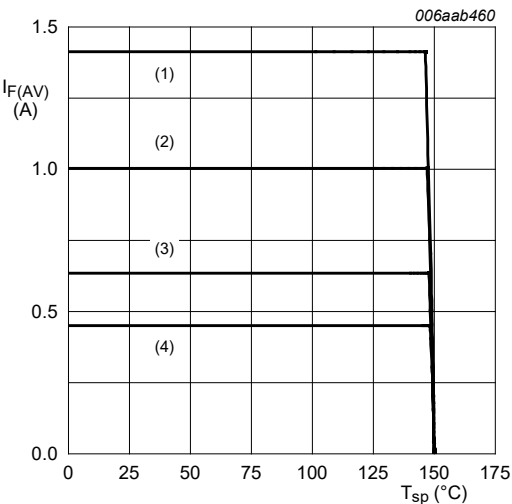
FR4 PCB, mounting pad for cathode 1 cm²
 $T_j = 150$ °C
(1) $\delta = 1$; DC
(2) $\delta = 0.5$; $f = 20$ kHz
(3) $\delta = 0.2$; $f = 20$ kHz
(4) $\delta = 0.1$; $f = 20$ kHz

Fig. 10. Average forward current as a function of ambient temperature; typical values



Ceramic PCB, Al₂O₃, standard footprint
 $T_j = 150$ °C
(1) $\delta = 1$; DC
(2) $\delta = 0.5$; $f = 20$ kHz
(3) $\delta = 0.2$; $f = 20$ kHz
(4) $\delta = 0.1$; $f = 20$ kHz

Fig. 11. Average forward current as a function of ambient temperature; typical values



$T_j = 150$ °C
(1) $\delta = 1$; DC
(2) $\delta = 0.5$; $f = 20$ kHz
(3) $\delta = 0.2$; $f = 20$ kHz
(4) $\delta = 0.1$; $f = 20$ kHz

Fig. 12. Average forward current as a function of solder point temperature; typical values

11. Test information

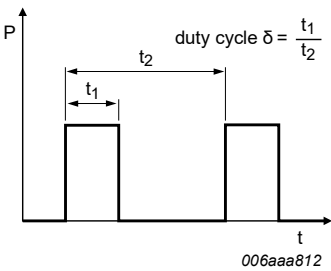


Fig. 13. Duty cycle definition

The current ratings for the typical waveforms are calculated according to the equations:

$I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current

$I_{RMS} = I_{F(AV)}$ at DC

$I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current

12. Package outline

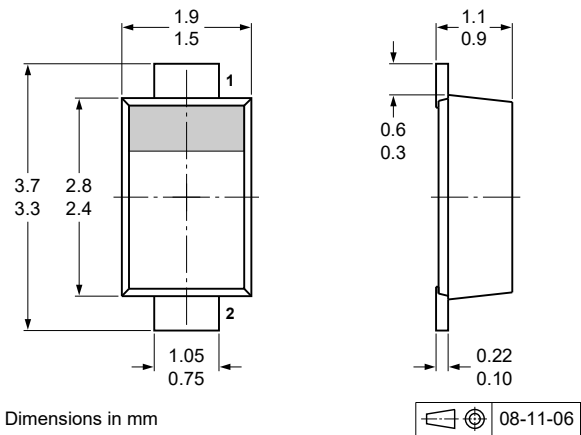


Fig. 14. Package outline CFP3 (SOD123W)

13. Soldering

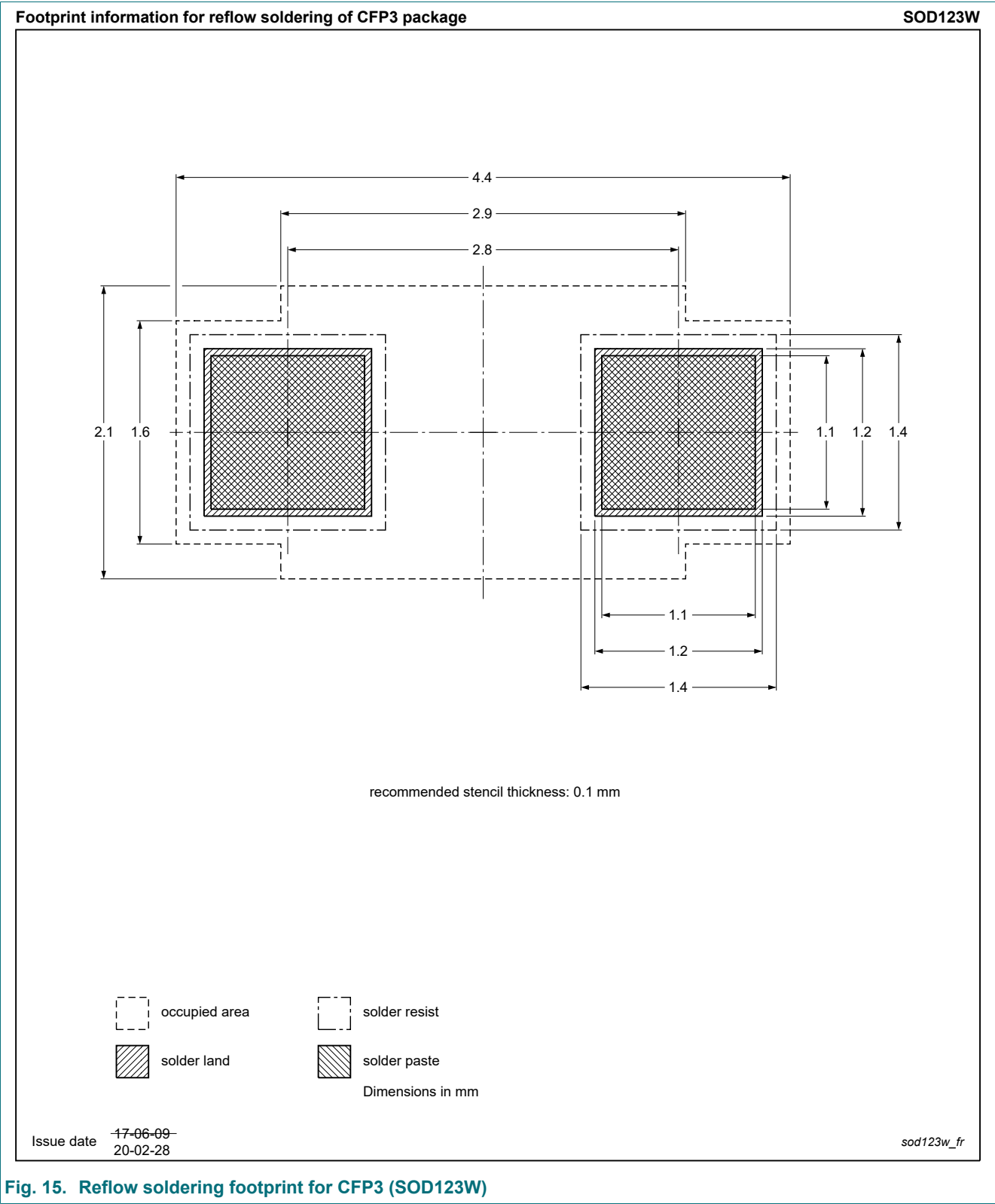


Fig. 15. Reflow soldering footprint for CFP3 (SOD123W)

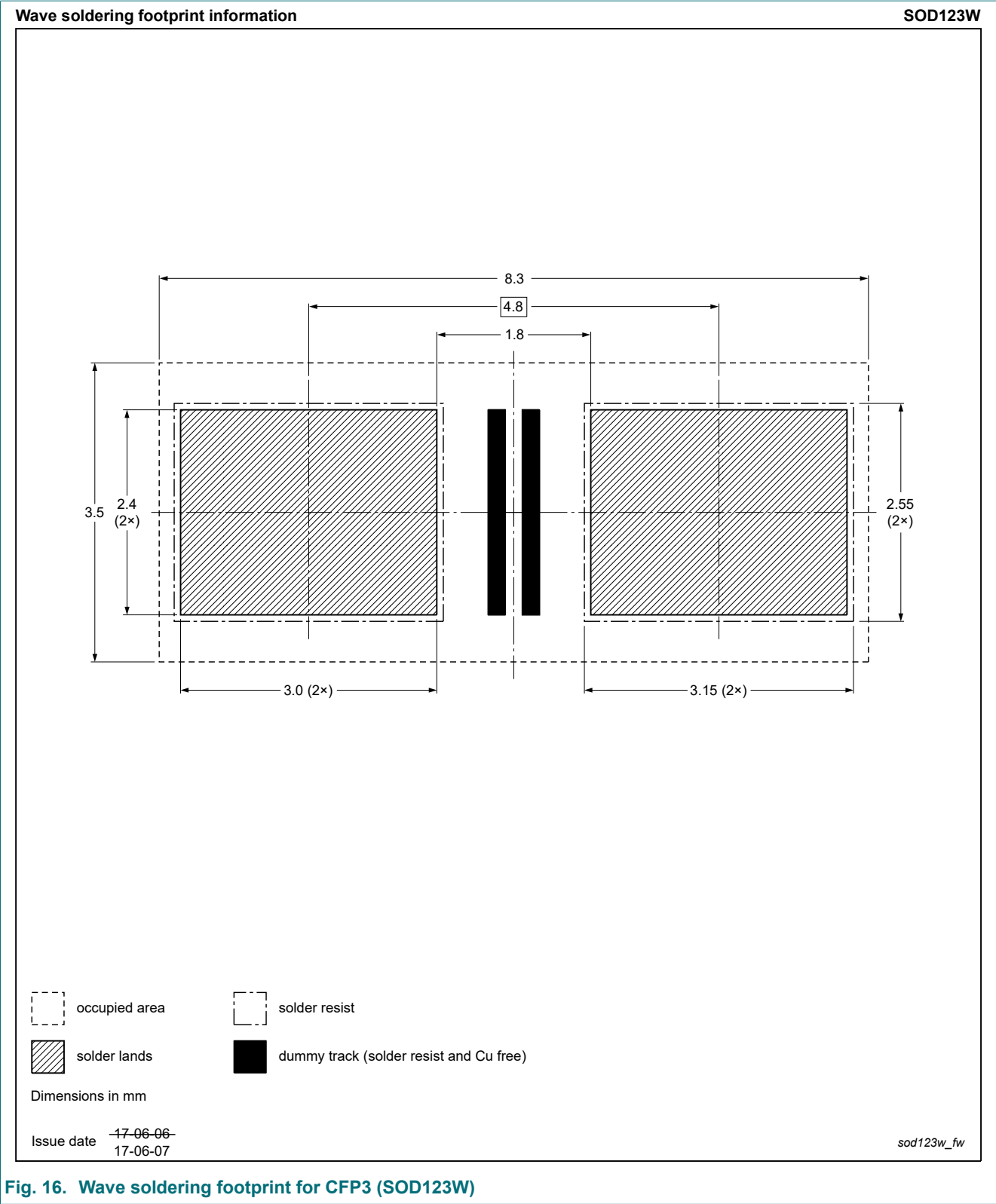


Fig. 16. Wave soldering footprint for CFP3 (SOD123W)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG2010BER v.3	20230101	Product data sheet	-	PMEG2010BER v.2
Modifications	- Limiting values: Measurement conditions for I_{FSM} changed from square wave to half-sine wave. - Product changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s).			
PMEG2010BER v.2	20180822	Product data sheet	-	PMEG2010BER_1
PMEG2010BER_1	20090416	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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Date of release: 1 January 2023