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Electronic Components Sourcing Information

Product Reference Information

Part Number:	PMDXB290UNEZ
Manufacturer:	Nexperia USA Inc.
Package:	Please confirm with sales
Availability:	Please confirm with sales

Product Page:

<https://antrexco.com/product/details/nexperia-usa-inc/pmdxb290unez.html>

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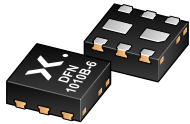
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Note:

This PDF includes an ANTREX product reference cover page.

The original manufacturer datasheet follows from the next page.



PMDXB290UNE

20 V, dual N-channel Trench MOSFET

19 January 2023

Product data sheet

1. General description

Dual N-channel enhancement mode Field-Effect Transistor (FET) in a leadless ultra small DFN1010B-6 (SOT1216) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Low threshold voltage
- Very fast switching
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection > 2 kV HBM

3. Applications

- Relay driver
- High-speed line driver
- Low-side load switch
- Switching circuits

4. Quick reference data

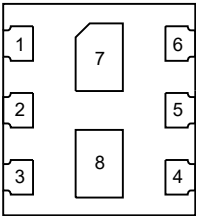
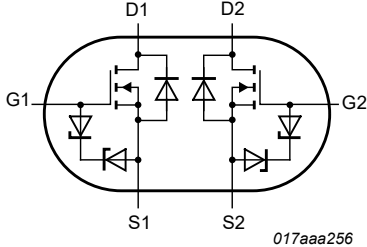
Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{DS}	drain-source voltage	$T_j = 25\text{ }^{\circ}\text{C}$		-	-	20	V
V_{GS}	gate-source voltage			-8	-	8	V
I_D	drain current	$V_{GS} = 4.5\text{ V}; T_{amb} = 25\text{ }^{\circ}\text{C}$	[1]	-	-	930	mA
Static characteristics							
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 4.5\text{ V}; I_D = 1.2\text{ A}; T_j = 25\text{ }^{\circ}\text{C}$		-	270	320	m Ω

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S1	source TR1	 Transparent top view DFN1010B-6 (SOT1216)	 017aaa256
2	G1	gate TR1		
3	D2	drain TR2		
4	S2	source TR2		
5	G2	gate TR2		
6	D1	drain TR1		
7	D1	drain TR1		
8	D2	drain TR2		

6. Ordering information

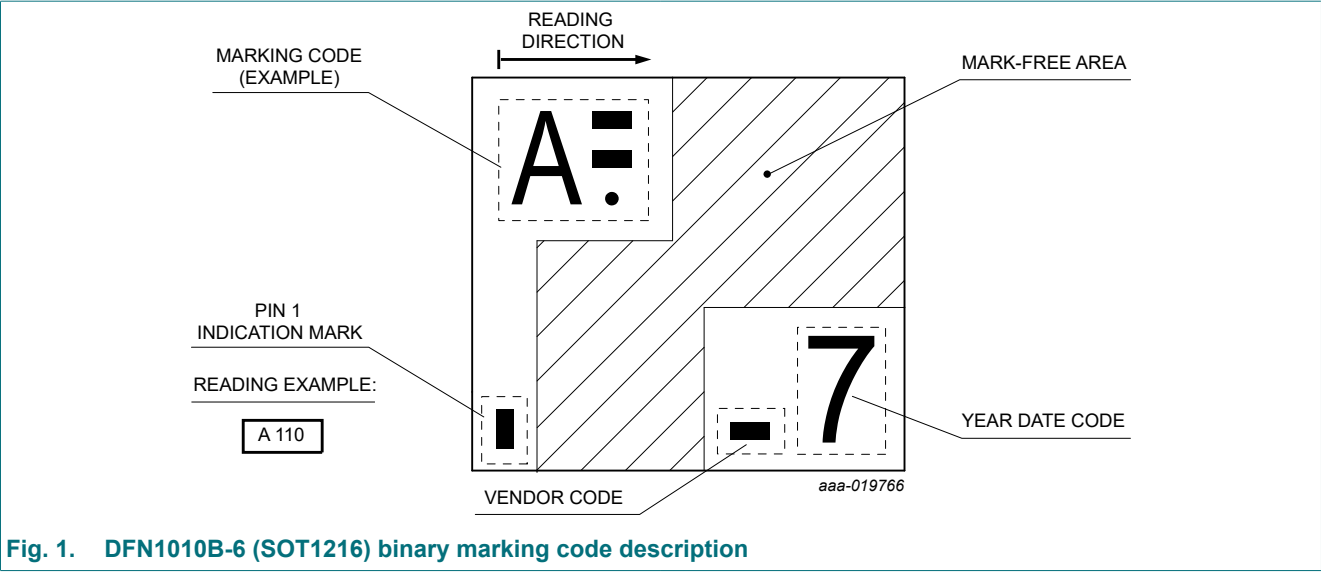
Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMDXB290UNE	DFN1010B-6	plastic, leadless thermal enhanced ultra thin small outline package; 6 terminals; 0.35 mm pitch; 1.1 mm x 1 mm x 0.37 mm body	SOT1216

7. Marking

Table 4. Marking codes

Type number	Marking code
PMDXB290UNE	D 001



8. Limiting values

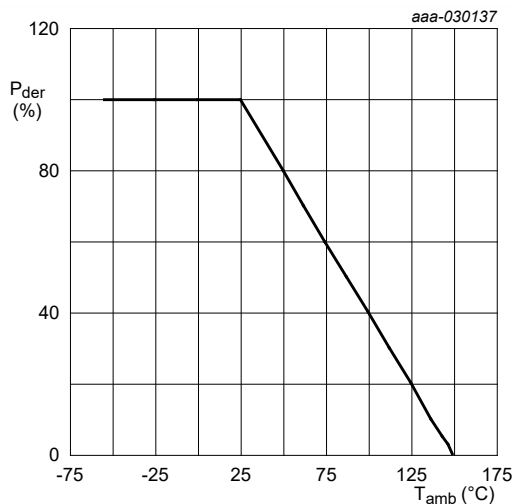
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	20	V
V _{GS}	gate-source voltage			-8	8	V
I _D	drain current	V _{GS} = 4.5 V; T _{amb} = 25 °C	[1]	-	930	mA
		V _{GS} = 4.5 V; T _{sp} = 25 °C		-	3.5	A
		V _{GS} = 4.5 V; T _{amb} = 100 °C	[1]	-	590	mA
		V _{GS} = 4.5 V; T _{sp} = 100 °C		-	2.2	A
I _{DM}	peak drain current	T _{amb} = 25 °C; single pulse; t _p ≤ 10 μs		-	14	A
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	280	mW
			[1]	-	370	mW
		T _{sp} = 25 °C		-	6	W
T _j	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drain diode						
I _S	source current	T _{amb} = 25 °C	[1]	-	0.3	A
ESD maximum rating						
V _{ESD}	electrostatic discharge voltage	HBM		-	2000	V

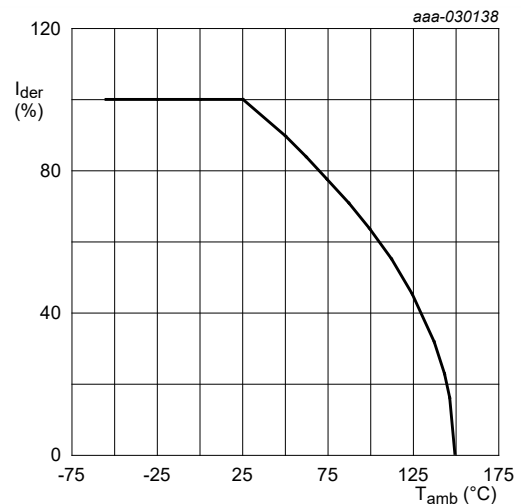
[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



$$P_{der} = \frac{P_{tot}}{P_{tot}(25^{\circ}\text{C})} \times 100\%$$

Fig. 2. Normalized total power dissipation as a function of ambient temperature



$$I_{der} = \frac{I_D}{I_D(25^{\circ}\text{C})} \times 100\%$$

Fig. 3. Normalized continuous drain current as a function of ambient temperature

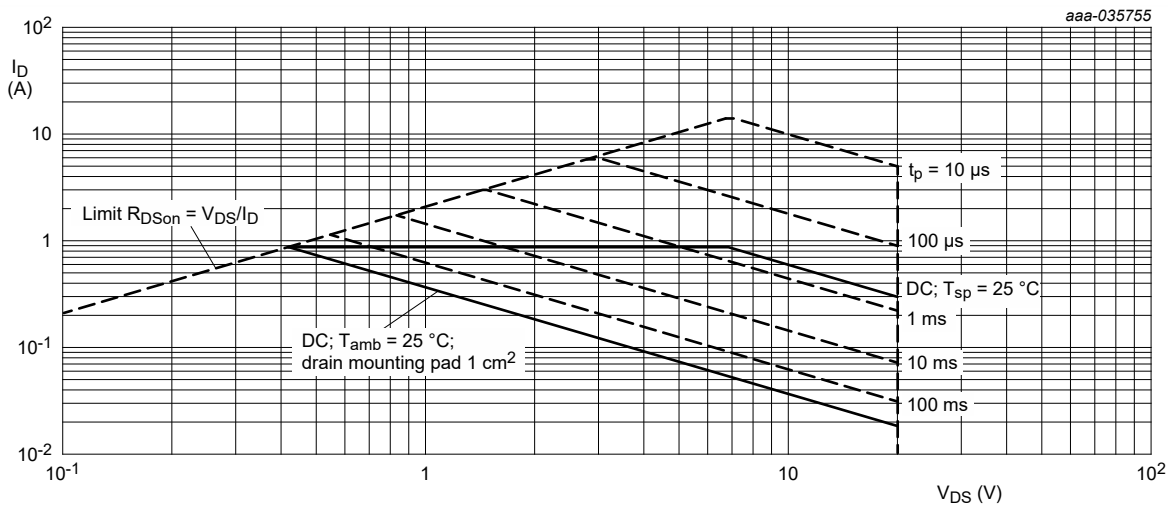


Fig. 4. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	386	444	K/W
			[2]	-	297	342	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	18	21	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm².

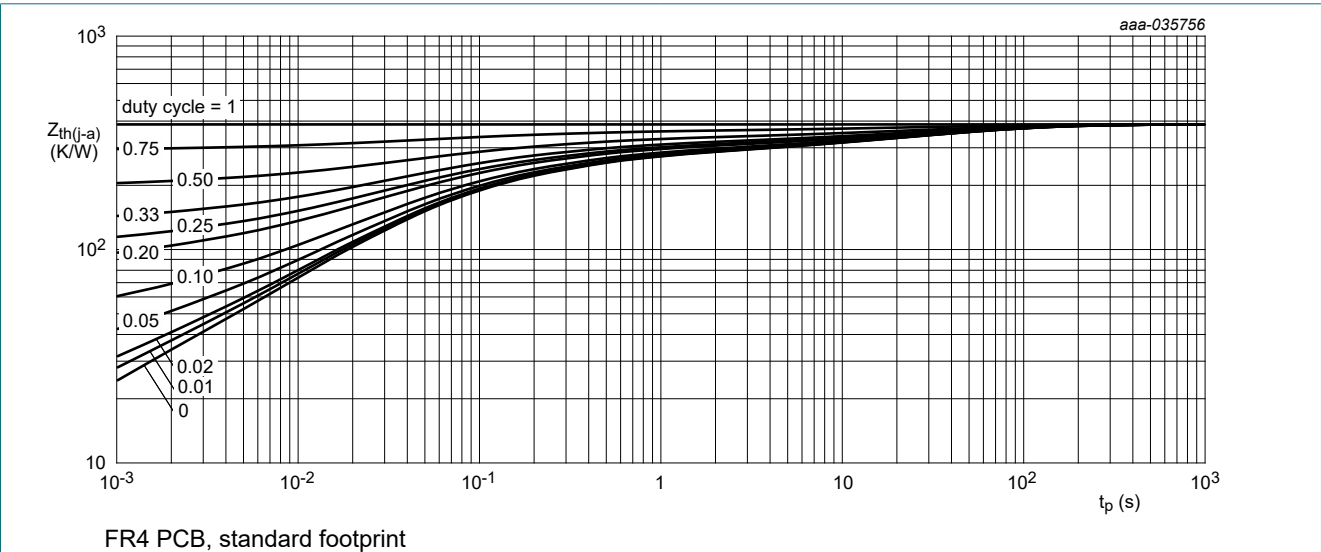


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

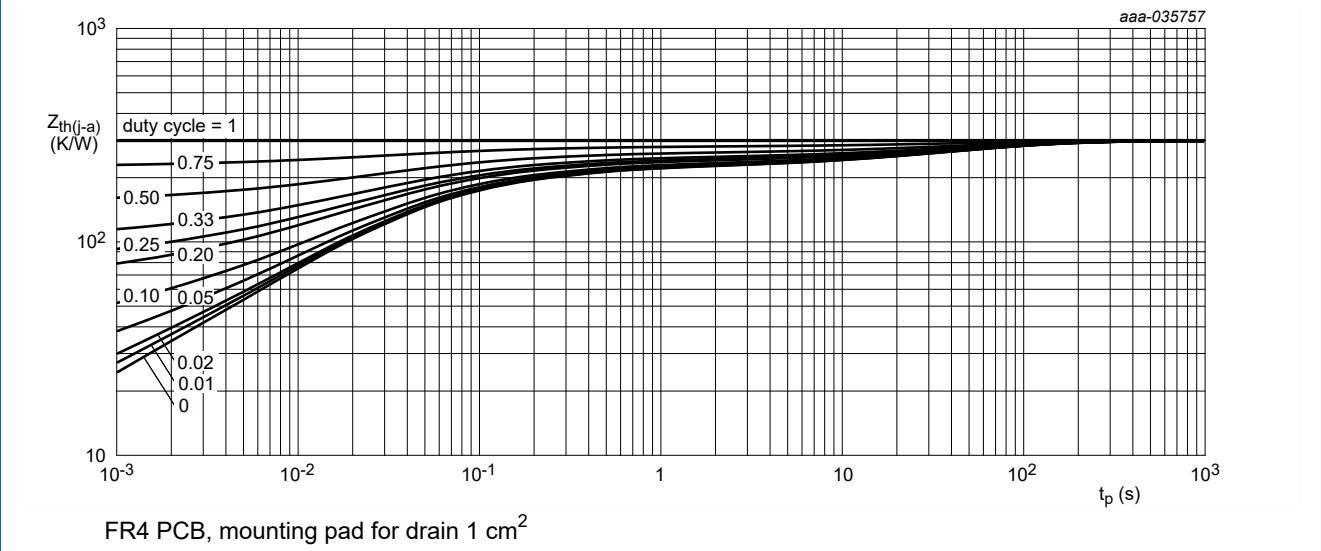


Fig. 6. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Static characteristics							
V _{(BR)DSS}	drain-source breakdown voltage	I _D = 250 μA; V _{GS} = 0 V; T _j = 25 °C		20	-	-	V
V _{GSth}	gate-source threshold voltage	I _D = 250 μA; V _{DS} =V _{GS} ; T _j = 25 °C		0.5	0.7	1	V
I _{DSS}	drain leakage current	V _{DS} = 20 V; V _{GS} = 0 V; T _j = 25 °C		-	-	1	μA
		V _{DS} = 20 V; V _{GS} = 0 V; T _j = 150 °C		-	-	20	μA
I _{GSS}	gate leakage current	V _{GS} = 8 V; V _{DS} = 0 V; T _j = 25 °C		-	-	10	μA
		V _{GS} = -8 V; V _{DS} = 0 V; T _j = 25 °C		-	-	-10	μA
		V _{GS} = 4.5 V; V _{DS} = 0 V; T _j = 25 °C		-	-	1	μA
		V _{GS} = -4.5 V; V _{DS} = 0 V; T _j = 25 °C		-	-	-1	μA
		V _{GS} = 2.5 V; V _{DS} = 0 V; T _j = 25 °C		-	-	500	nA
		V _{GS} = -2.5 V; V _{DS} = 0 V; T _j = 25 °C		-	-	-500	nA
R _{DSon}	drain-source on-state resistance	V _{GS} = 4.5 V; I _D = 1.2 A; T _j = 25 °C		-	270	320	mΩ
		V _{GS} = 4.5 V; I _D = 1.2 A; T _j = 150 °C		-	400	480	mΩ
		V _{GS} = 2.5 V; I _D = 1 A; T _j = 25 °C		-	360	480	mΩ
		V _{GS} = 1.8 V; I _D = 120 mA; T _j = 25 °C		-	470	680	mΩ
		V _{GS} = 1.5 V; I _D = 10 mA; T _j = 25 °C		-	600	1190	mΩ
g _{fs}	forward transconductance	V _{DS} = 5 V; I _D = 1.2 A; T _j = 25 °C		-	1.9	-	S
Dynamic characteristics							
Q _{G(tot)}	total gate charge	V _{DS} = 10 V; I _D = 1.2 A; V _{GS} = 4.5 V; T _j = 25 °C		-	0.6	0.9	nC
Q _{GS}	gate-source charge			-	0.1	-	nC
Q _{GD}	gate-drain charge			-	0.2	-	nC
C _{iss}	input capacitance	V _{DS} = 10 V; f = 1 MHz; V _{GS} = 0 V; T _j = 25 °C		-	43.6	-	pF
C _{oss}	output capacitance			-	10.1	-	pF
C _{rss}	reverse transfer capacitance			-	8.2	-	pF
t _{d(on)}	turn-on delay time	V _{DS} = 10 V; I _D = 1.2 A; V _{GS} = 4.5 V; R _{G(ext)} = 6 Ω; T _j = 25 °C		-	1	-	ns
t _r	rise time			-	3	-	ns
t _{d(off)}	turn-off delay time			-	5	-	ns
t _f	fall time			-	3	-	ns
Source-drain diode							
V _{SD}	source-drain voltage	I _S = 0.7 A; V _{GS} = 0 V; T _j = 25 °C		-	0.9	1.2	V

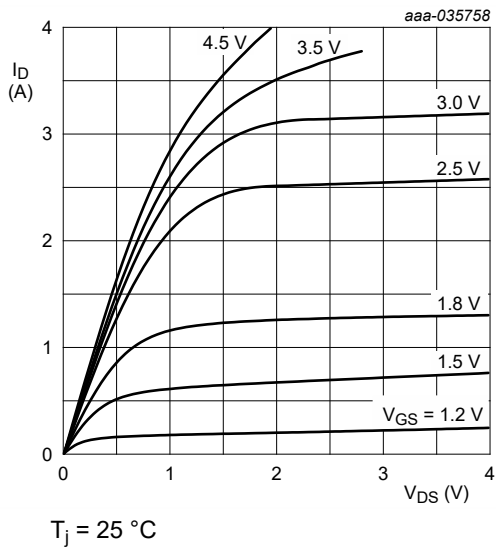


Fig. 7. Output characteristics: drain current as a function of drain-source voltage; typical values

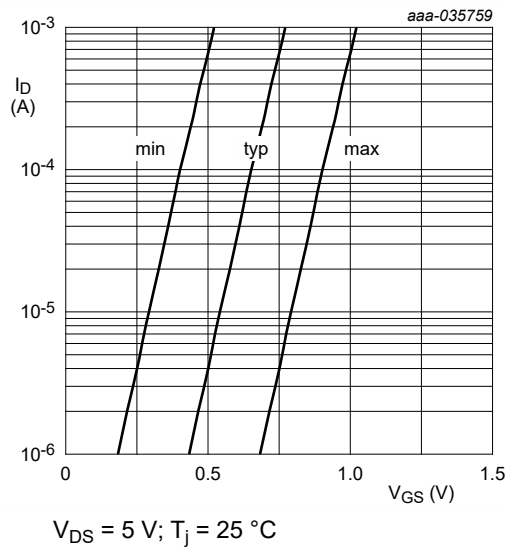


Fig. 8. Sub-threshold drain current as a function of gate-source voltage

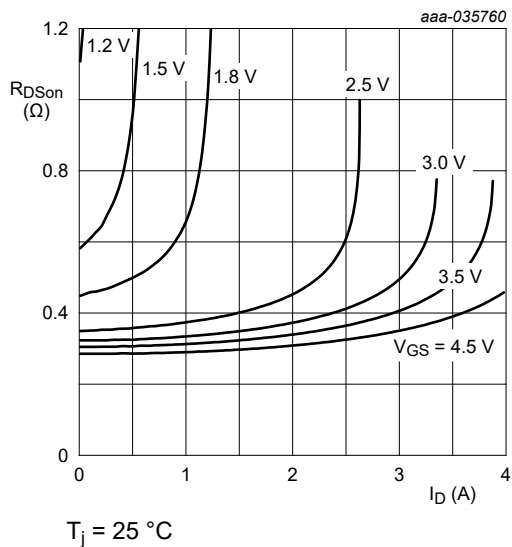


Fig. 9. Drain-source on-state resistance as a function of drain current; typical values

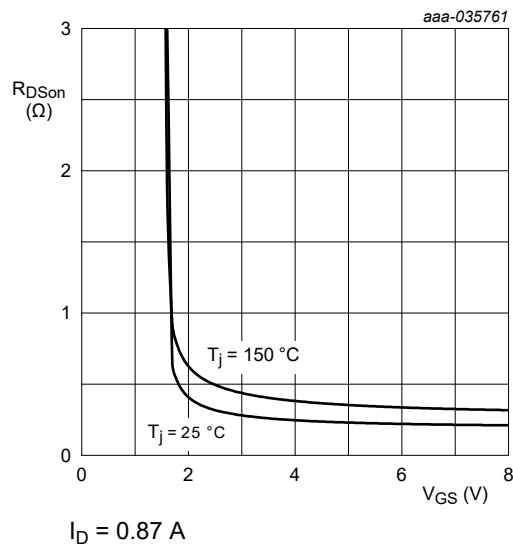


Fig. 10. Drain-source on-state resistance as a function of gate-source voltage; typical values

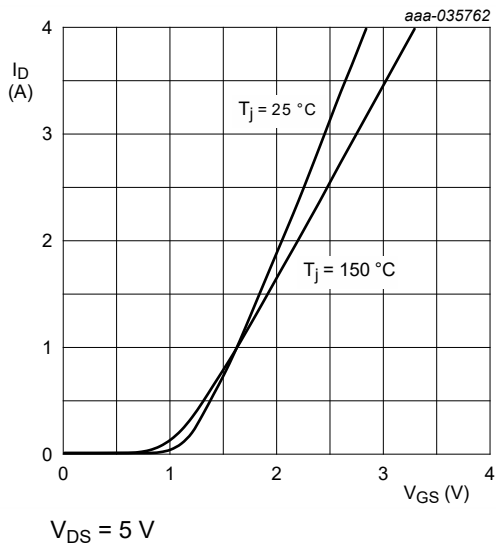


Fig. 11. Transfer characteristics: drain current as a function of gate-source voltage; typical values

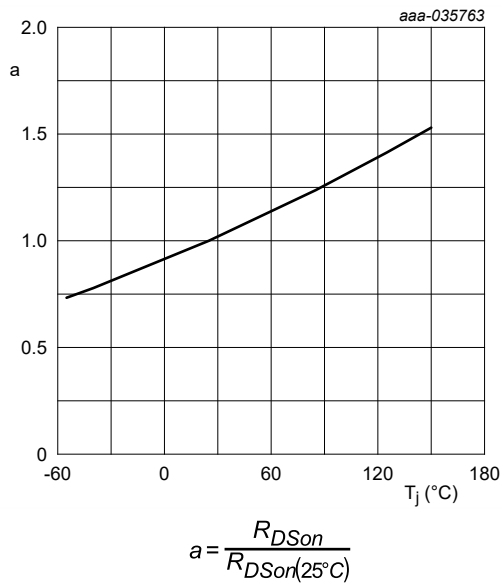


Fig. 12. Normalized drain-source on-state resistance as a function of junction temperature; typical values

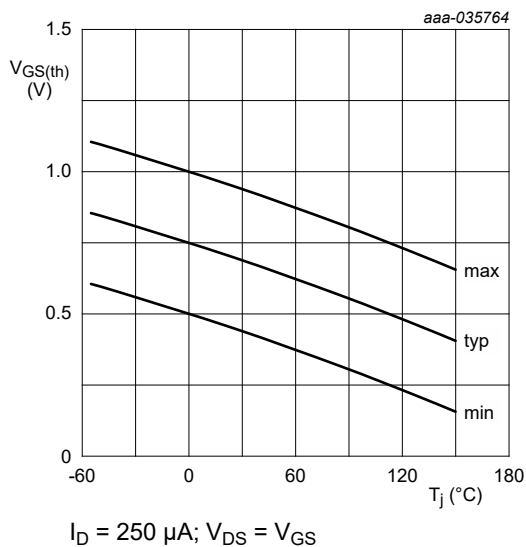


Fig. 13. Gate-source threshold voltage as a function of junction temperature

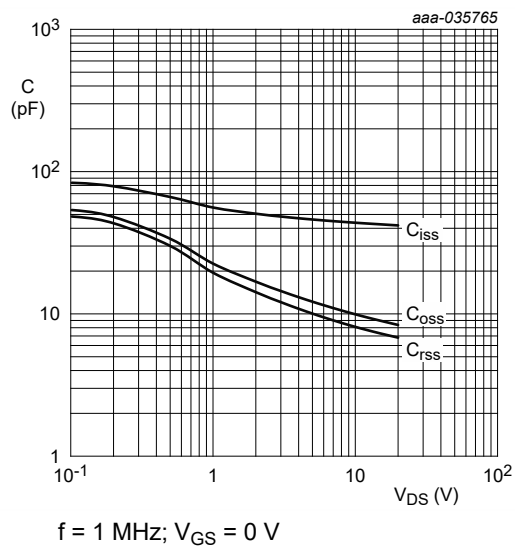


Fig. 14. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

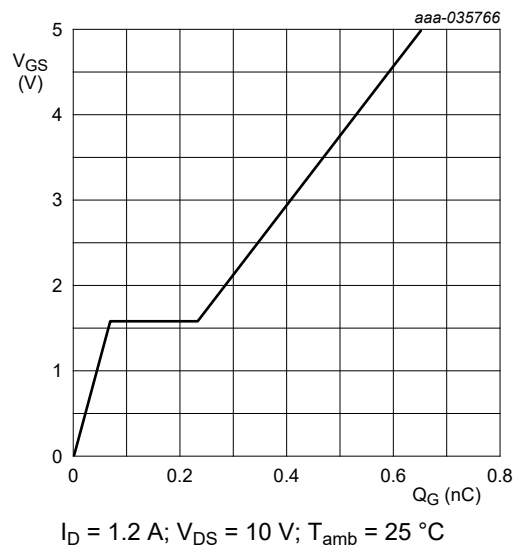


Fig. 15. Gate-source voltage as a function of gate charge; typical values

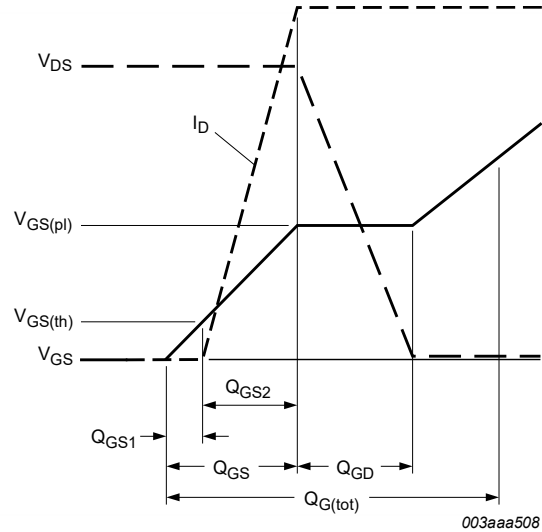


Fig. 16. Gate charge waveform definitions

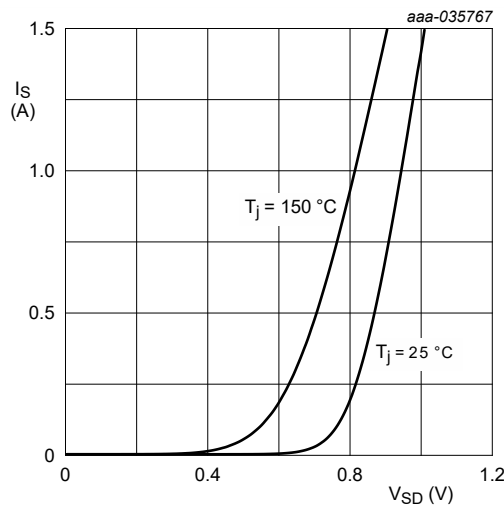


Fig. 17. Source current as a function of source-drain voltage; typical values

11. Test information

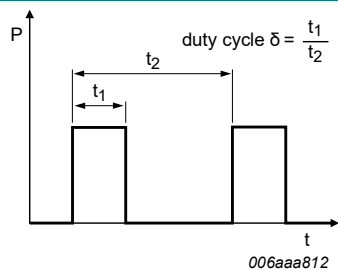


Fig. 18. Duty cycle definition

12. Package outline

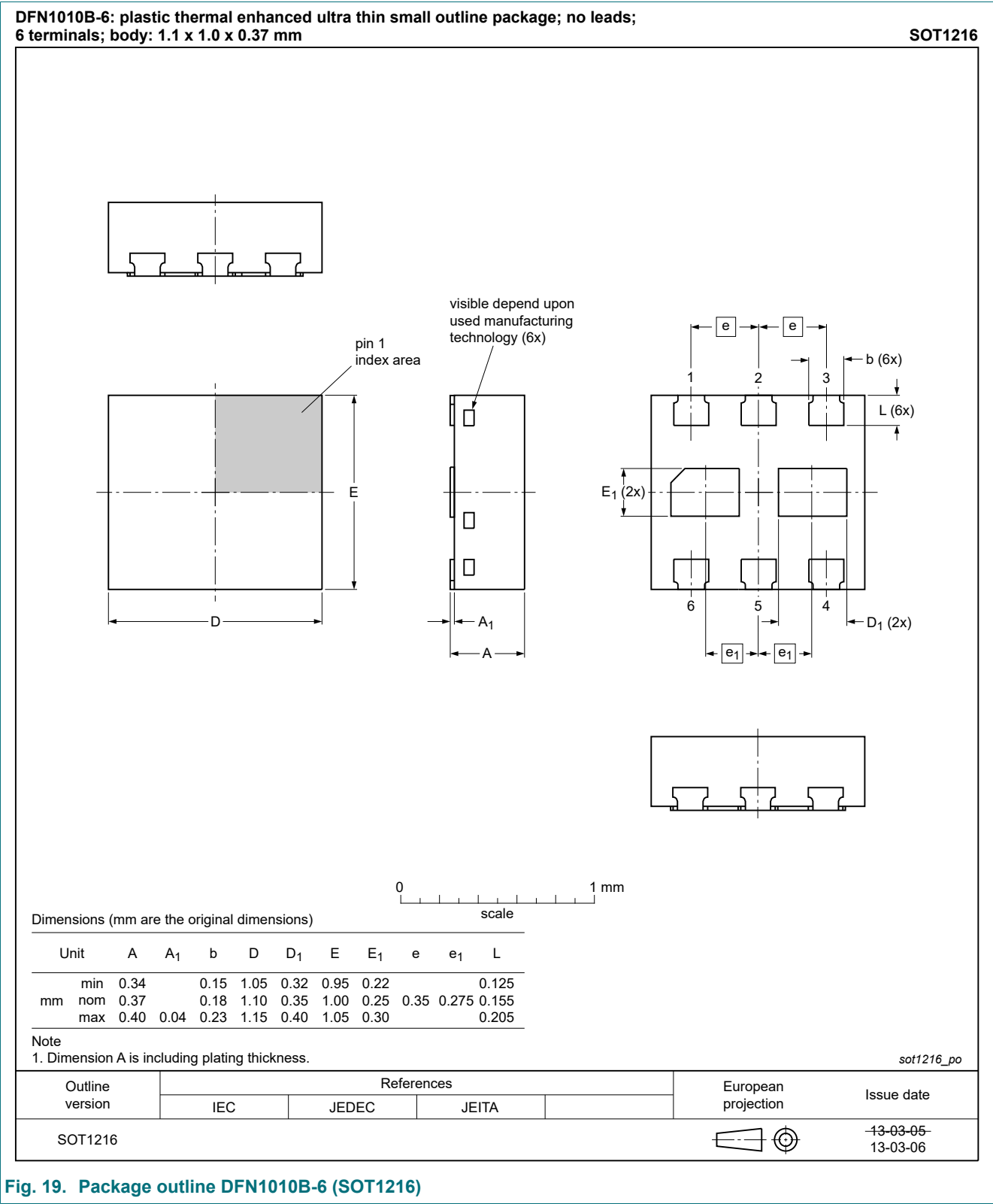


Fig. 19. Package outline DFN1010B-6 (SOT1216)

13. Soldering

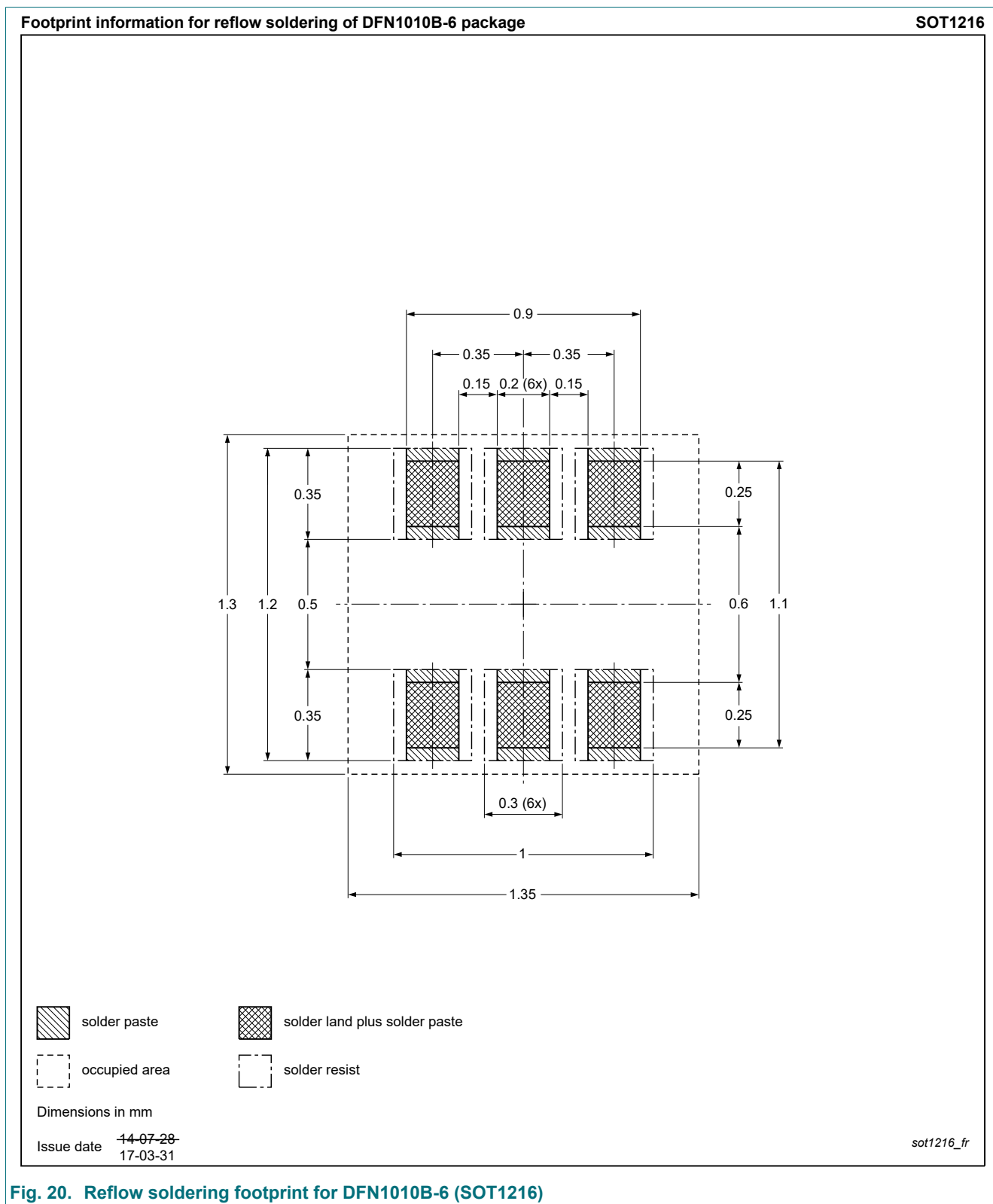


Fig. 20. Reflow soldering footprint for DFN1010B-6 (SOT1216)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMDXB290UNE v.1	20230119	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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