

ANTREX Electronics

Electronic Components Sourcing Information

Product Reference Information

Part Number: PBSS8110TVL

Manufacturer: Nexperia USA Inc.

Package: TO-236-3, SC-59, SOT-23-3

Availability: Please confirm with sales

Product Page:

<https://antrexco.com/product/details/nexperia-usa-inc/pbss8110tv.html>

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Note:

This PDF includes an ANTREX product reference cover page.

The original manufacturer datasheet follows from the next page.



PBSS8110T

100 V, 1 A NPN low V_{CEsat} transistor

28 September 2023

Product data sheet

1. General description

NPN low V_{CEsat} transistor in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

PNP complement: PBSS9110T

2. Features and benefits

- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability: I_C and I_{CM}

3. Applications

- Major application segments
 - Automotive 42 V power
 - Telecom infrastructure
 - Industrial
- Power management
 - DC/DC converters
 - Supply line switching
 - Battery charger
 - LCD backlighting
- Peripheral drivers
 - Driver in low supply voltage applications (e.g. lamps and LEDs)
 - Inductive load driver (e.g. relays, buzzers and motors)

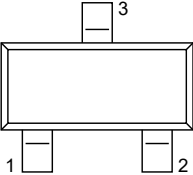
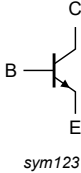
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	100	V
I _C	collector current		-	-	1	A
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	3	A
R _{CEsat}	collector-emitter saturation resistance	I _C = 1 A; I _B = 100 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	165	200	mΩ

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 SOT23	 sym123
2	E	emitter		
3	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS8110T	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PBSS8110T	%U8

[1] % = placeholder for manufacturing site code

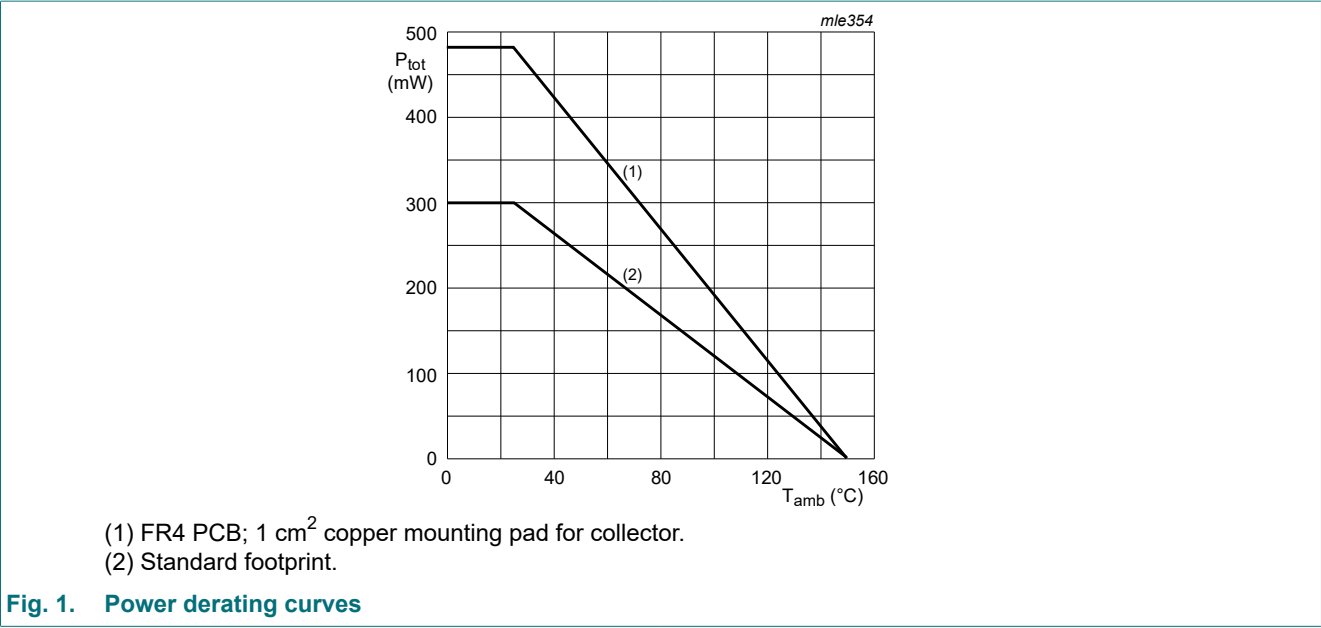
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	120	V
V _{CEO}	collector-emitter voltage	open base		-	100	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	1	A
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	3	A
I _B	base current			-	300	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW
			[2]	-	480	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².



9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	417	K/W
			[2]	-	-	260	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

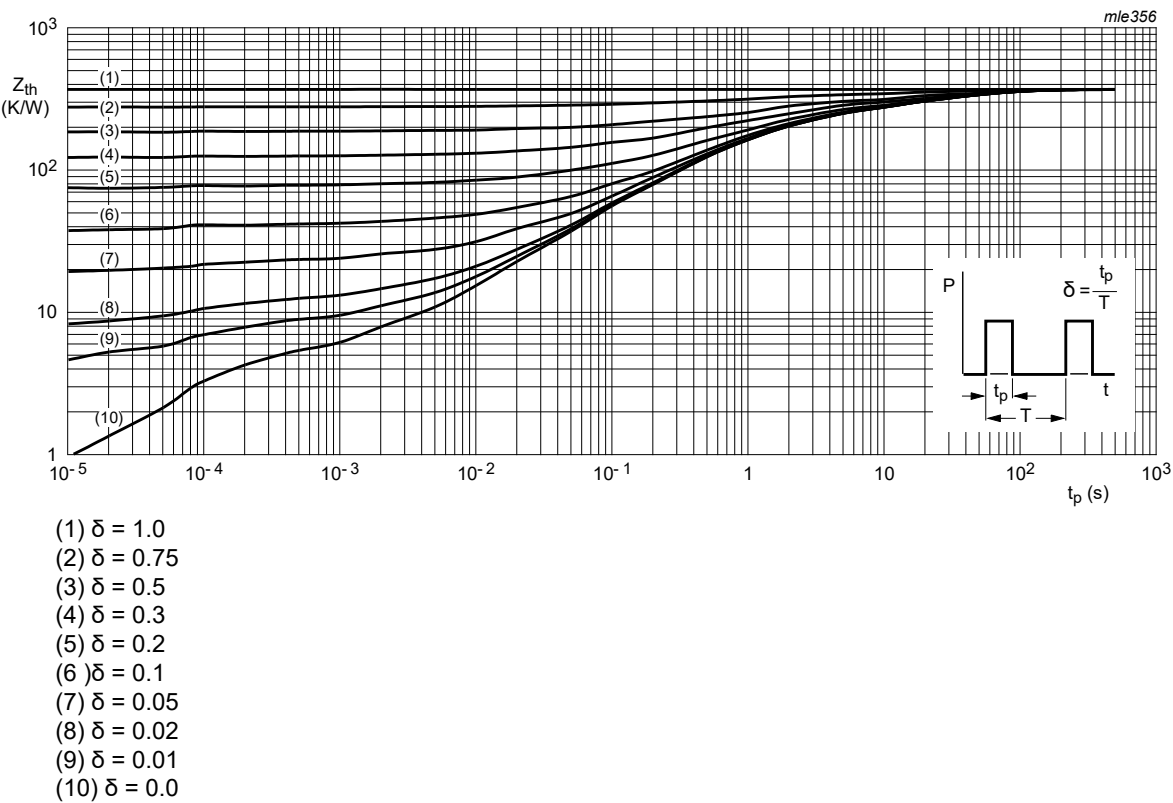


Fig. 2. Transient thermal impedance as a function of pulse time for standard PCB footprint

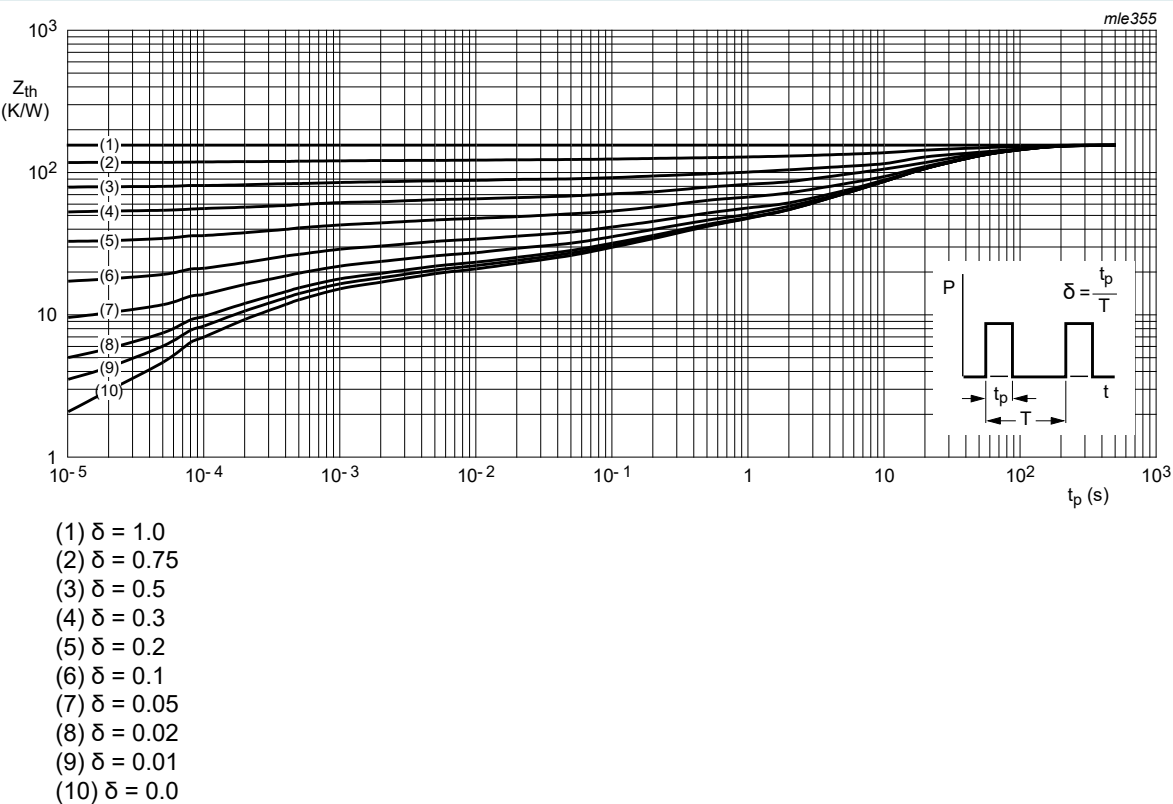


Fig. 3. Transient thermal impedance as a function of pulse time for collector 1 cm^2 copper mounting pad

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{(BR)CBO}$	collector-base breakdown voltage	$I_C = 100\ \mu\text{A}$; $I_E = 0\ \text{A}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	120	-	-	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	$I_C = 10\ \text{mA}$; $I_B = 0\ \text{A}$; pulsed; $t_p \leq 300\ \mu\text{s}$; $\delta \leq 0.02$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	100	-	-	V
$V_{(BR)EBO}$	emitter-base breakdown voltage (collector open)	$I_E = 100\ \mu\text{A}$; $I_C = 0\ \text{A}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	5	-	-	V
I_{CBO}	collector-base cut-off current	$V_{CB} = 80\ \text{V}$; $I_E = 0\ \text{A}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	-	-	100	nA
		$V_{CB} = 80\ \text{V}$; $I_E = 0\ \text{A}$; $T_J = 150\ ^\circ\text{C}$	-	-	50	μA
I_{EBO}	emitter-base cut-off current	$V_{EB} = 5\ \text{V}$; $I_C = 0\ \text{A}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	-	-	100	nA
I_{CES}	collector-emitter cut-off current	$V_{CE} = 80\ \text{V}$; $V_{BE} = 0\ \text{V}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	-	-	100	nA
h_{FE}	DC current gain	$V_{CE} = 10\ \text{V}$; $I_C = 1\ \text{mA}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	150	-	-	
		$V_{CE} = 10\ \text{V}$; $I_C = 250\ \text{mA}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	150	-	500	
		$V_{CE} = 10\ \text{V}$; $I_C = 500\ \text{mA}$; pulsed; $t_p \leq 300\ \mu\text{s}$; $\delta \leq 0.02$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	100	-	-	
		$V_{CE} = 10\ \text{V}$; $I_C = 1\ \text{A}$; pulsed; $t_p \leq 300\ \mu\text{s}$; $\delta \leq 0.02$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	80	-	-	
V_{CEsat}	collector-emitter saturation voltage	$I_C = 100\ \text{mA}$; $I_B = 10\ \text{mA}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	-	-	40	mV
		$I_C = 500\ \text{mA}$; $I_B = 50\ \text{mA}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	-	-	120	mV
		$I_C = 1\ \text{A}$; $I_B = 100\ \text{mA}$; pulsed; $t_p \leq 300\ \mu\text{s}$; $\delta \leq 0.02$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	-	-	200	mV
R_{CEsat}	collector-emitter saturation resistance		-	165	200	m Ω
V_{BEsat}	base-emitter saturation voltage	$I_C = 1\ \text{A}$; $I_B = 100\ \text{mA}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	-	-	1.05	V
V_{BEon}	base-emitter turn-on voltage	$V_{CE} = 10\ \text{V}$; $I_C = 1\ \text{A}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	-	-	0.9	V
f_T	transition frequency	$V_{CE} = 10\ \text{V}$; $I_C = 50\ \text{mA}$; $f = 100\ \text{MHz}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	100	-	-	MHz
C_c	collector capacitance	$V_{CB} = 10\ \text{V}$; $I_E = 0\ \text{A}$; $i_e = 0\ \text{A}$; $f = 1\ \text{MHz}$; $T_{\text{amb}} = 25\ ^\circ\text{C}$	-	-	7.5	pF

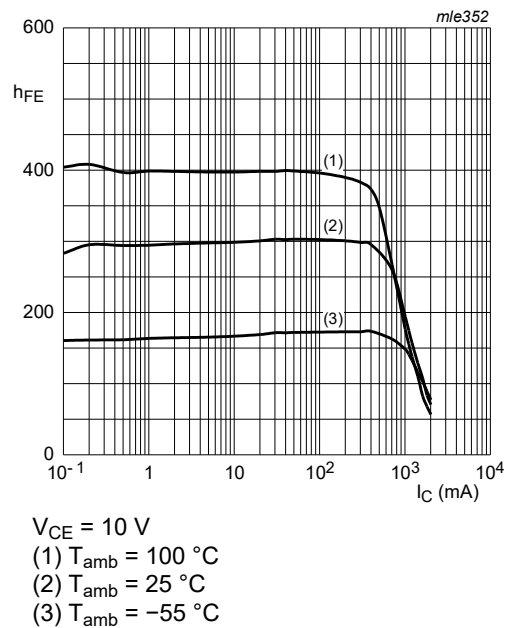


Fig. 4. DC current gain as a function of collector current; typical values

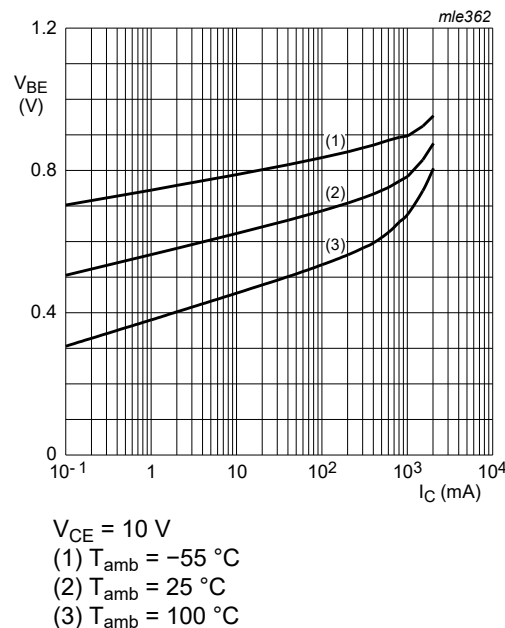


Fig. 5. Base-emitter voltage as a function of collector current; typical values

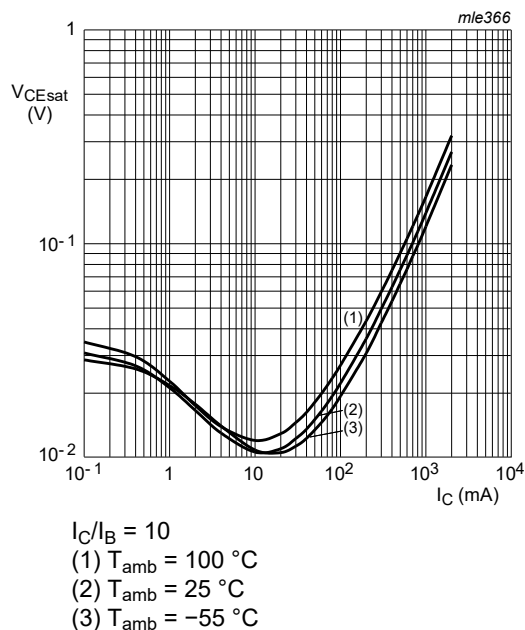


Fig. 6. Collector-emitter saturation voltage as a function of collector current; typical values

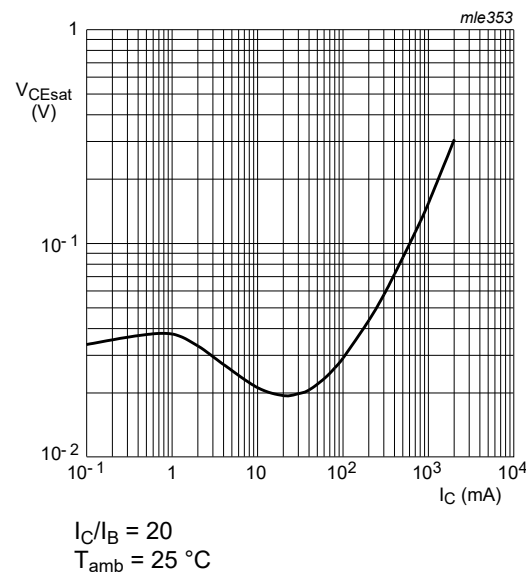


Fig. 7. Collector-emitter saturation voltage as a function of collector current; typical values

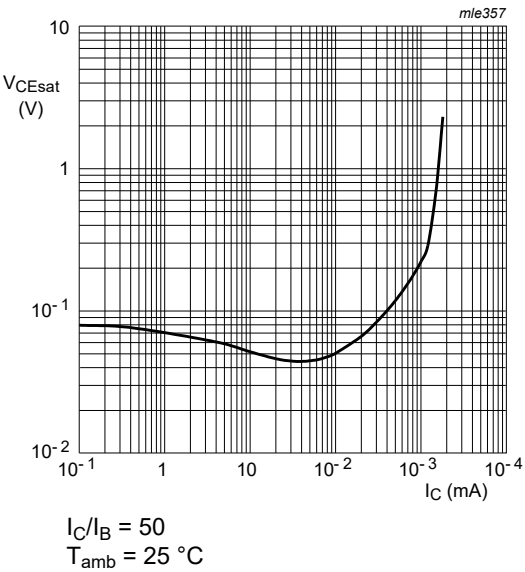


Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

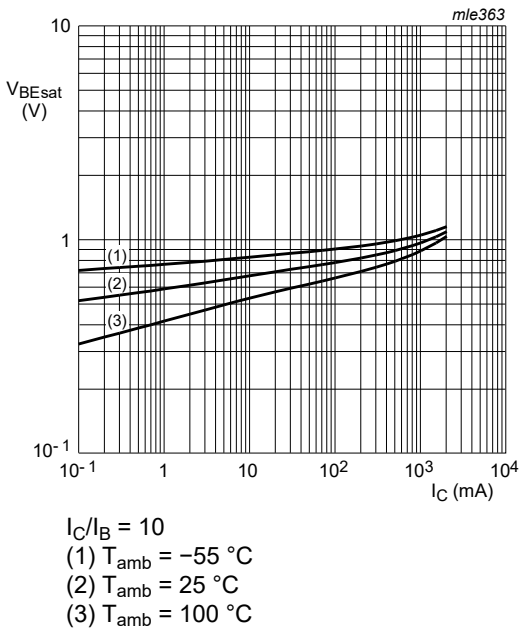


Fig. 9. Base-emitter saturation voltage as a function of collector current; typical values

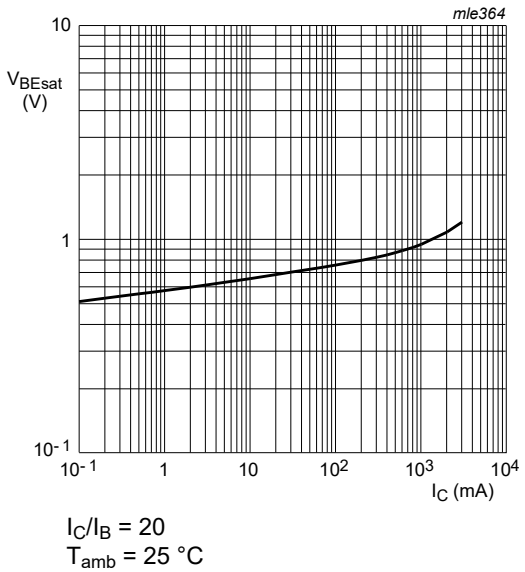


Fig. 10. Base-emitter saturation voltage as a function of collector current; typical values

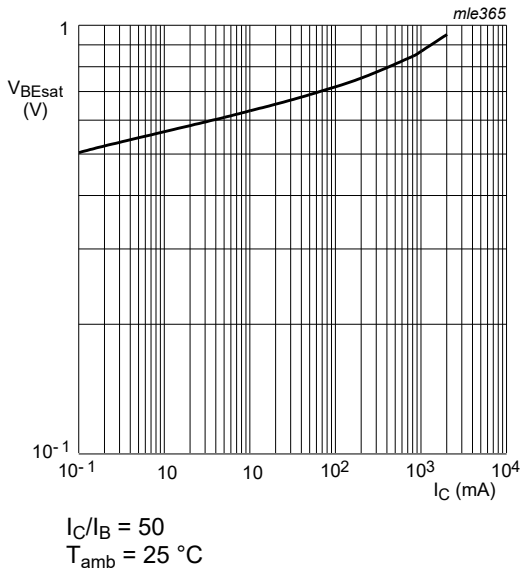
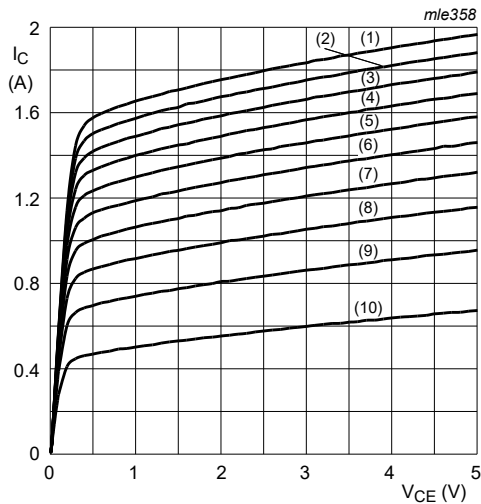
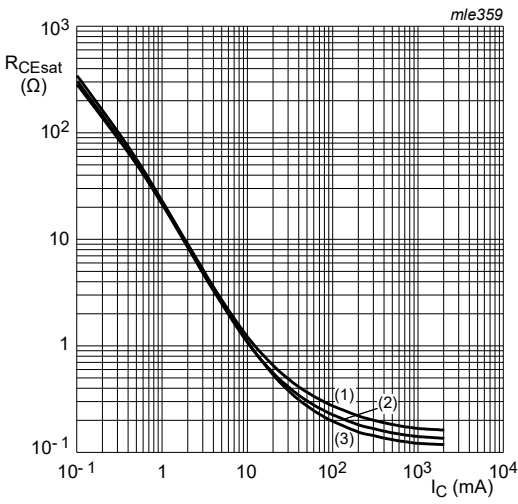


Fig. 11. Base-emitter saturation voltage as a function of collector current; typical values



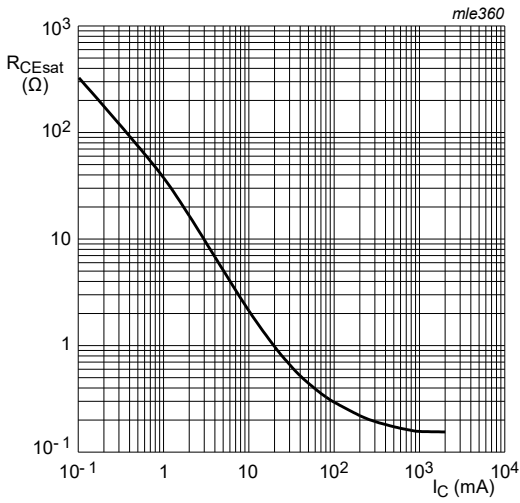
$T_{amb} = 25^\circ\text{C}$
(1) $I_B = 35.0\text{ mA}$
(2) $I_B = 31.5\text{ mA}$
(3) $I_B = 28.0\text{ mA}$
(4) $I_B = 24.5\text{ mA}$
(5) $I_B = 21.0\text{ mA}$
(6) $I_B = 17.5\text{ mA}$
(7) $I_B = 14.0\text{ mA}$
(8) $I_B = 10.5\text{ mA}$
(9) $I_B = 7.0\text{ mA}$
(10) $I_B = 3.5\text{ mA}$

Fig. 12. Collector current as a function of collector-emitter voltage; typical values



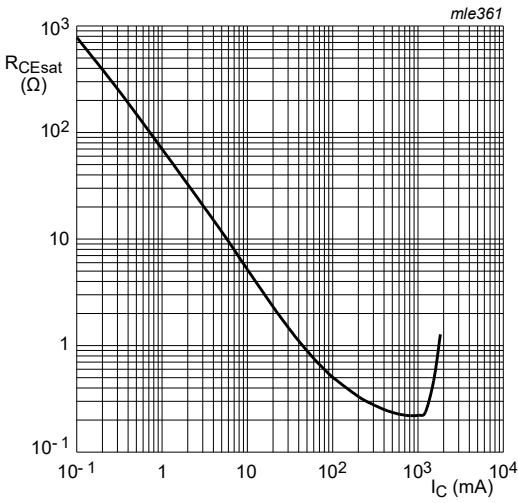
$I_C/I_B = 10$
(1) $T_{amb} = 100^\circ\text{C}$
(2) $T_{amb} = 25^\circ\text{C}$
(3) $T_{amb} = -55^\circ\text{C}$

Fig. 13. Collector-emitter equivalent on-resistance as a function of collector current; typical values



$I_C/I_B = 20$
 $T_{amb} = 25^\circ\text{C}$

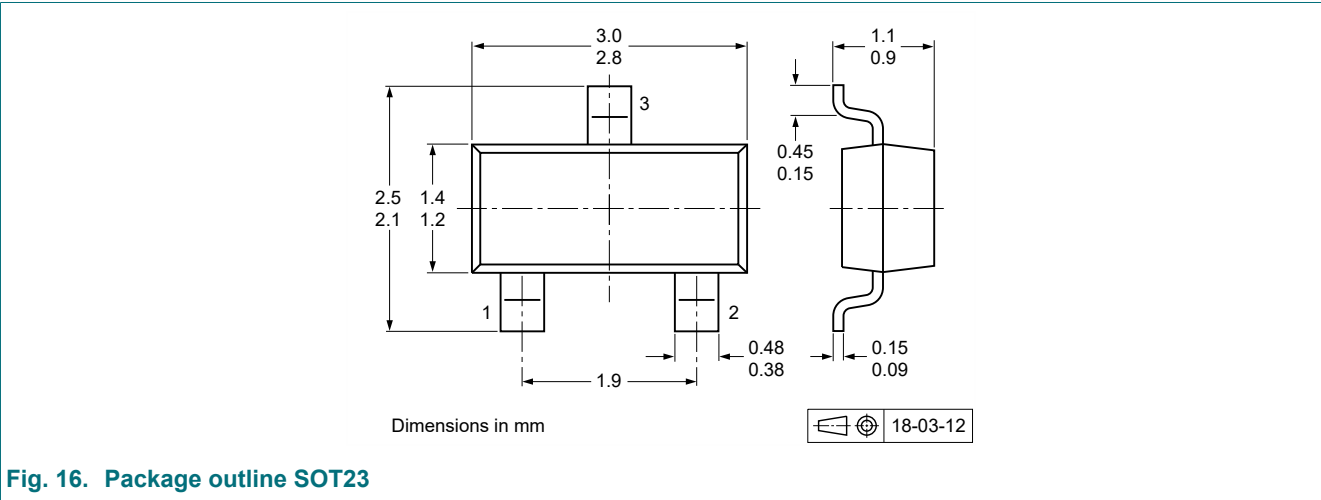
Fig. 14. Collector-emitter equivalent on-resistance as a function of collector current; typical values



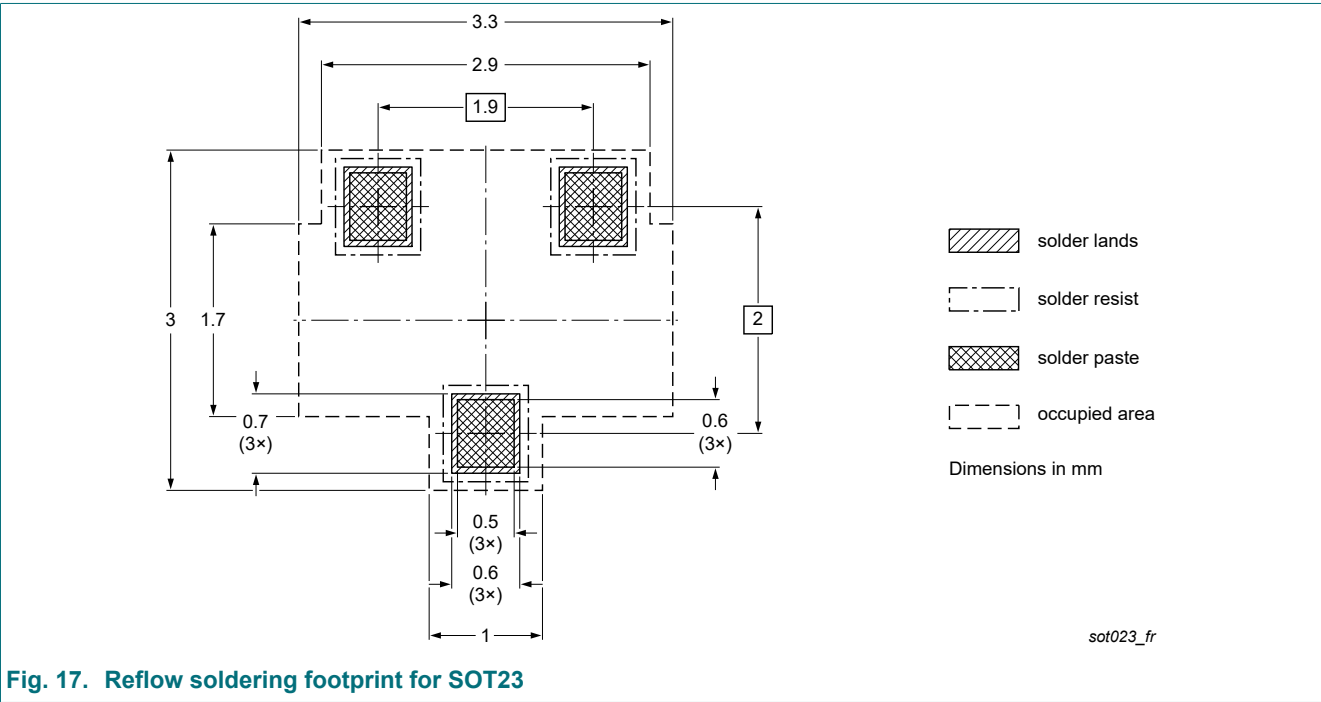
$I_C/I_B = 50$
 $T_{amb} = 25^\circ\text{C}$

Fig. 15. Collector-emitter equivalent on-resistance as a function of collector current; typical values

11. Package outline



12. Soldering



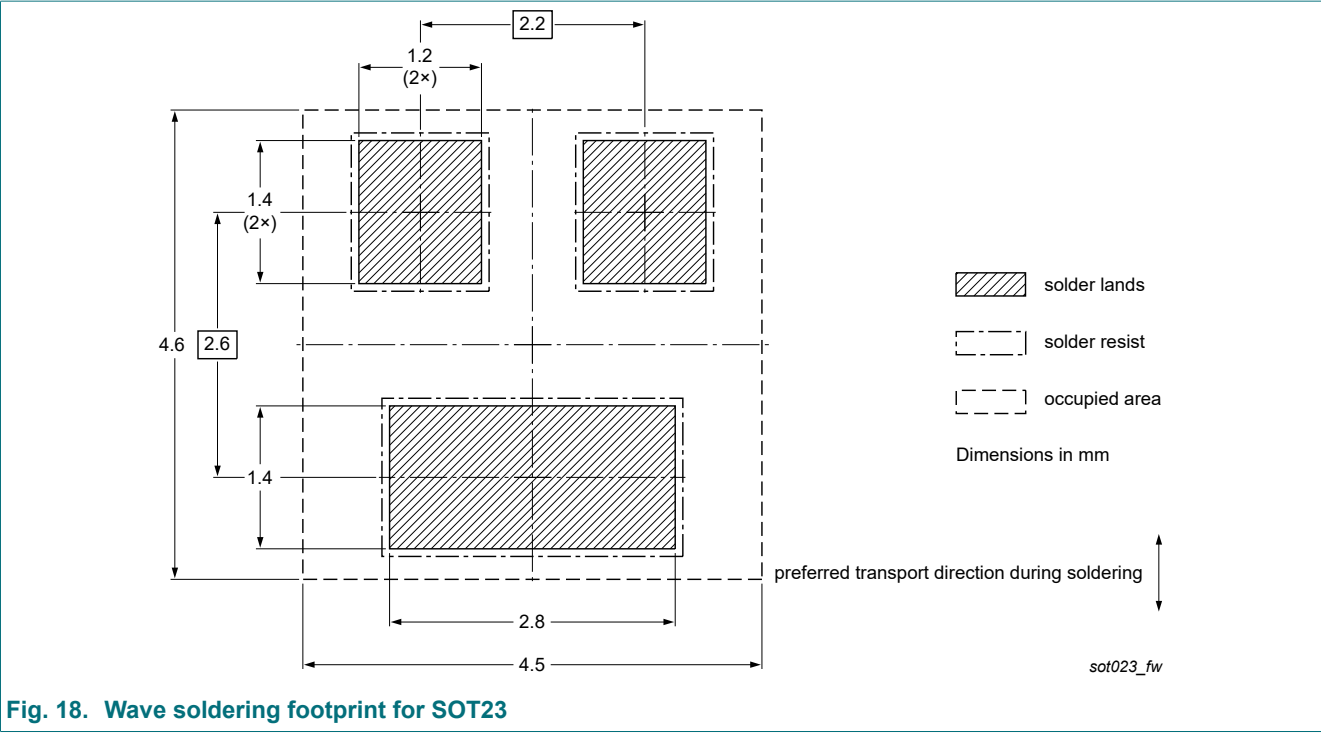


Fig. 18. Wave soldering footprint for SOT23

13. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS8110T v.5	20230928	Product data sheet	-	PBSS8110T v.4
Modifications:	• Characteristics, Fig. 12: Values of curves are corrected			
PBSS8110T v.4	20230101	Product data sheet	-	PBSS8110T v.3
PBSS8110T v.3	20220513	Product data sheet	-	PBSS8110T v.2
PBSS8110T v.2	20031222	Product data sheet	-	PBSS8110T v.1
PBSS8110T v.1	20030728	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

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Contents

1. General description..... 1

2. Features and benefits..... 1

3. Applications..... 1

4. Quick reference data..... 1

5. Pinning information.....2

6. Ordering information.....2

7. Marking.....2

8. Limiting values..... 2

9. Thermal characteristics..... 3

10. Characteristics.....5

11. Package outline..... 9

12. Soldering..... 9

13. Revision history..... 11

14. Legal information.....12

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