

ANTREX Electronics

Electronic Components Sourcing Information

Product Reference Information

Part Number: PBSS5350D,135
Manufacturer: Nexperia USA Inc.
Package: SC-74, SOT-457
Availability: Please confirm with sales

Product Page:

<https://antrexco.com/product/details/nexperia-usa-inc/pbss5350d-135.html>

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Note:

This PDF includes an ANTREX product reference cover page.

The original manufacturer datasheet follows from the next page.



PBSS5350D

50 V, 3 A PNP low V_{CEsat} transistor

1 January 2023

Product data sheet

1. General description

PNP low V_{CEsat} transistor in a small SOT457 (SC-74) Surface-Mounted Device (SMD) plastic package.

NPN complement: PBSS4350D

2. Features and benefits

- Low collector-emitter saturation voltage V_{CEsat}
- High current capability
- High efficiency due to less heat generation
- Smaller Printed-Circuit Board (PCB) area than for conventional transistors

3. Applications

- Supply line switching circuits
- Battery management applications
- DC-to-DC conversion

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	-50	V
I _C	collector current		-	-	-3	A
I _{CM}	peak collector current		-	-	-5	A
R _{CEsat}	collector-emitter saturation resistance	I _C = -2 A; I _B = -200 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	120	150	mΩ

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	C	collector	SC-74; TSOP6 (SOT457)	sym030
2	C	collector		
3	B	base		
4	E	emitter		
5	C	collector		
6	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS5350D	SC-74; TSOP6	plastic, surface-mounted package (SC-74; TSOP6); 6 leads	SOT457

7. Marking

Table 4. Marking codes

Type number	Marking code
PBSS5350D	53

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-60	V
V _{CEO}	collector-emitter voltage	open base		-	-50	V
V _{EBO}	emitter-base voltage	open collector		-	-6	V
I _C	collector current			-	-3	A
I _{CM}	peak collector current			-	-5	A
I _{BM}	peak base current			-	-1	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	600	mW
			[2]	-	750	mW
			[3]	-	1200	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for collector 1 cm².
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².
- [3] Device mounted on an FR4 4-layer PCB.

9. Thermal characteristics

Table 6. Thermal characteristics

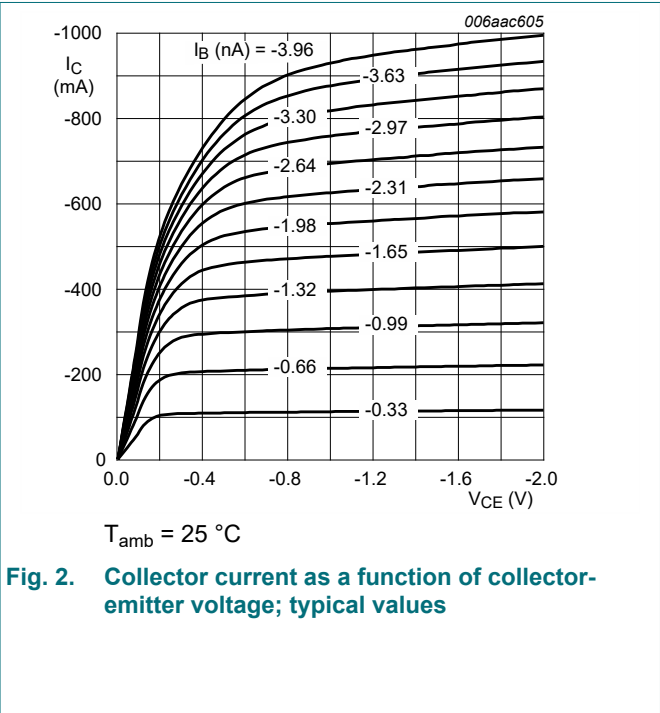
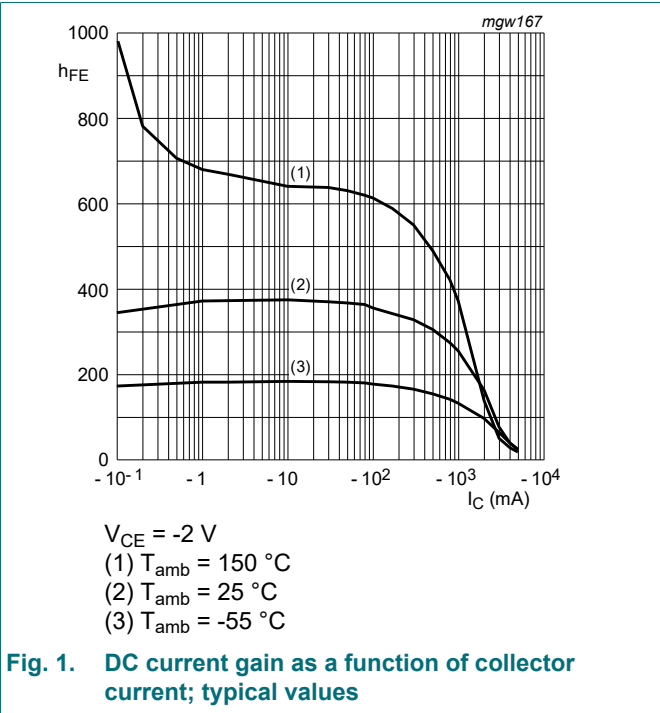
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	208	K/W
			[2]	-	-	160	K/W
		pulsed; t _p ≤ 50 ms; δ ≤ 0.5.; in free air	[2]	-	-	100	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I_{CBO}	collector-base cut-off current	$V_{CB} = -50\text{ V}; I_E = 0\text{ A}; T_{amb} = 25\text{ }^{\circ}\text{C}$	-	-	-100	nA
		$V_{CB} = -50\text{ V}; I_E = 0\text{ A}; T_j = 150\text{ }^{\circ}\text{C}$	-	-	-50	μA
I_{EBO}	emitter-base cut-off current	$V_{EB} = -5\text{ V}; I_C = 0\text{ A}; T_{amb} = 25\text{ }^{\circ}\text{C}$	-	-	-100	nA
h_{FE}	DC current gain	$V_{CE} = -2\text{ V}; I_C = -500\text{ mA}; T_{amb} = 25\text{ }^{\circ}\text{C}$	200	-	-	
		$V_{CE} = -2\text{ V}; I_C = -1\text{ A}; \text{pulsed}; t_p \leq 300\text{ }\mu\text{s}; \delta \leq 0.02; T_{amb} = 25\text{ }^{\circ}\text{C}$	200	-	-	
		$V_{CE} = -2\text{ V}; I_C = -2\text{ A}; \text{pulsed}; t_p \leq 300\text{ }\mu\text{s}; \delta \leq 0.02; T_{amb} = 25\text{ }^{\circ}\text{C}$	100	-	-	
V_{CEsat}	collector-emitter saturation voltage	$I_C = -500\text{ mA}; I_B = -50\text{ mA}; T_{amb} = 25\text{ }^{\circ}\text{C}$	-	-	-100	mV
		$I_C = -1\text{ A}; I_B = -50\text{ mA}; T_{amb} = 25\text{ }^{\circ}\text{C}$	-	-	-180	mV
		$I_C = -2\text{ A}; I_B = -200\text{ mA}; \text{pulsed}; t_p \leq 300\text{ }\mu\text{s}; \delta \leq 0.02; T_{amb} = 25\text{ }^{\circ}\text{C}$	-	-	-300	mV
R_{CEsat}	collector-emitter saturation resistance		-	120	150	m Ω
V_{BEsat}	base-emitter saturation voltage		-	-	-1.2	V
V_{BEon}	base-emitter turn-on voltage	$V_{CE} = -2\text{ V}; I_C = -1\text{ A}; \text{pulsed}; t_p \leq 300\text{ }\mu\text{s}; \delta \leq 0.02; T_{amb} = 25\text{ }^{\circ}\text{C}$	-	-	-1.1	V
f_T	transition frequency	$V_{CE} = -5\text{ V}; I_C = -100\text{ mA}; f = 100\text{ MHz}; T_{amb} = 25\text{ }^{\circ}\text{C}$	100	-	-	MHz
C_c	collector capacitance	$V_{CB} = -10\text{ V}; I_E = 0\text{ A}; i_e = 0\text{ A}; f = 1\text{ MHz}; T_{amb} = 25\text{ }^{\circ}\text{C}$	-	-	40	pF



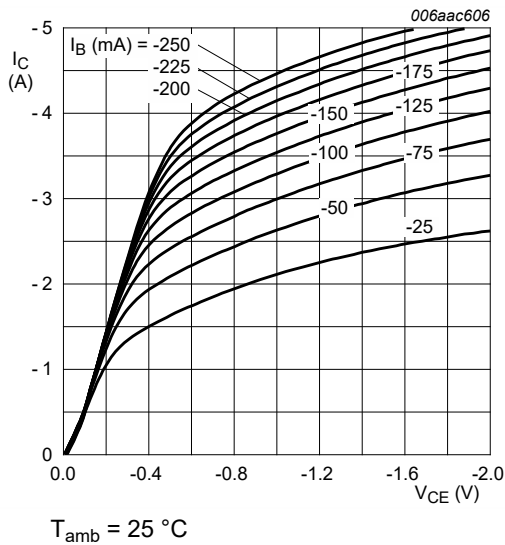


Fig. 3. Collector current as a function of collector-emitter voltage; typical values

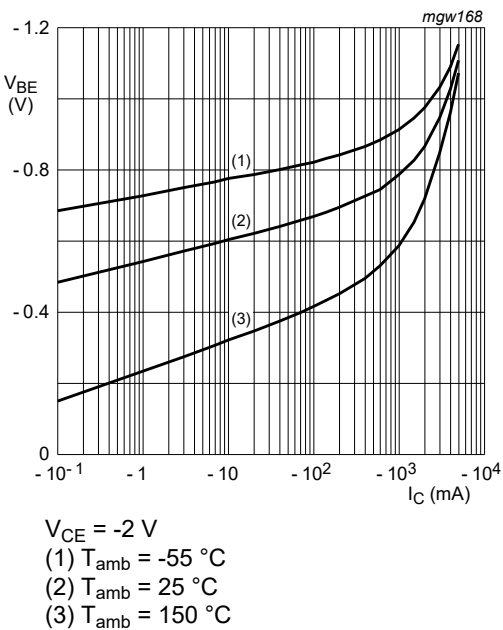


Fig. 4. Base-emitter voltage as a function of collector current; typical values

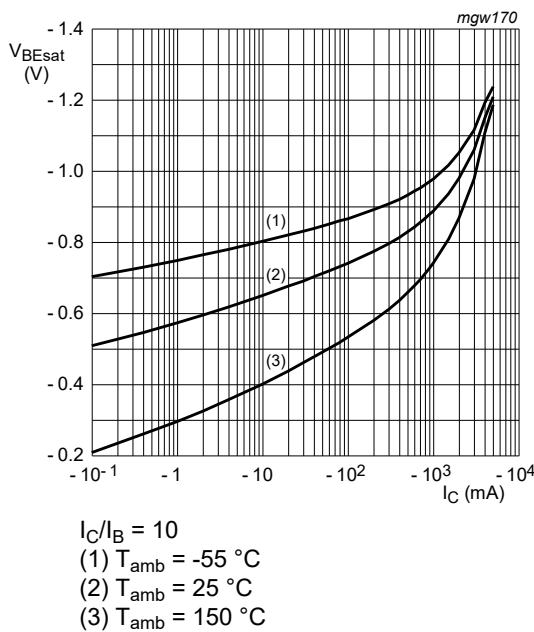


Fig. 5. Base-emitter saturation voltage as a function of collector current; typical values

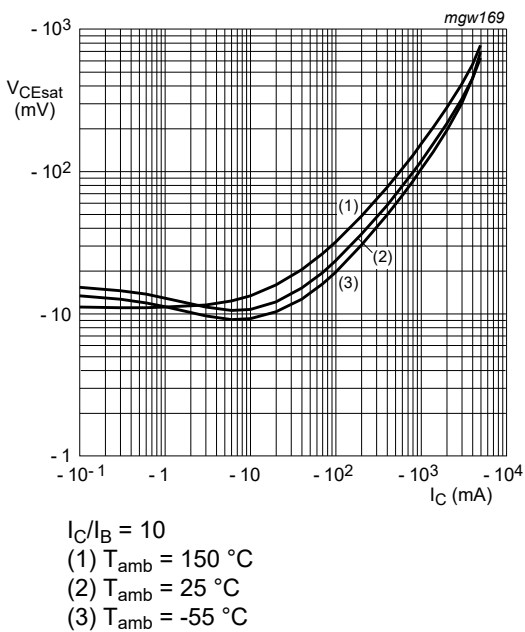
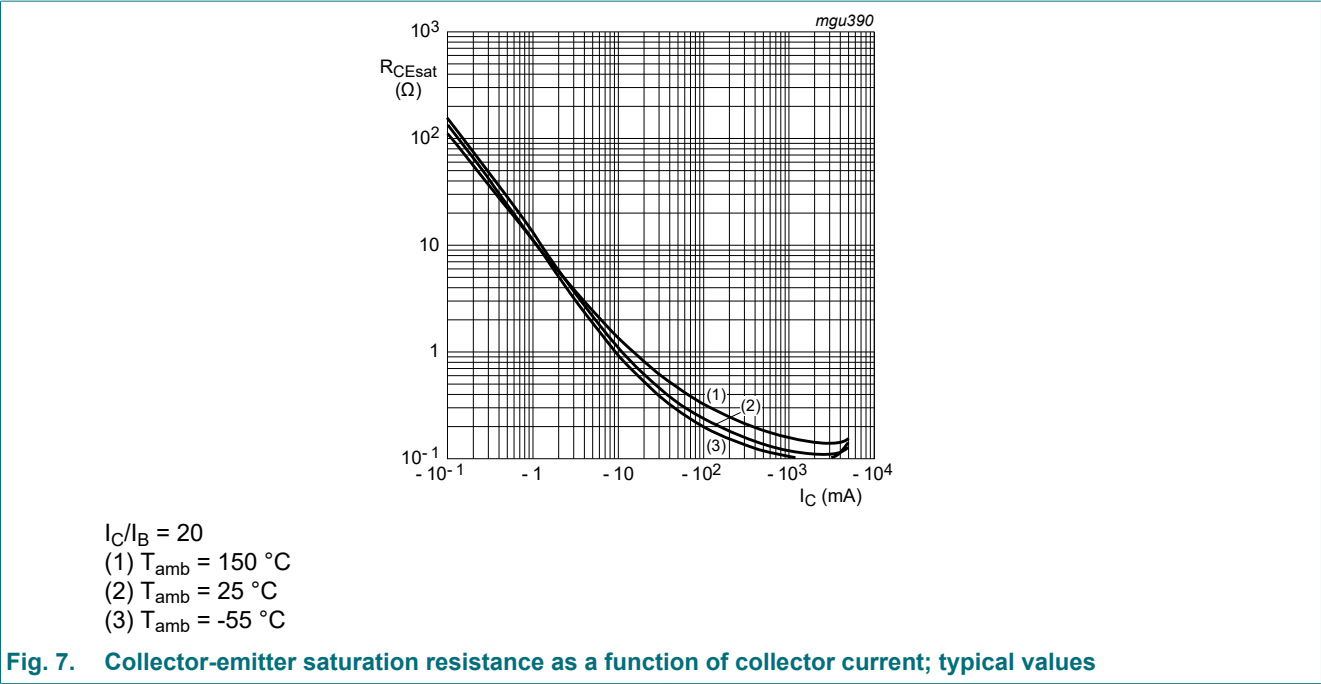
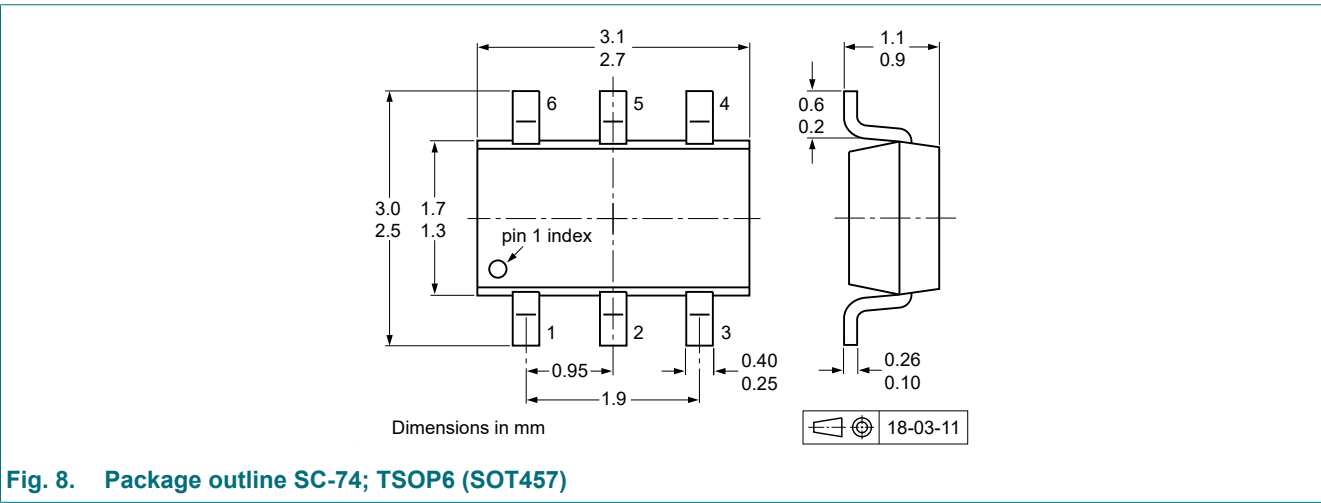


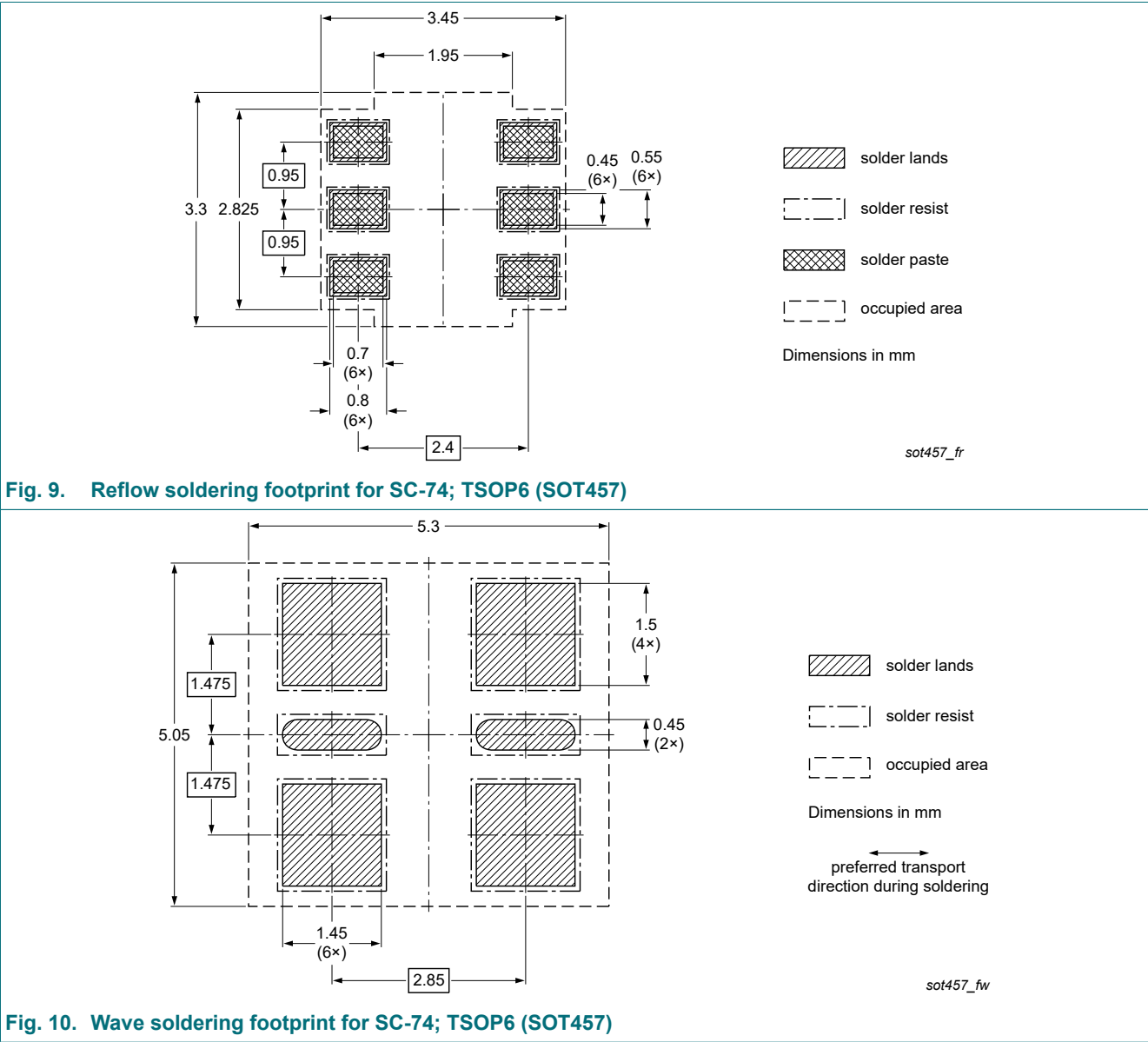
Fig. 6. Collector-emitter saturation voltage as a function of collector current; typical values



11. Package outline



12. Soldering



13. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS5350D v.7	20230101	Product data sheet	-	PBSS5350D v.6
Modifications:	Product changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s).			
PBSS5350D v.6	20110628	Product data sheet	-	PBSS5350D v.5
PBSS5350D v.5	20110323	Product data sheet	-	PBSS5350D v.4
PBSS5350D v.4	20011113	Product specification	-	PBSS5350D v.3
PBSS5350D v.3	20010713	Product specification	-	PBSS5350D v.2
PBSS5350D v.2	20010126	Product specification	-	PBSS5350D v.1
PBSS5350D v.1	20000308	Product specification	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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