

ANTREX Electronics

Electronic Components Sourcing Information

Product Reference Information

Part Number: PBSS5320D,125
Manufacturer: Nexperia USA Inc.
Package: SC-74, SOT-457
Availability: Please confirm with sales

Product Page:

<https://antrexco.com/product/details/nexperia-usa-inc/pbss5320d-125.html>

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Contact Information

Email: info@antrexco.com

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Note:

This PDF includes an ANTREX product reference cover page.

The original manufacturer datasheet follows from the next page.



PBSS5320D

20 V low V_{CEsat} PNP transistor

25 February 2026

Product data sheet

1. General description

PNP low V_{CEsat} transistor in a SOT457 (SC-74) plastic package.

2. Features and benefits

- Low collector-emitter saturation voltage
- High current capability
- Improved device reliability due to reduced heat generation
- AEC-Q101 qualified

3. Applications

- Supply line switching circuits
- Battery management applications
- DC/DC converter applications
- Strobe flash units
- Heavy duty battery powered equipment (motor and lamp drivers)

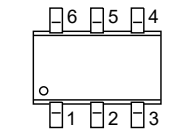
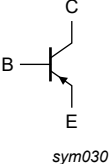
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	-20	V
I_C	collector current		-	-	-3	A
I_{CM}	peak collector current		-	-	-5	A
R_{CEsat}	collector-emitter saturation resistance	$I_C = -3\text{ A}$; $I_B = -300\text{ mA}$; pulsed; $t_p \leq 300\text{ }\mu\text{s}$; $\delta \leq 0.02$; $T_{amb} = 25\text{ }^\circ\text{C}$	-	85	133	$\text{m}\Omega$

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	C	collector	 TSOP6 (SOT457)	 sym030
2	C	collector		
3	B	base		
4	E	emitter		
5	C	collector		
6	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS5320D	TSOP6	plastic, surface-mounted package (SC-74; TSOP6); 6 leads	SOT457

7. Marking

Table 4. Marking codes

Type number	Marking code
PBSS5320D	52

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-20	V
V _{CEO}	collector-emitter voltage	open base		-	-20	V
V _{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-3	A
I _{CM}	peak collector current			-	-5	A
I _B	base current			-	-500	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	600	mW
			[2]	-	750	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	208	K/W
			[2]	-	-	160	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

10. Characteristics

Table 7. Characteristics

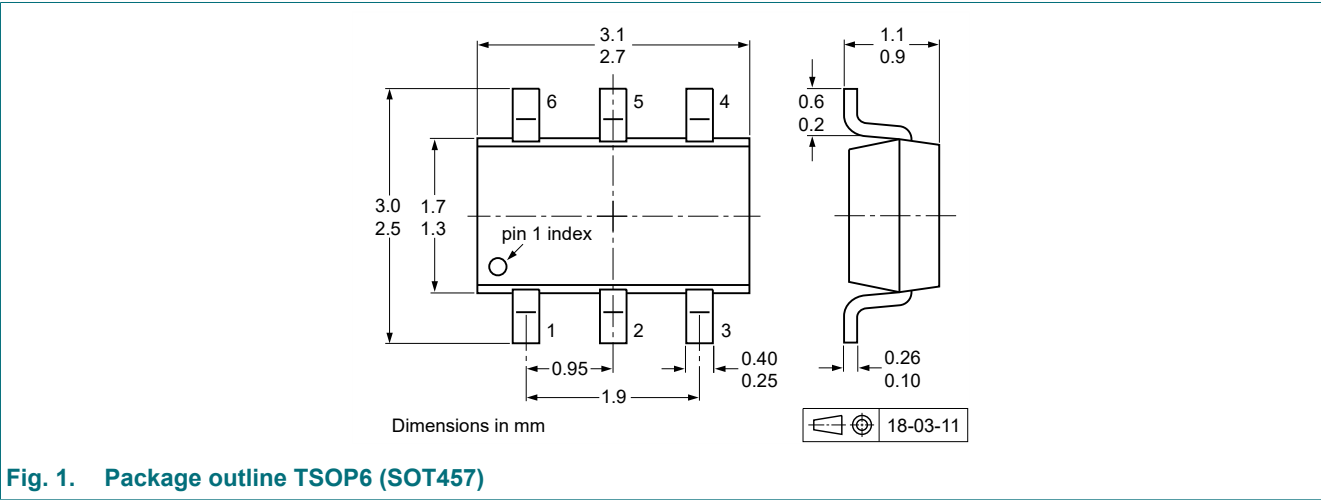
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
I_{CBO}	collector-base cut-off current	$V_{CB} = -20\text{ V}; I_E = 0\text{ A}; T_{amb} = 25\text{ °C}$		-	-	-100	nA
		$V_{CB} = -20\text{ V}; I_E = 0\text{ A}; T_j = 150\text{ °C}$		-	-	-50	μA
I_{EBO}	emitter-base cut-off current	$V_{EB} = -5\text{ V}; I_C = 0\text{ A}; T_{amb} = 25\text{ °C}$		-	-	-100	nA
h_{FE}	DC current gain	$V_{CE} = -2\text{ V}; I_C = -100\text{ mA}; T_{amb} = 25\text{ °C}$		200	-	-	
		$V_{CE} = -2\text{ V}; I_C = -500\text{ mA}; T_{amb} = 25\text{ °C}$		200	-	-	
		$V_{CE} = -2\text{ V}; I_C = -1\text{ A}; \text{pulsed}; t_p \leq 300\text{ μs}; \delta \leq 0.02; T_{amb} = 25\text{ °C}$		200	-	-	
		$V_{CE} = -2\text{ V}; I_C = -2\text{ A}; \text{pulsed}; t_p \leq 300\text{ μs}; \delta \leq 0.02; T_{amb} = 25\text{ °C}$		150	-	-	
V_{CEsat}	collector-emitter saturation voltage	$I_C = -500\text{ mA}; I_B = -5\text{ mA}; T_{amb} = 25\text{ °C}$		-	-	-130	mV
		$I_C = -500\text{ mA}; I_B = -50\text{ mA}; T_{amb} = 25\text{ °C}$		-	-	-80	mV
		$I_C = -1\text{ A}; I_B = -50\text{ mA}; T_{amb} = 25\text{ °C}$		-	-	-160	mV
		$I_C = -2\text{ A}; I_B = -20\text{ mA}; \text{pulsed}; t_p \leq 300\text{ μs}; \delta \leq 0.02; T_{amb} = 25\text{ °C}$		-	-	-400	mV
		$I_C = -2\text{ A}; I_B = -200\text{ mA}; \text{pulsed}; t_p \leq 300\text{ μs}; \delta \leq 0.02; T_{amb} = 25\text{ °C}$		-	-	-250	mV
		$I_C = -3\text{ A}; I_B = -300\text{ mA}; \text{pulsed}; t_p \leq 300\text{ μs}; \delta \leq 0.02; T_{amb} = 25\text{ °C}$		-	-	-400	mV
R_{CEsat}	collector-emitter saturation resistance			-	85	133	mΩ
V_{BEsat}	base-emitter saturation voltage	$I_C = -2\text{ A}; I_B = -200\text{ mA}; \text{pulsed}; t_p \leq 300\text{ μs}; \delta \leq 0.02; T_{amb} = 25\text{ °C}$		-	-	-1.2	V
V_{BEon}	base-emitter turn-on voltage	$V_{CE} = -2\text{ V}; I_C = -1\text{ A}; \text{pulsed}; t_p \leq 300\text{ μs}; \delta \leq 0.02; T_{amb} = 25\text{ °C}$		-1.2	-	-	V
f_T	transition frequency	$V_{CE} = -10\text{ V}; I_C = -200\text{ mA}; f = 100\text{ MHz}; T_{amb} = 25\text{ °C}$		100	-	-	MHz
C_c	collector capacitance	$V_{CB} = -10\text{ V}; I_E = 0\text{ A}; i_e = 0\text{ A}; f = 1\text{ MHz}; T_{amb} = 25\text{ °C}$		-	-	50	pF

11. Test information

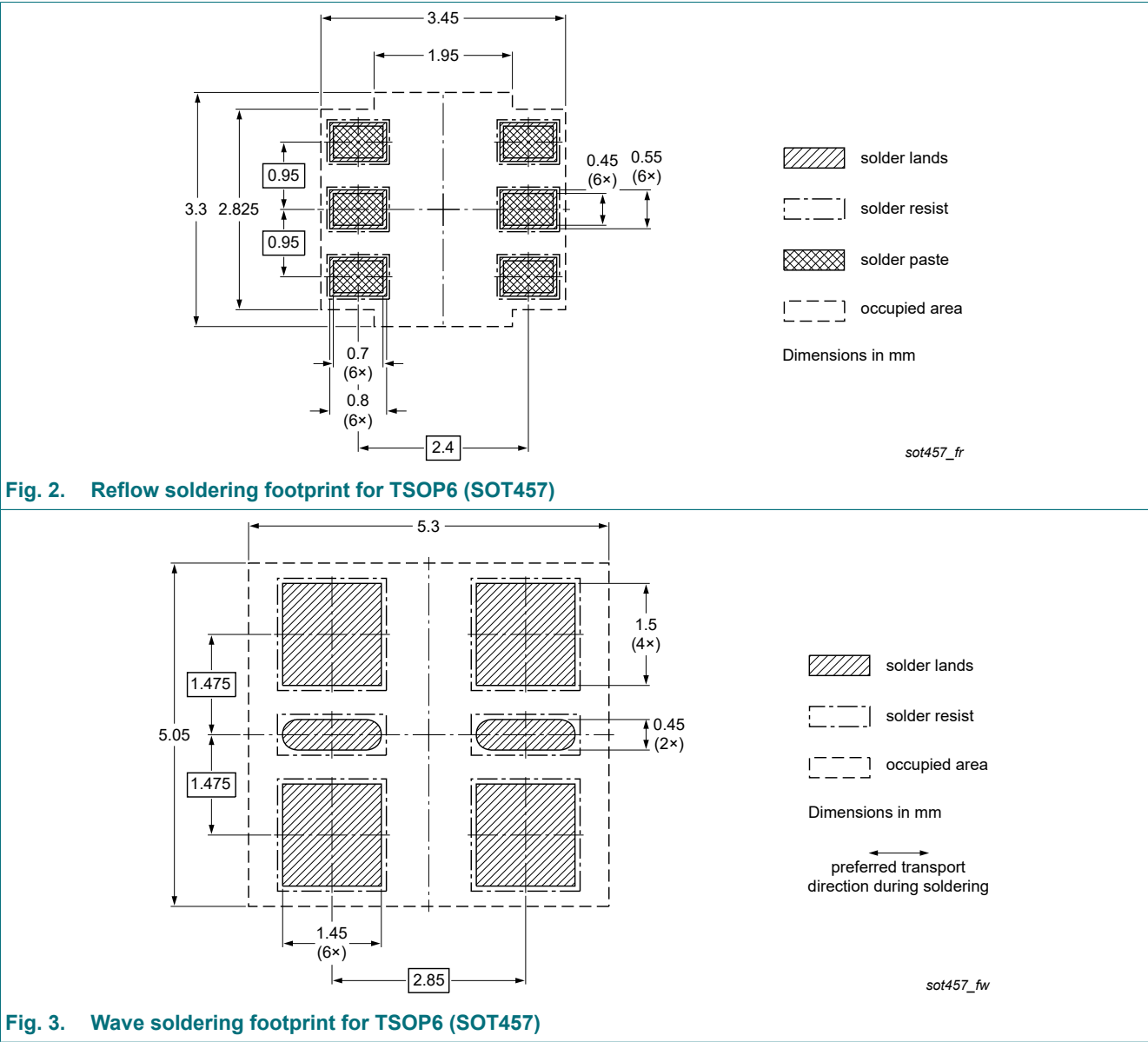
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS5320D v.2	20260225	Product data sheet	-	PBSS5320D v.1
Modifications:	- The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. - Legal texts have been adapted to the new company name where appropriate.			
PBSS5320D v.1	20020612	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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Contents

1. General description..... 1

2. Features and benefits..... 1

3. Applications..... 1

4. Quick reference data..... 1

5. Pinning information.....2

6. Ordering information.....2

7. Marking.....2

8. Limiting values..... 2

9. Thermal characteristics..... 3

10. Characteristics..... 3

11. Test information..... 3

12. Package outline..... 4

13. Soldering..... 5

14. Revision history.....6

15. Legal information.....7

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