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Electronic Components Sourcing Information

Product Reference Information

Part Number:	BSS84AKQBZ
Manufacturer:	Nexperia USA Inc.
Package:	Please confirm with sales
Availability:	Please confirm with sales

Product Page:

<https://antrexco.com/product/details/nexperia-usa-inc/bss84akqbz.html>

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Note:

This PDF includes an ANTREX product reference cover page.

The original manufacturer datasheet follows from the next page.



BSS84AKQB

50 V, P-channel Trench MOSFET

13 July 2021

Product data sheet

1. General description

P-channel enhancement mode Field-Effect Transistor (FET) in an ultra small DFN1110D-3 (SOT8015) leadless Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Logic-level compatible
- Side wettable flanks for optical solder inspection
- Ultra small and leadless SMD plastic package: 1.1 x 1 x 0.48 mm
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection > 1 kV HBM (Class H1C)
- AEC-Q101 qualified

3. Applications

- Relay driver
- High-speed line driver
- High-side load switch
- Switching circuits

4. Quick reference data

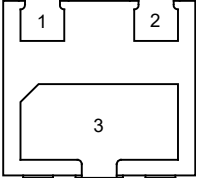
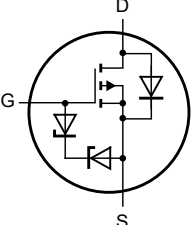
Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{DS}	drain-source voltage	$T_j = 25\text{ }^{\circ}\text{C}$		-	-	-50	V
V_{GS}	gate-source voltage			-20	-	12	V
I_D	drain current	$V_{GS} = -10\text{ V}; T_{amb} = 25\text{ }^{\circ}\text{C}$	[1]	-	-	-270	mA
Static characteristics							
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = -10\text{ V}; I_D = -100\text{ mA}; T_j = 25\text{ }^{\circ}\text{C}$		-	3.8	7.5	Ω

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	 DFN1110D-3 (SOT8015)	 017aaa259
2	S	source		
3	D	drain		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BSS84AKQB	DFN1110D-3	plastic, leadless extremely thin small outline package with side-wettable flanks (SWF); 3 terminals; 0.65 mm pitch; 1.1 mm x 1 mm x 0.48 mm body	SOT8015

7. Marking

Table 4. Marking codes

Type number	Marking code
BSS84AKQB	B 9

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-50	V
V _{GS}	gate-source voltage			-20	12	V
V _{GSMlim}	peak gate-source voltage	δ _{factor} = 0.1; t _p = 50 μs		-20	20	V
I _D	drain current	V _{GS} = -10 V; T _{amb} = 25 °C	[1]	-	-270	mA
		V _{GS} = -10 V; T _{amb} = 100 °C	[1]	-	-170	mA
I _{DM}	peak drain current	T _{amb} = 25 °C; single pulse; t _p ≤ 10 μs		-	-1.1	A
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	420	mW
			[1]	-	960	mW
		T _{sp} = 25 °C		-	4.2	W
T _j	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drain diode						
I _S	source current	T _{amb} = 25 °C	[1]	-	-115	mA
ESD maximum rating						
V _{ESD}	electrostatic discharge voltage	HBM		-	1000	V
Avalanche ruggedness						
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	T _{j(initial)} = 25 °C; I _D = 0.05 A; DUT in avalanche (unclamped)		-	1.2	mJ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

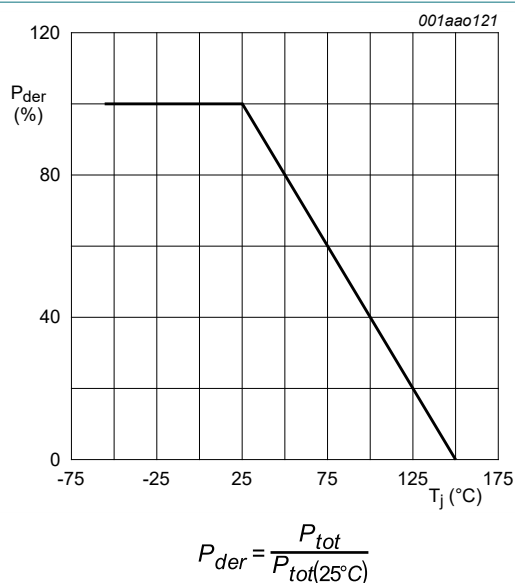


Fig. 1. Normalized total power dissipation as a function of junction temperature

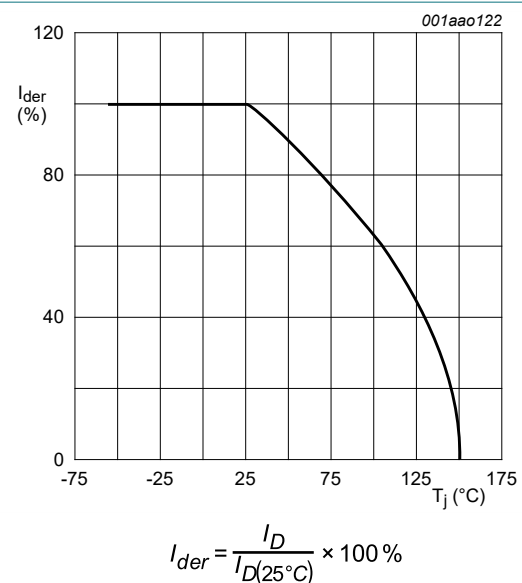


Fig. 2. Normalized continuous drain current as a function of junction temperature

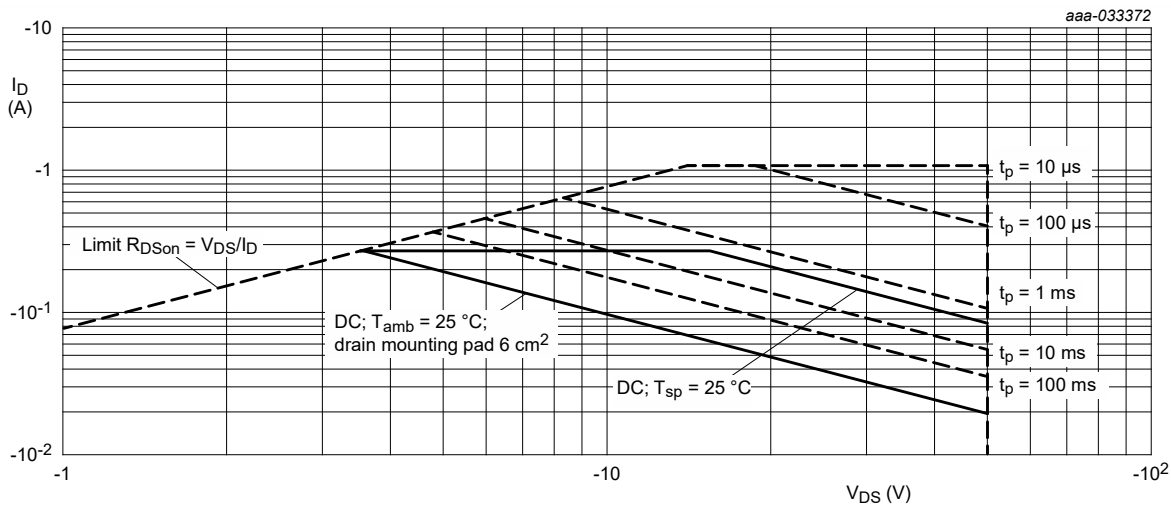


Fig. 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	245	300	K/W
			[2]	-	110	130	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	25	30	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².

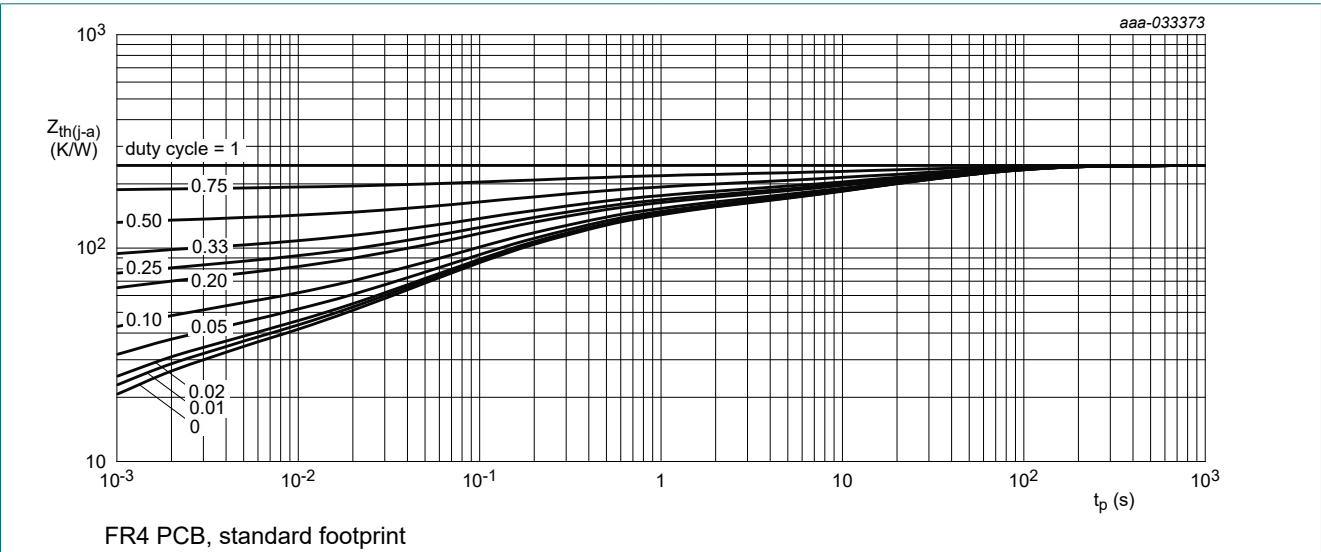


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

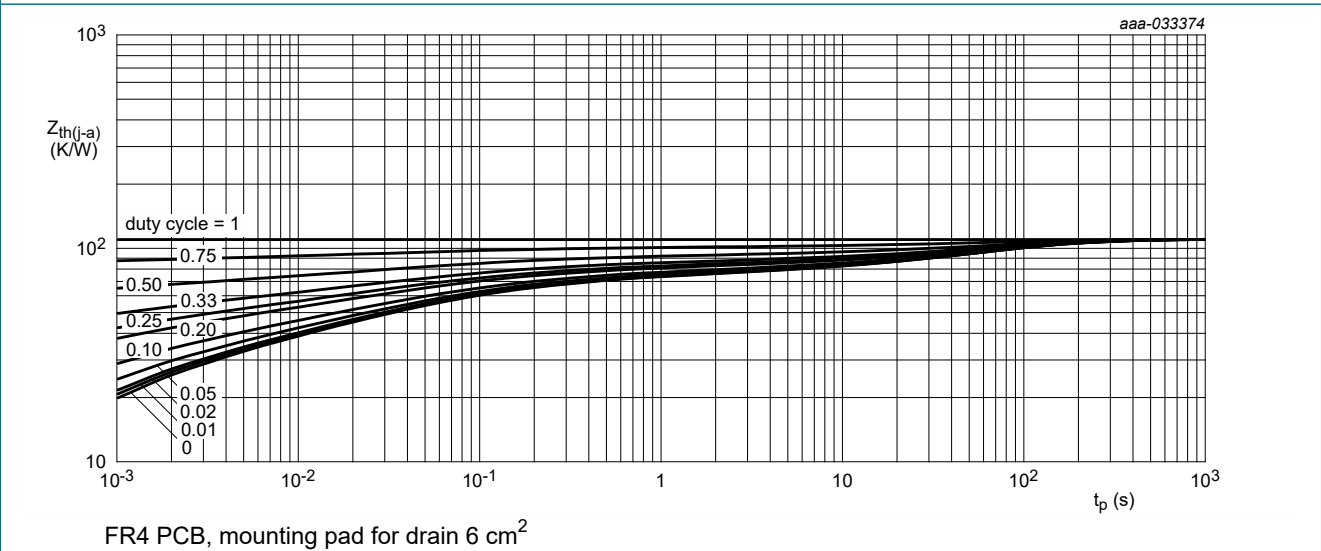


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Static characteristics							
V _{(BR)DSS}	drain-source breakdown voltage	I _D = -250 μA; V _{GS} = 0 V; T _j = 25 °C		-50	-	-	V
V _{GSt}	gate-source threshold voltage	I _D = -250 μA; V _{DS} =V _{GS} ; T _j = 25 °C		-1.1	-1.6	-2.1	V
I _{DSS}	drain leakage current	V _{DS} = -50 V; V _{GS} = 0 V; T _j = 25 °C		-	-	-1	μA
I _{GSS}	gate leakage current	V _{GS} = -20 V; V _{DS} = 0 V; T _j = 25 °C		-	-	-10	μA
		V _{GS} = 20 V; V _{DS} = 0 V; T _j = 25 °C		-	-	10	μA
R _{DSon}	drain-source on-state resistance	V _{GS} = -10 V; I _D = -100 mA; T _j = 25 °C		-	3.8	7.5	Ω
		V _{GS} = -10 V; I _D = -100 mA; T _j = 150 °C		-	6.7	13	Ω
		V _{GS} = -4.5 V; I _D = -100 mA; T _j = 25 °C		-	5	8.5	Ω
g _{fs}	forward transconductance	V _{DS} = -5 V; I _D = -100 mA; T _j = 25 °C		-	0.2	-	S
Dynamic characteristics							
Q _{G(tot)}	total gate charge	V _{DS} = -25 V; I _D = -0.1 A; V _{GS} = -10 V; T _j = 25 °C		-	0.4	0.6	nC
Q _{GS}	gate-source charge			-	0.1	-	nC
Q _{GD}	gate-drain charge			-	0.1	-	nC
C _{iss}	input capacitance	V _{DS} = -25 V; f = 1 MHz; V _{GS} = 0 V; T _j = 25 °C		-	23.2	-	pF
C _{oss}	output capacitance			-	3.5	-	pF
C _{rss}	reverse transfer capacitance			-	1.1	-	pF
t _{d(on)}	turn-on delay time	V _{DS} = -25 V; I _D = -0.1 A; V _{GS} = -10 V; R _{G(ext)} = 6 Ω; T _j = 25 °C		-	6	-	ns
t _r	rise time			-	9	-	ns
t _{d(off)}	turn-off delay time			-	40	-	ns
t _f	fall time			-	22	-	ns
Source-drain diode							
V _{SD}	source-drain voltage	I _S = -0.115 A; V _{GS} = 0 V; T _j = 25 °C		-	-0.7	-1.2	V
t _{rr}	reverse recovery time	I _S = -0.1 A; dI _S /dt = 100 A/μs;		-	43	-	ns
Q _r	recovered charge	V _{GS} = -10 V; V _{DS} = -25 V; T _j = 25 °C		-	39	-	nC

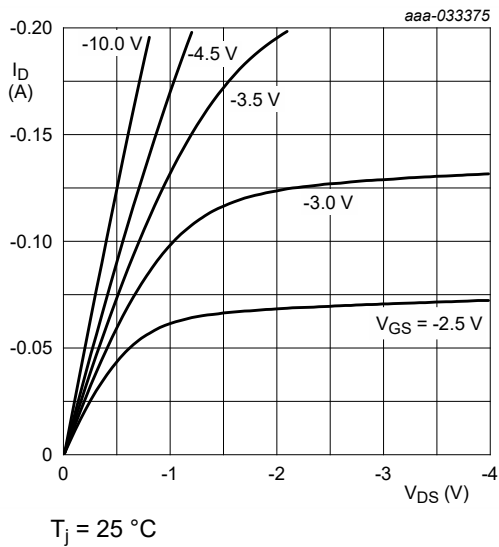


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values

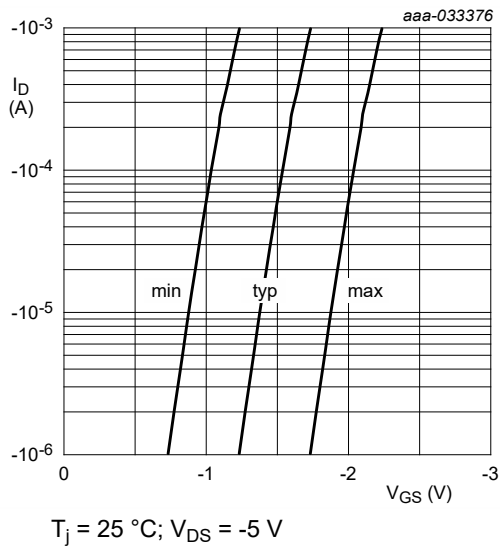


Fig. 7. Sub-threshold drain current as a function of gate-source voltage

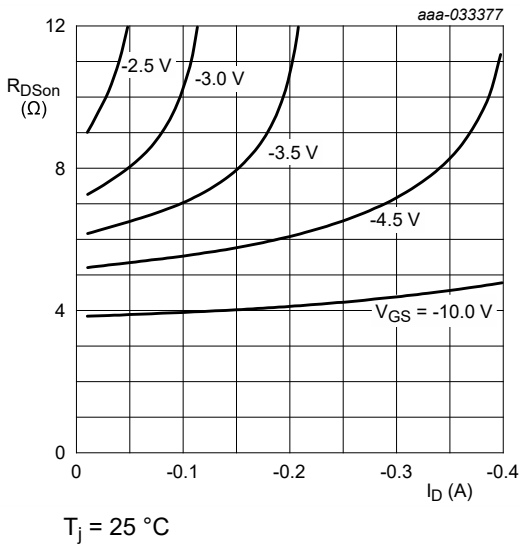


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

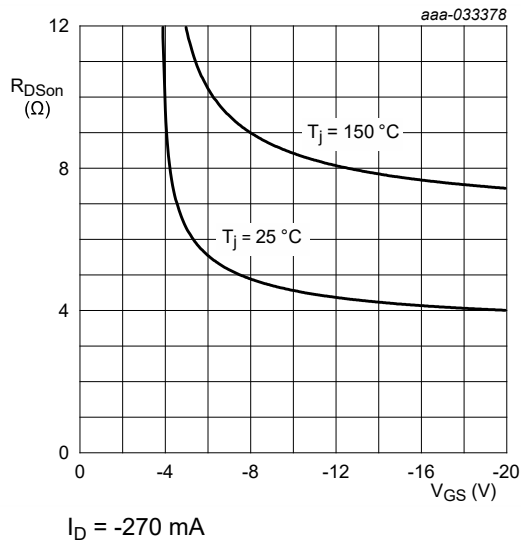


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

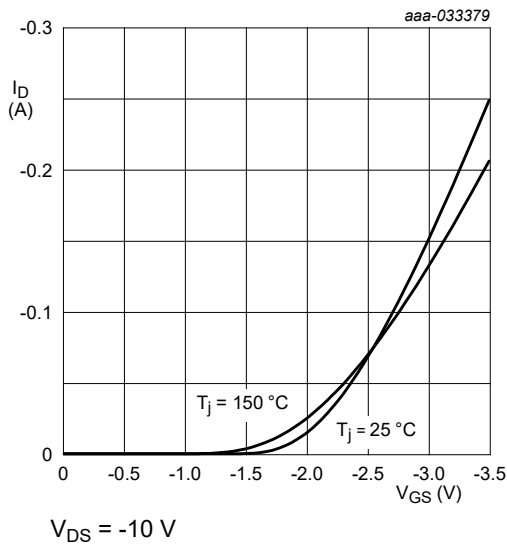


Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

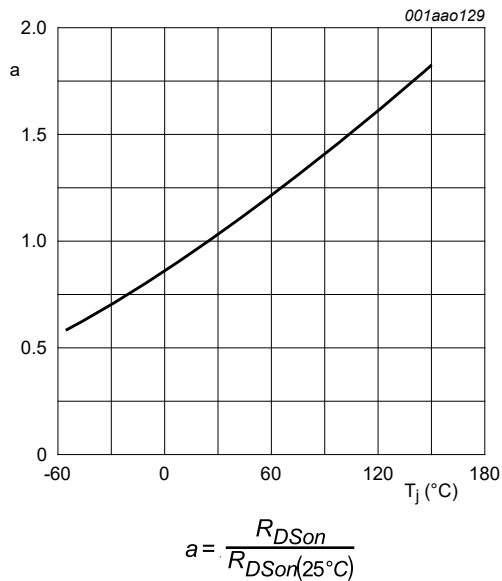


Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

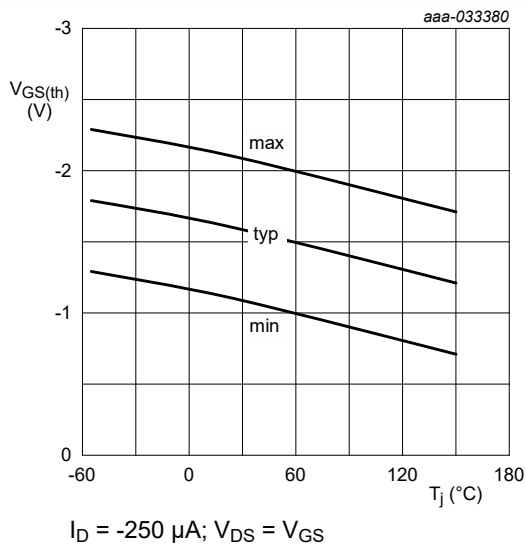


Fig. 12. Gate-source threshold voltage as a function of junction temperature

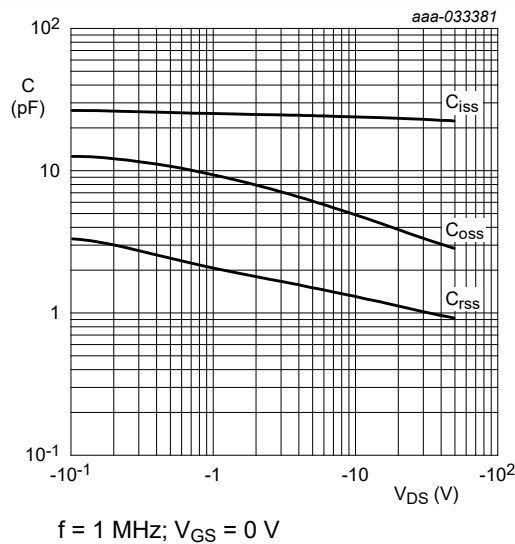


Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

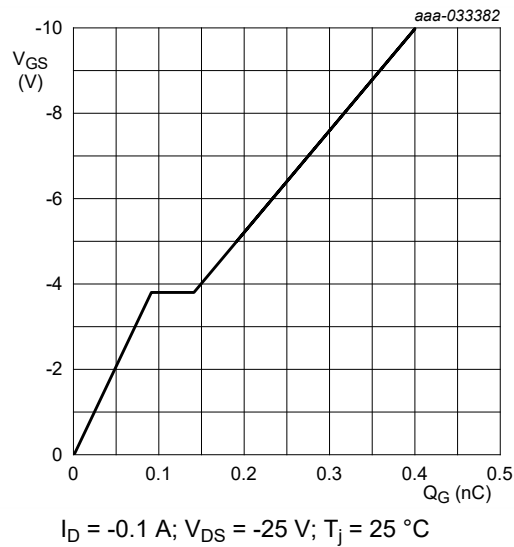


Fig. 14. Gate-source voltage as a function of gate charge; typical values

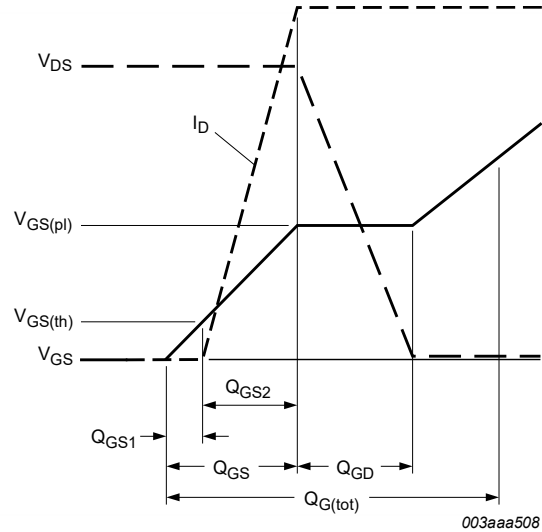


Fig. 15. Gate charge waveform definitions

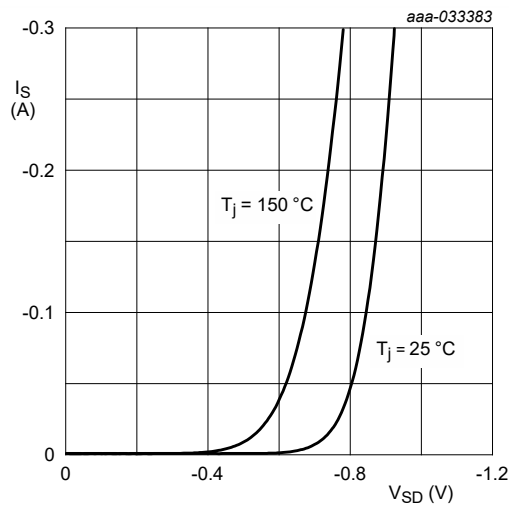


Fig. 16. Source current as a function of source-drain voltage; typical values

11. Test information

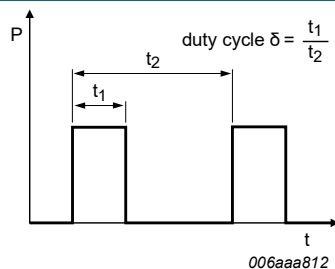


Fig. 17. Duty cycle definition

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

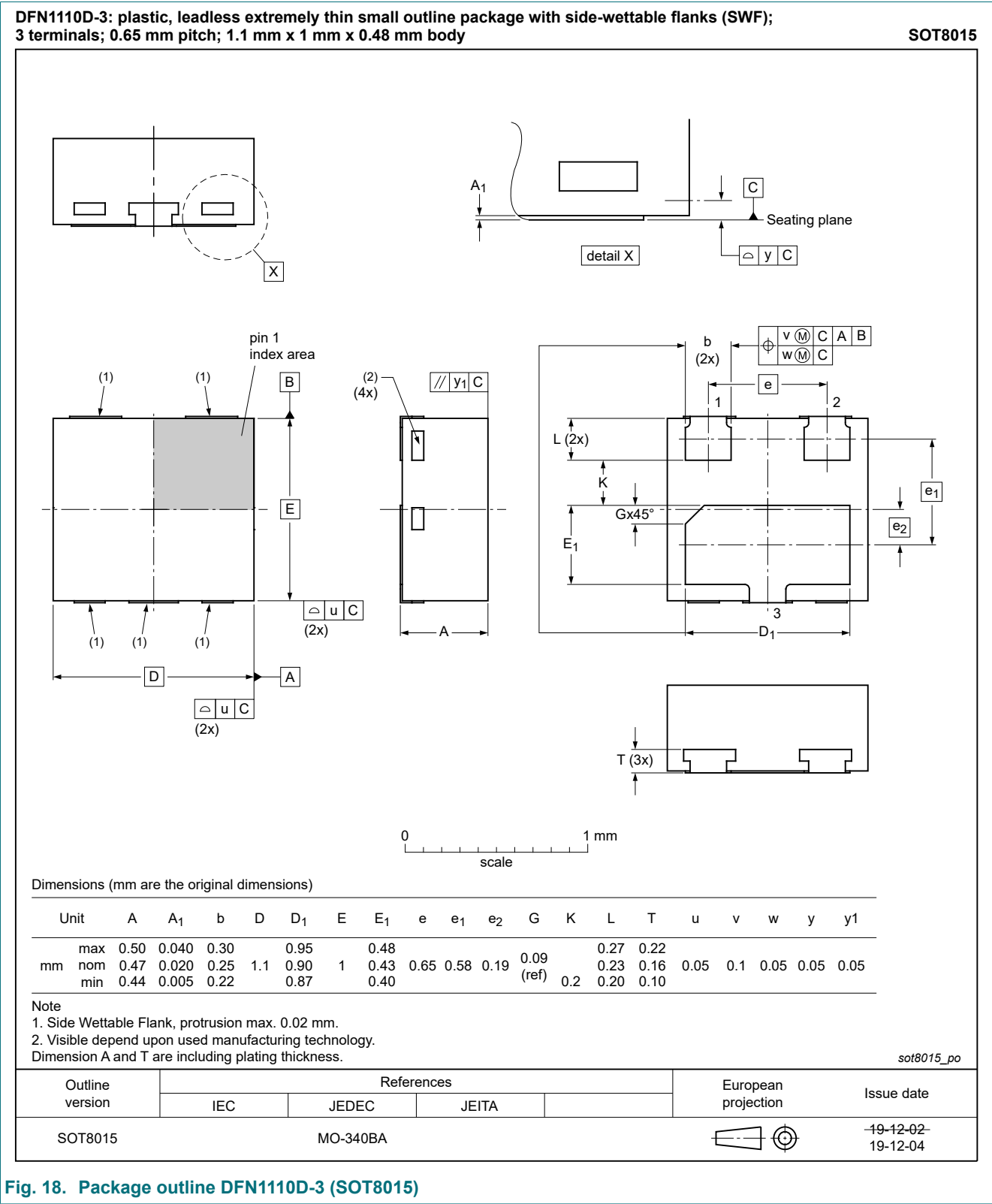


Fig. 18. Package outline DFN1110D-3 (SOT8015)

13. Soldering

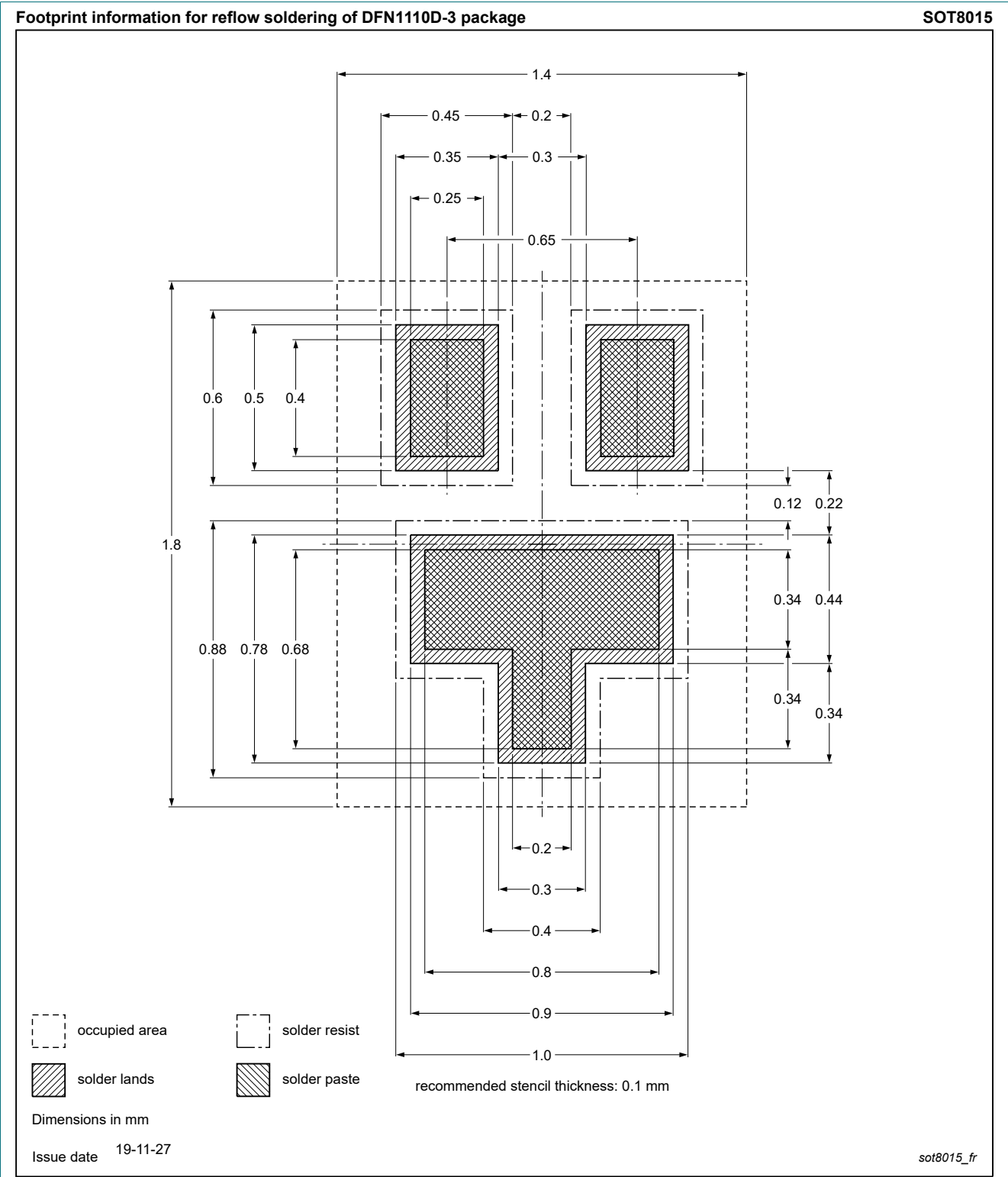


Fig. 19. Reflow soldering footprint for DFN1110D-3 (SOT8015)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BSS84AKQB v.1	20210713	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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