

ANTREX Electronics

Electronic Components Sourcing Information

Product Reference Information

Part Number: BC846S/DG/B4F

Manufacturer: Nexperia USA Inc.

Package: 6-TSSOP, SC-88, SOT-363

Availability: Please confirm with sales

Product Page:

<https://antrexco.com/product/details/nexperia-usa-inc/bc846s-dg-b4f.html>

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Note:

This PDF includes an ANTREX product reference cover page.

The original manufacturer datasheet follows from the next page.



BC846S

NPN general purpose double transistor

15 July 2026

Product data sheet

1. General description

NPN double transistor in a very small SOT363-3 (SC-88) Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Two transistors in one package
- Reduces number of components and board space
- No mutual interference between the transistors

3. Applications

- General purpose switching and small signal amplification.

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Per transistor						
V_{CEO}	collector-emitter voltage	open base	-	-	65	V
I_C	collector current		-	-	100	mA
h_{FE}	DC current gain	$V_{CE} = 5\text{ V}$; $I_C = 2\text{ mA}$; $T_{amb} = 25\text{ °C}$	110	-	-	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1	Transparent top view TSSOP6 (SOT363-3)	sym140
2	B1	base TR1		
3	C2	collector TR2		
4	E2	emitter TR2		
5	B2	base TR2		
6	C1	collector TR1		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BC846S	TSSOP6	Plastic surface-mounted package; 6 leads	SOT363-3

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
BC846S	4F%

[1] % = placeholder for manufacturing site code

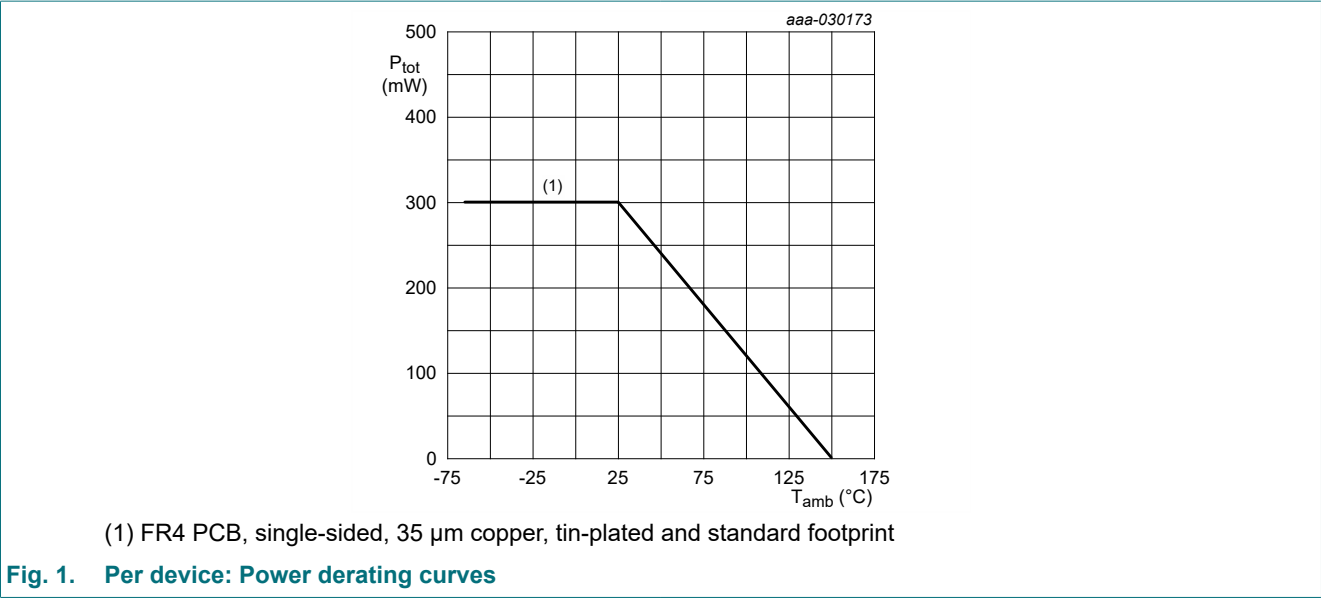
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transistor						
V _{CBO}	collector-base voltage	open emitter		-	80	V
V _{CEO}	collector-emitter voltage	open base		-	65	V
V _{EBO}	emitter-base voltage	open collector		-	6	V
I _C	collector current			-	100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	200	mA
I _{BM}	peak base current			-	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	220	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C
Per device						
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided, 35 µm copper, tin-plated and standard footprint.

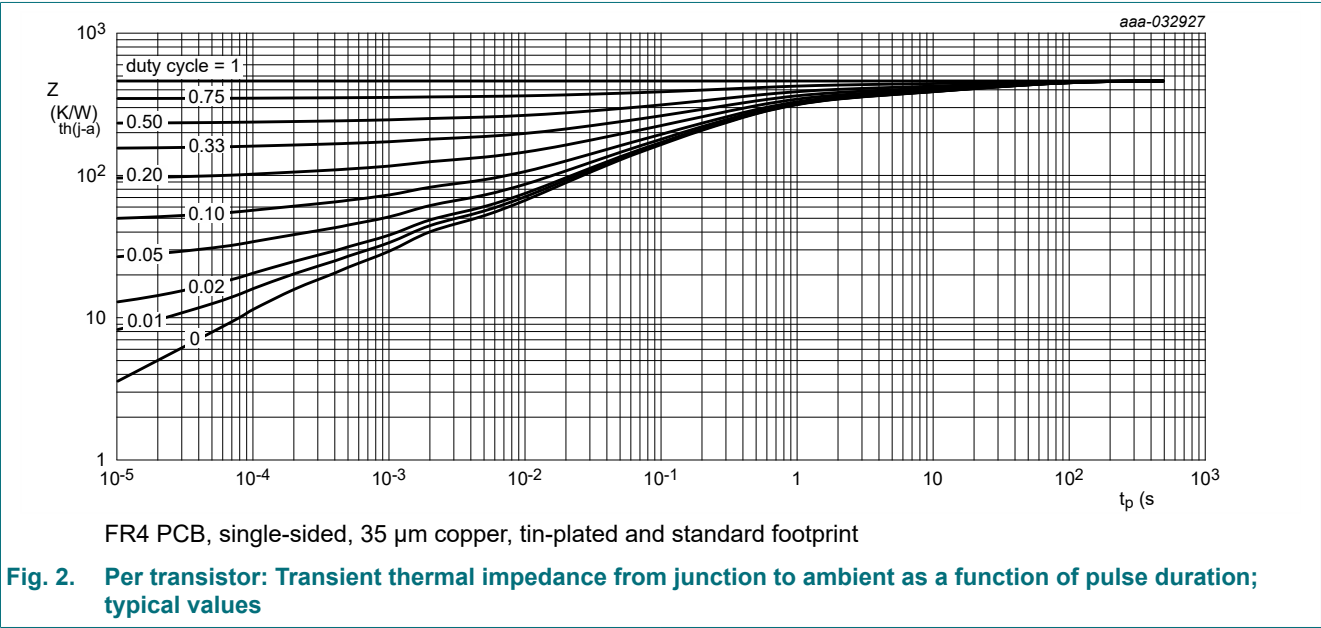


9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	416	K/W
Per transistor							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	568	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	230	K/W

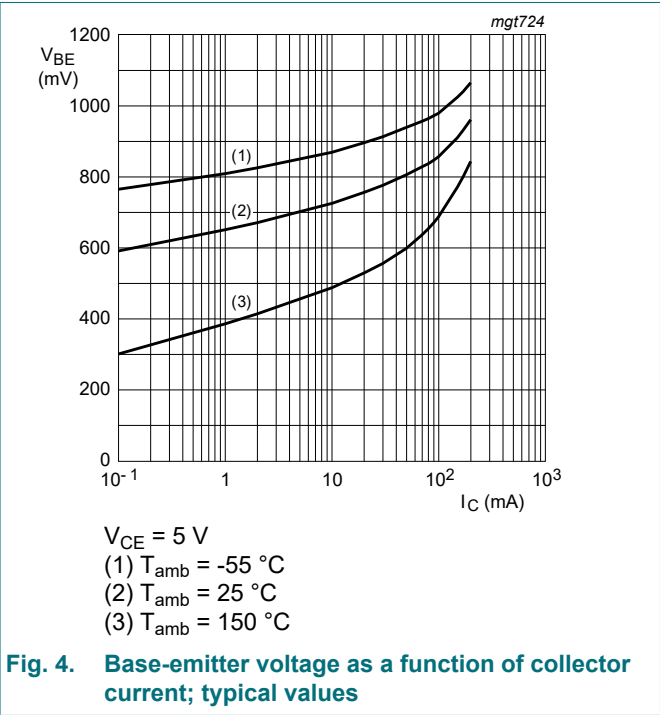
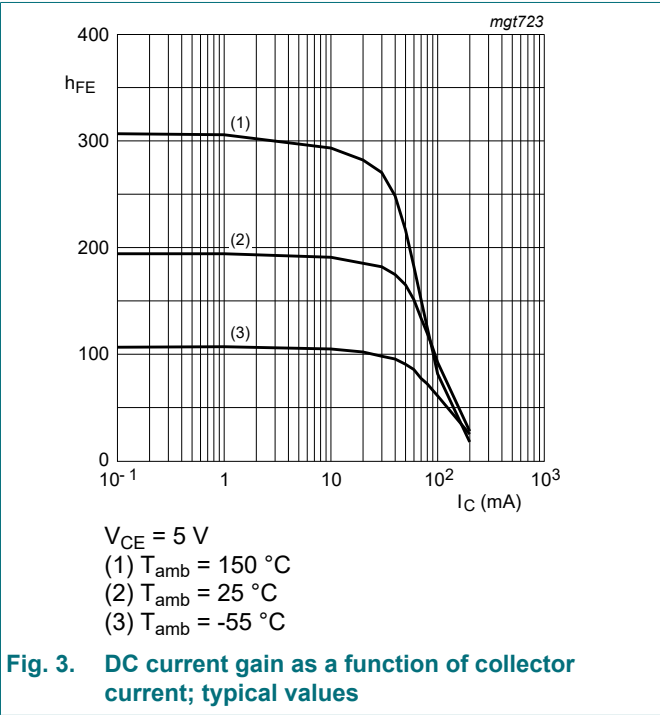
[1] Device mounted on an FR4 PCB, single-sided, 35 μ m copper, tin-plated and standard footprint.

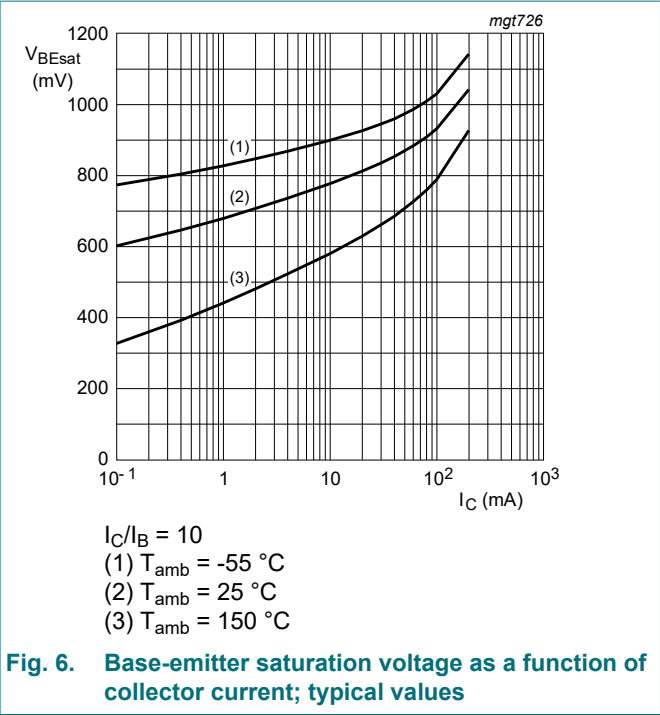
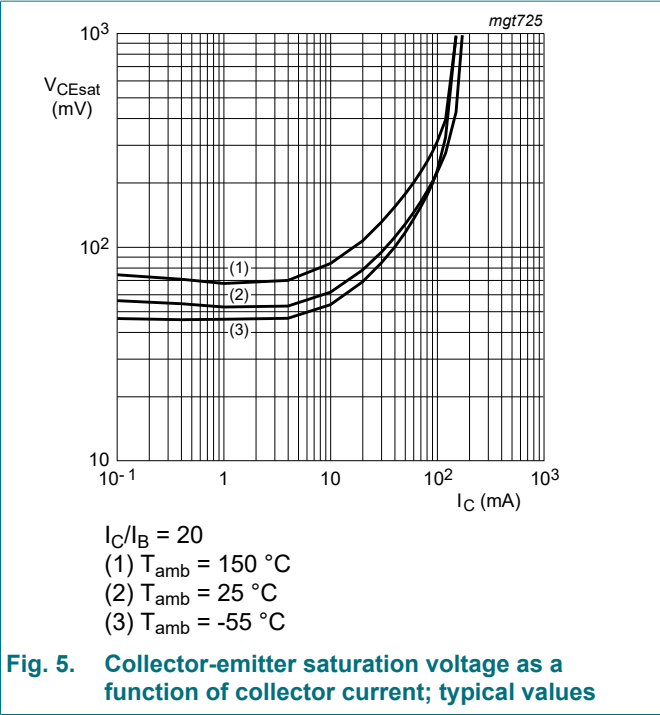


10. Characteristics

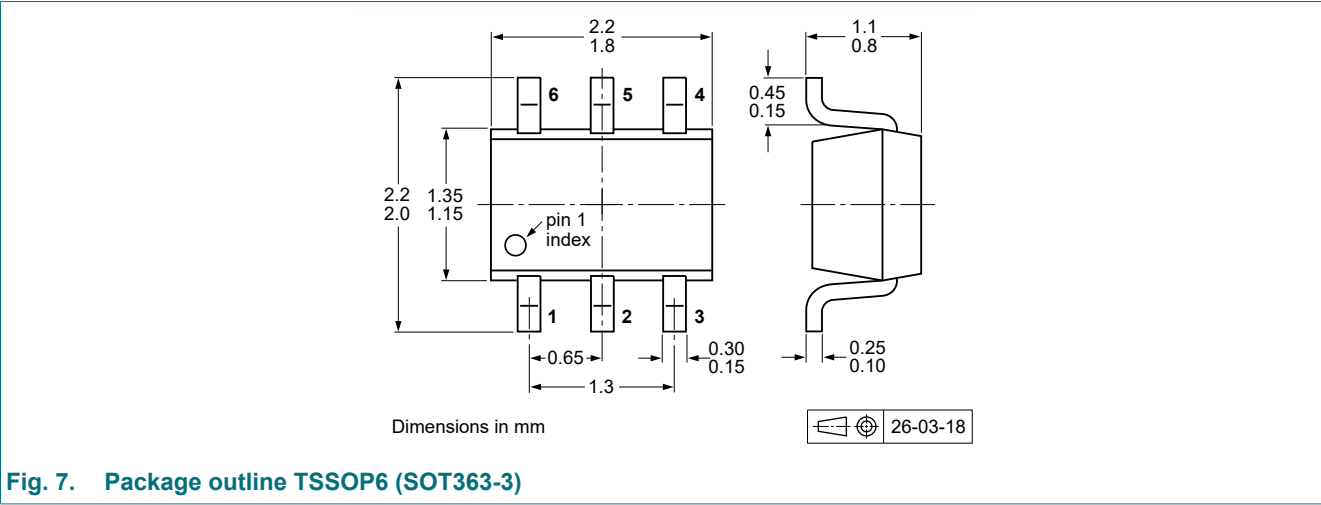
Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Per transistor						
$V_{(BR)CBO}$	collector-base breakdown voltage	$I_C = 100\text{ }\mu\text{A}$; $I_E = 0\text{ A}$; $T_{amb} = 25\text{ }^\circ\text{C}$	80	-	-	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	$I_C = 2\text{ mA}$; $I_B = 0\text{ A}$; $T_{amb} = 25\text{ }^\circ\text{C}$	65	-	-	V
$V_{(BR)EBO}$	emitter-base breakdown voltage	$I_C = 0\text{ A}$; $I_E = 100\text{ }\mu\text{A}$; $T_{amb} = 25\text{ }^\circ\text{C}$	6	-	-	V
I_{CBO}	collector-base cut-off current	$V_{CB} = 30\text{ V}$; $I_E = 0\text{ A}$; $T_{amb} = 25\text{ }^\circ\text{C}$	-	-	15	nA
		$V_{CB} = 30\text{ V}$; $I_E = 0\text{ A}$; $T_j = 150\text{ }^\circ\text{C}$	-	-	5	μA
I_{EBO}	emitter-base cut-off current	$V_{EB} = 5\text{ V}$; $I_C = 0\text{ A}$; $T_{amb} = 25\text{ }^\circ\text{C}$	-	-	100	nA
h_{FE}	DC current gain	$V_{CE} = 5\text{ V}$; $I_C = 2\text{ mA}$; $T_{amb} = 25\text{ }^\circ\text{C}$	110	-	-	
V_{CEsat}	collector-emitter saturation voltage	$I_C = 10\text{ mA}$; $I_B = 0.5\text{ mA}$; $T_{amb} = 25\text{ }^\circ\text{C}$	-	-	100	mV
		$I_C = 100\text{ mA}$; $I_B = 5\text{ mA}$; pulsed; $t_p \leq 300\text{ }\mu\text{s}$; $\delta \leq 0.02$; $T_{amb} = 25\text{ }^\circ\text{C}$	-	-	300	mV
V_{BEsat}	base-emitter saturation voltage	$I_C = 10\text{ mA}$; $I_B = 0.5\text{ mA}$; $T_{amb} = 25\text{ }^\circ\text{C}$	-	770	-	mV
V_{BE}	base-emitter voltage	$V_{CE} = 5\text{ V}$; $I_C = 2\text{ mA}$; $T_{amb} = 25\text{ }^\circ\text{C}$	580	-	700	mV
C_c	collector capacitance	$V_{CB} = 10\text{ V}$; $I_E = 0\text{ A}$; $i_e = 0\text{ A}$; $f = 1\text{ MHz}$; $T_{amb} = 25\text{ }^\circ\text{C}$	-	-	1.5	pF
f_T	transition frequency	$V_{CE} = 5\text{ V}$; $I_C = 10\text{ mA}$; $f = 100\text{ MHz}$; $T_{amb} = 25\text{ }^\circ\text{C}$	100	-	-	MHz





11. Package outline



12. Soldering

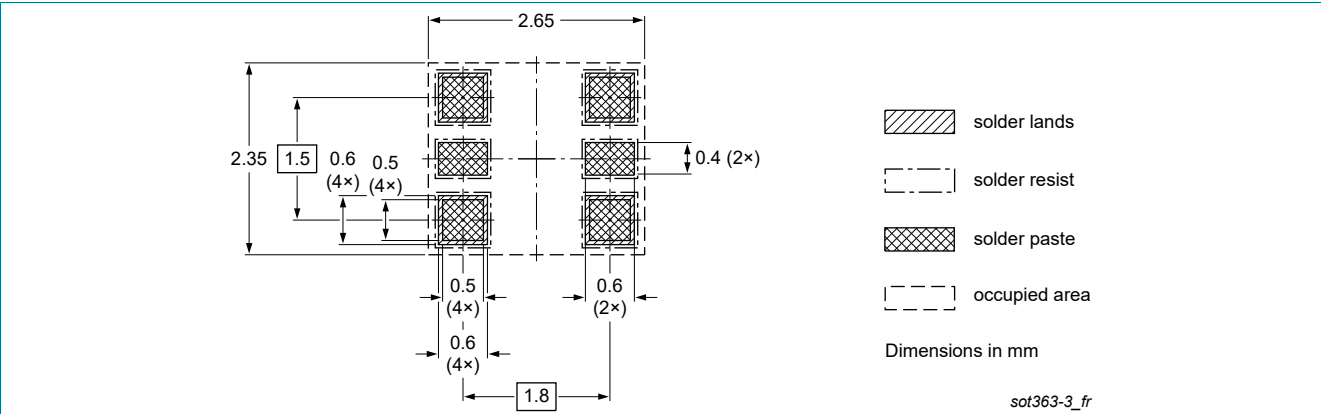


Fig. 8. Reflow soldering footprint for TSSOP6 (SOT363-3)

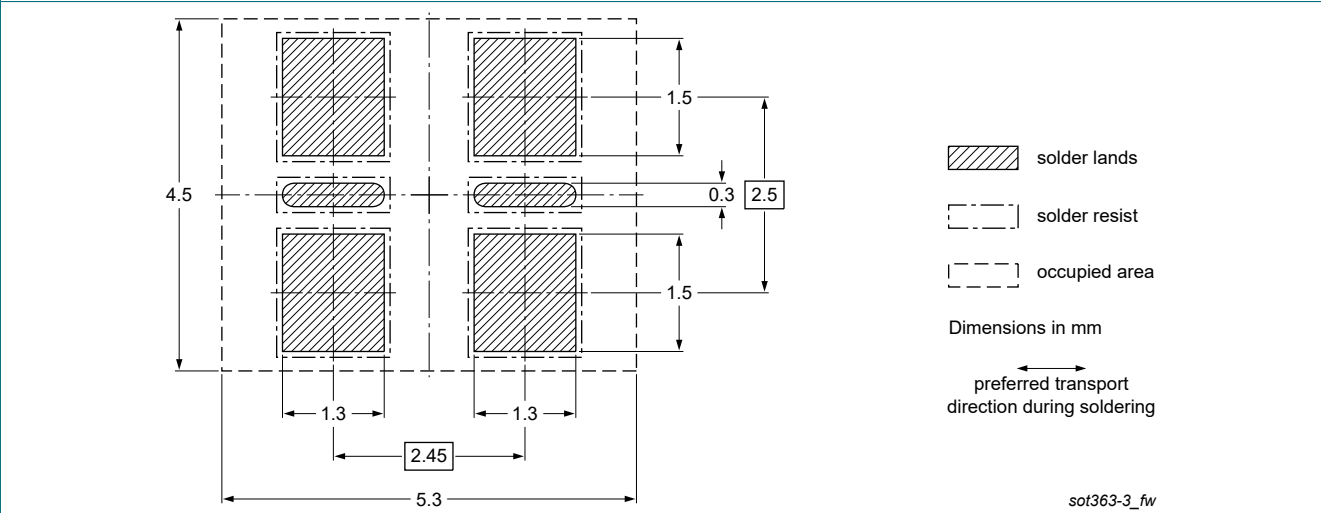


Fig. 9. Wave soldering footprint for TSSOP6 (SOT363-3)

13. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BC846S v.5	20260715	Product data sheet	-	BC846S v.4
Modifications:	• Package changed from SOT363 to SOT363-3			
BC846S v.4	20240528	Product data sheet	-	BC846S v.3
BC846S v.3	20221001	Product data sheet	-	BC846S v.2
BC846S v.2	19990901	Product data sheet	-	BC846S v.1
BC846S v.1	19990528	Product specification	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

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