

# ANTREX Electronics

## Electronic Components Sourcing Information

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### Product Reference Information

|                      |                                 |
|----------------------|---------------------------------|
| <b>Part Number:</b>  | SPK0838HT4H-B                   |
| <b>Manufacturer:</b> | Knowles                         |
| <b>Package:</b>      | Tape & Reel (TR), Cut Tape (CT) |
| <b>Availability:</b> | Please confirm with sales       |

#### Product Page:

<https://antrexco.com/product/details/knowles/spk0838ht4h-b.html>

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Supplier verification and packaging condition review

CoC / RoHS / REACH documents available on request

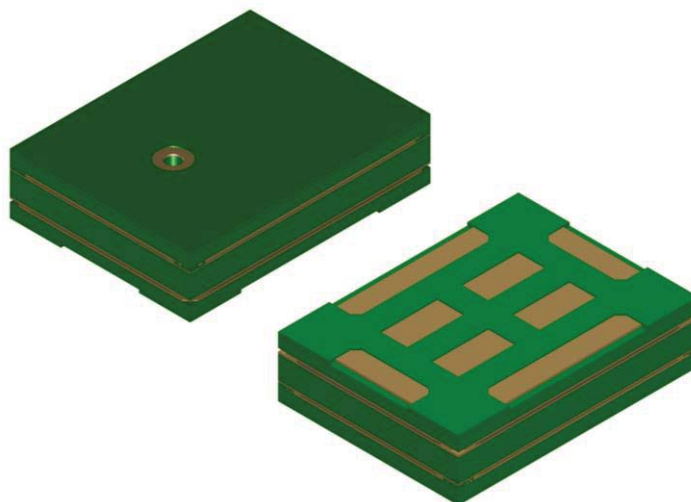
Secure sourcing support for OEM / EMS projects

#### Note:

This PDF includes an ANTREX product reference cover page.

The original manufacturer datasheet follows from the next page.

## Digital High-SNR SiSonic™ Microphone



The SPK0838HT4H-B is a miniature, high-performance, low power, top port silicon digital microphone with a single bit PDM output. Using Knowles' proven high performance SiSonic™ MEMS technology, the SPK0838HT4H-B consists of an acoustic sensor, a low noise input buffer, and a sigma-delta modulator. These devices are suitable for applications such as cellphones, smart phones, laptop computers, sensors, digital still cameras, portable music recorders, and other portable electronic devices where excellent audio performance and RF immunity are required. The high Signal-to-Noise-Ratio (SNR) of the SPK0838HT4H-B enhances the performance of far-field applications and many complex, multi-microphone algorithms.

### Features:

- High Signal to Noise Ratio of 64dB(A)
- Flat Frequency Response up to 20kHz
- Low Current
- RF Shielded
- PDM Output
- Supports Dual Multiplexed Channels
- Ultra-Stable Performance
- Standard SMD Reflow
- Omnidirectional

# SPK0838HT4H-B

## 1. ABSOLUTE MAXIMUM RATINGS

| Parameter                          | Absolute Maximum Rating          | Units |
|------------------------------------|----------------------------------|-------|
| $V_{DD}$ , DATA to Ground          | -0.3, +5.0                       | V     |
| CLOCK to Ground                    | -0.3, +5.0                       | V     |
| SELECT to Ground                   | -0.3, +5.0                       | V     |
| Input Current                      | $\pm 5$                          | mA    |
| Short Circuit Current to/from DATA | Indefinite to Ground or $V_{DD}$ | sec   |
| Temperature Range                  | -40 to +100                      | °C    |

Stresses exceeding these “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only. Functional operation at these or any other conditions beyond those indicated under “Acoustic & Electrical Specifications” is not implied. Exposure beyond those indicated under “Acoustic & Electrical Specifications” for extended periods may affect device reliability.

## 2. ACOUSTIC & ELECTRICAL SPECIFICATIONS

TEST CONDITIONS: 23  $\pm$  2°C, 55 $\pm$ 20% R.H.,  $V_{DD}$ =1.8 V,  $F_{CLOCK}$ =2.4 MHz, SELECT pin grounded, no load, unless otherwise indicated

| Parameter                     | Symbol      | Conditions                                    | Min                       | Typ  | Max | Units    |
|-------------------------------|-------------|-----------------------------------------------|---------------------------|------|-----|----------|
| Supply Voltage <sup>1</sup>   | $V_{DD}$    |                                               | 1.6                       | 1.8  | 3.6 | V        |
| Supply Current <sup>2</sup>   | $I_{DD}$    | $V_{DD} = 1.8V$                               | -                         | 500  | 600 | $\mu A$  |
| Supply Current <sup>1,2</sup> | $I_{DD}$    | $V_{DD} = 3.6V$                               | -                         | 600  | 700 | $\mu A$  |
| Sleep Current                 | $I_{SLEEP}$ | $F_{CLOCK} < 1 \text{ kHz}$ , $V_{DD} = 1.8V$ | -                         | 4    | 8   | $\mu A$  |
| Sleep Current <sup>1</sup>    | $I_{SLEEP}$ | $F_{CLOCK} < 1 \text{ kHz}$ , $V_{DD} = 3.6V$ | -                         | 5    | 10  | $\mu A$  |
| Sensitivity <sup>1</sup>      | S           | 94 dB SPL @ 1 kHz                             | -29                       | -26  | -23 | dBFS     |
| Signal to Noise Ratio         | SNR         | 94 dB SPL @ 1 kHz, A-weighted                 | -                         | 64   | -   | dB(A)    |
| Total Harmonic Distortion     | THD         | 94 dB SPL @ 1 kHz, S = Typ                    | -                         | 0.25 | 1   | %        |
| Acoustic Overload Point       | AOP         | 10% THD @ 1 kHz, S = Typ, $V_{DD} = 3.6V$     | 118                       | 121  | -   | dB SPL   |
| Power Supply Rejection Ratio  | PSRR        | 200 mVpp sinewave @ 1 kHz                     | -                         | 65   | -   | dBFS /V  |
| Power Supply Rejection        | PSR+N       | 100 mVpp square wave @ 217 Hz, A-weighted     | -                         | -87  | -   | dBFS (A) |
| DC Output                     |             | Fullscale = $\pm 100$                         | -                         | -5   | -   | % FS     |
| Directivity                   |             |                                               | Omnidirectional           |      |     |          |
| Polarity                      |             | Increasing sound pressure                     | Increasing density of 1's |      |     |          |

# SPK0838HT4H-B

| Parameter                         | Symbol                 | Conditions                          | Min                  | Typ | Max                  | Units |
|-----------------------------------|------------------------|-------------------------------------|----------------------|-----|----------------------|-------|
| Data Format                       |                        |                                     | ½ Cycle PDM          |     |                      |       |
| Logic Input High                  | $V_{IH}$               |                                     | $0.65 \times V_{DD}$ | -   | 3.6                  | V     |
| Logic Input Low                   | $V_{IL}$               |                                     | GND-0.3              | -   | $0.35 \times V_{DD}$ | V     |
| Logic Output High                 | $V_{OH}$               | $I_{OUT} = 2 \text{ mA}$            | $0.7 \times V_{DD}$  | -   | $V_{DD}$             | V     |
| Logic Output Low                  | $V_{OL}$               | $I_{OUT} = 2 \text{ mA}$            | GND                  | -   | $0.3 \times V_{DD}$  | V     |
| SELECT (high)                     |                        |                                     | $V_{DD} - 0.2$       | -   | 3.6                  | V     |
| SELECT (low)                      |                        |                                     | GND                  | -   | GND+0.2              | V     |
| Short Circuit Current             | $I_{SC}$               | Grounded DATA pin                   | 2                    | -   | 10                   | mA    |
| Output Load                       | $C_{LOAD}$             |                                     | -                    | -   | 160                  | pF    |
| Fall-asleep Time <sup>3,4</sup>   |                        | $F_{CLOCK} < 1 \text{ kHz}$         | -                    | -   | 10                   | ms    |
| Wake-up Time <sup>3,5</sup>       |                        | $F_{CLOCK} \geq 1 \text{ MHz}$      | -                    | -   | 20                   | ms    |
| Startup Time <sup>3</sup>         |                        | Powered Down → Active Mode          | -                    | -   | 50                   | ms    |
| Restart Time <sup>3</sup>         |                        | Active → Powered Down → Active Mode | -                    | -   | 125                  | ms    |
| Clock Frequency                   | $F_{CLOCK}$            |                                     | 1.0                  | -   | 3.25                 | MHz   |
| Clock Duty Cycle                  |                        |                                     | 40                   | -   | 60                   | %     |
| Clock Rise/Fall Time              | $t_{EDGE}$             |                                     | -                    | -   | 13                   | ns    |
| Data Time                         | $t_{SRTUP} + t_{EDGE}$ | Max $C_{LOAD}$                      | 47                   | -   | -                    | ns    |
| Delay Time to Data Line Driven    | $t_{DD}$               |                                     | 18                   | -   | -                    | ns    |
| Delay Time to Valid Data          | $t_{DV}$               | Max $C_{LOAD}$                      | -                    | -   | 115                  | ns    |
| Delay Time to High Z <sup>6</sup> | $t_{DZ}$               |                                     | 0                    | -   | 16                   | ns    |

<sup>1</sup> 100% tested.

<sup>2</sup>  $I_{DD}$  varies with  $C_{LOAD}$  according to:  $\Delta I_{DD} = 0.5 \times V_{DD} \times \Delta C_{LOAD} \times F_{CLOCK}$ .

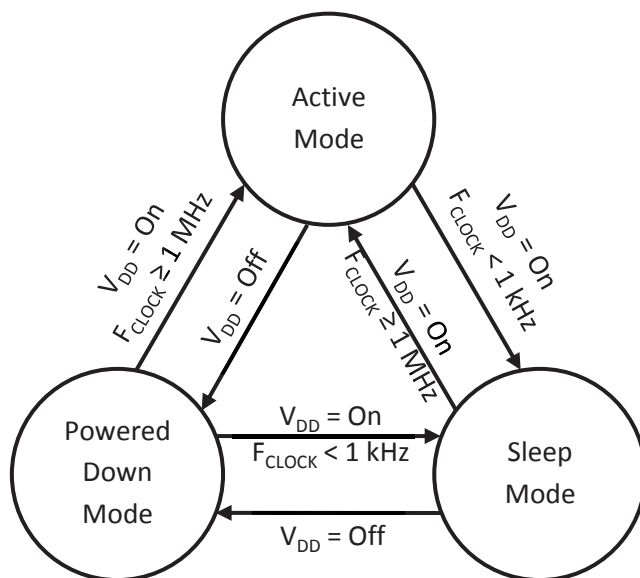
<sup>3</sup> Valid microphones states are: Powered Down Mode (mic off), Sleep Mode (low current, DATA = high-Z, fast startup), and Active Mode (normal operation).

<sup>4</sup> Time from  $F_{CLOCK} < 1 \text{ kHz}$  to  $I_{SLEEP}$  specification is met when transitioning from Active Mode to Sleep Mode.

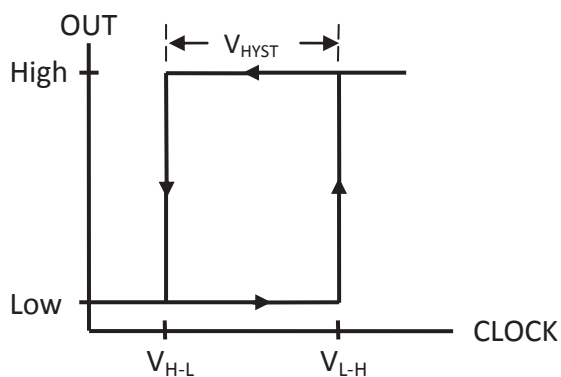
<sup>5</sup> Time from  $F_{CLOCK} \geq 1 \text{ MHz}$  to all applicable specifications are met when transitioning from Sleep Mode to Active Mode.

<sup>6</sup>  $t_{HOLD}$  is dependent on  $C_{LOAD}$

## 3. MICROPHONE STATE DIAGRAM

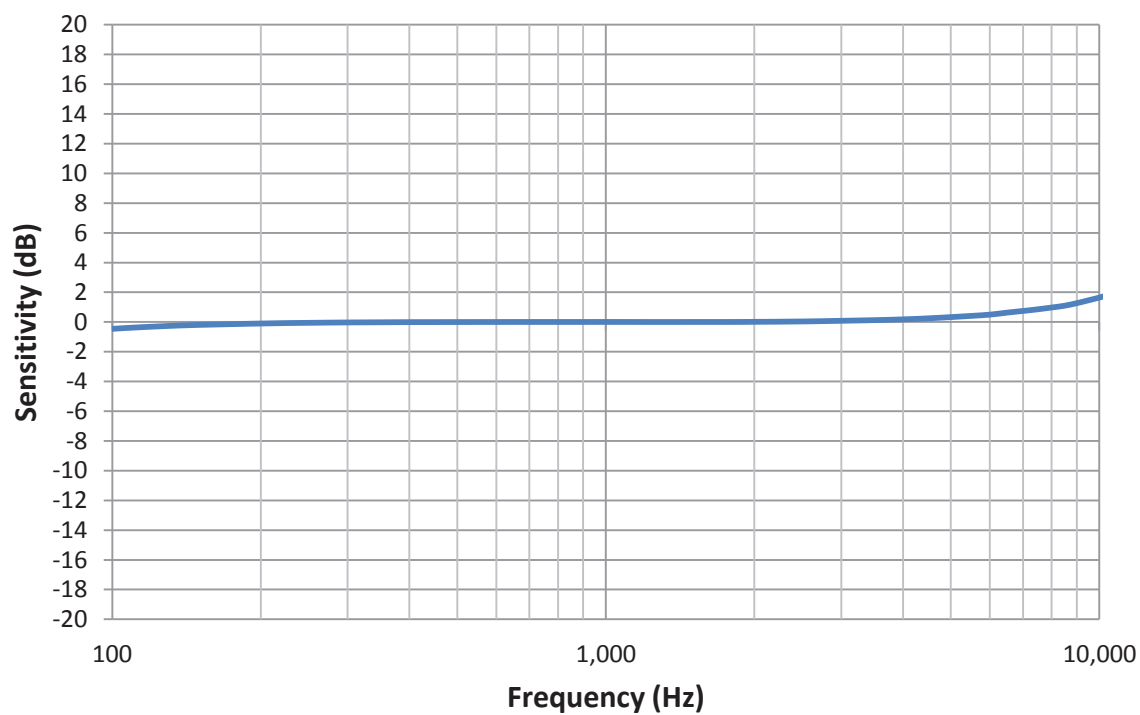


## 4. HYSTERESIS DIAGRAM



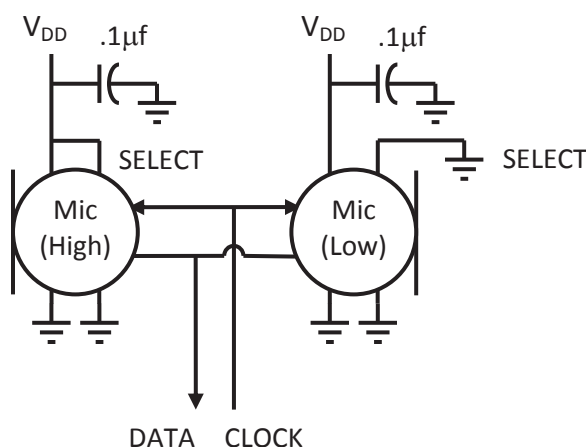
## 5. FREQUENCY RESPONSE CURVE

**Typical Free Field Response  
Normalized to 1kHz**



# SPK0838HT4H-B

## 6. INTERFACE CIRCUIT



| Microphone | SELECT          | Asserts DATA On    | Latch DATA On      |
|------------|-----------------|--------------------|--------------------|
| Mic (High) | V <sub>DD</sub> | Rising Clock Edge  | Falling Clock Edge |
| Mic (Low)  | GND             | Falling Clock Edge | Rising Clock Edge  |

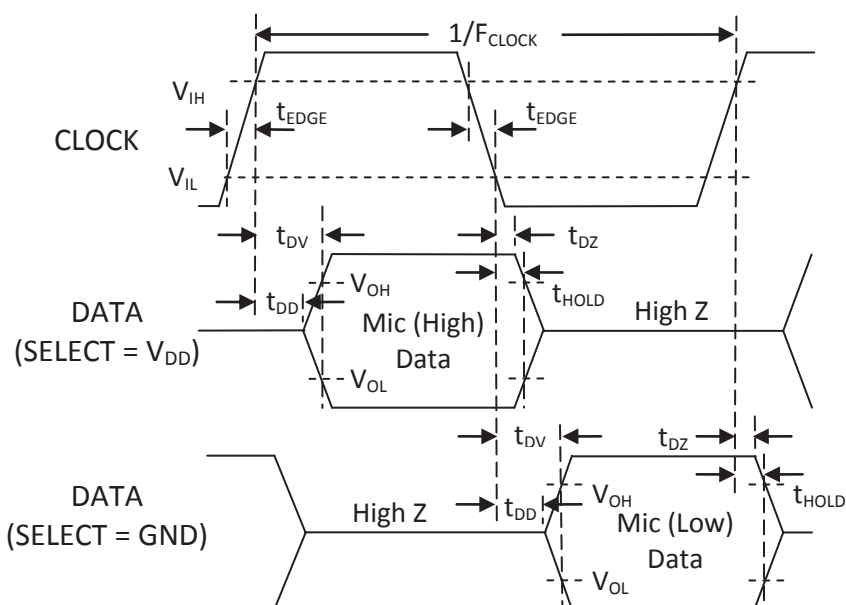
Note: All Ground pins must be connected to ground.

Bypass capacitors should be placed next to each Vdd pin for maximum performance.

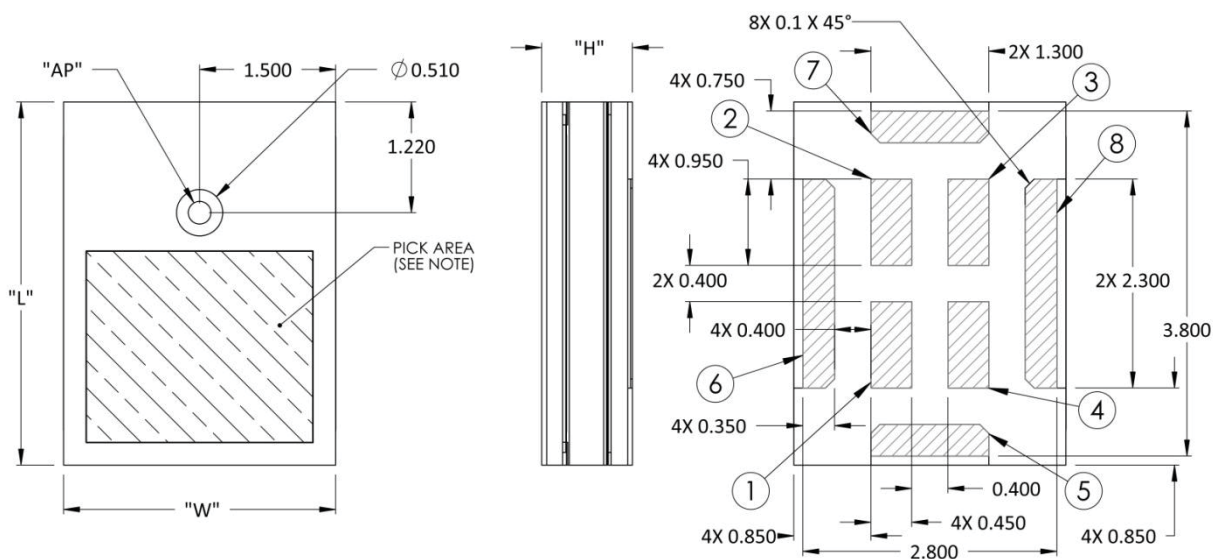
Capacitors near the microphone should not contain Class 2 dielectrics.

Detailed information on acoustic, mechanical, and system integration can be found in the latest *SiSonic™ Design Guide* application note.

## 7. TIMING DIAGRAM



## 8. MECHANICAL SPECIFICATIONS



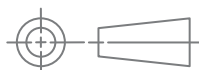
| Item               | Dimension   | Tolerance   | Units |
|--------------------|-------------|-------------|-------|
| Length (L)         | 4.00        | $\pm 0.10$  | mm    |
| Width (W)          | 3.00        | $\pm 0.10$  | mm    |
| Height (H)         | 1.00        | $\pm 0.10$  | mm    |
| Acoustic Port (AP) | $\phi 0.25$ | $\pm 0.075$ | mm    |

| Pin # | Pin Name        | Type              | Description                                              |
|-------|-----------------|-------------------|----------------------------------------------------------|
| 1     | V <sub>DD</sub> | Power             | Power Supply                                             |
| 2     | SELECT          | Non-Digital Input | Lo/Hi (L/R) Select<br>This pin is internally pulled low. |
| 3     | CLOCK           | Digital I         | Clock Input                                              |
| 4     | DATA            | Digital O         | PDM Output                                               |
| 5     | GROUND          | Power             | Ground                                                   |
| 6     | GROUND          | Power             | Ground                                                   |
| 7     | GROUND          | Power             | Ground                                                   |
| 8     | GROUND          | Power             | Ground                                                   |

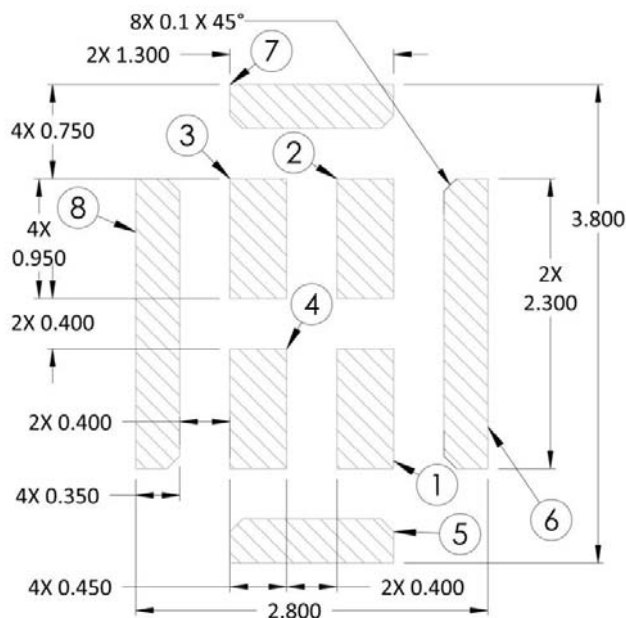
Notes: Pick Area only extends to 0.25 mm of any edge or hole unless otherwise specified.

Dimensions are in millimeters unless otherwise specified.

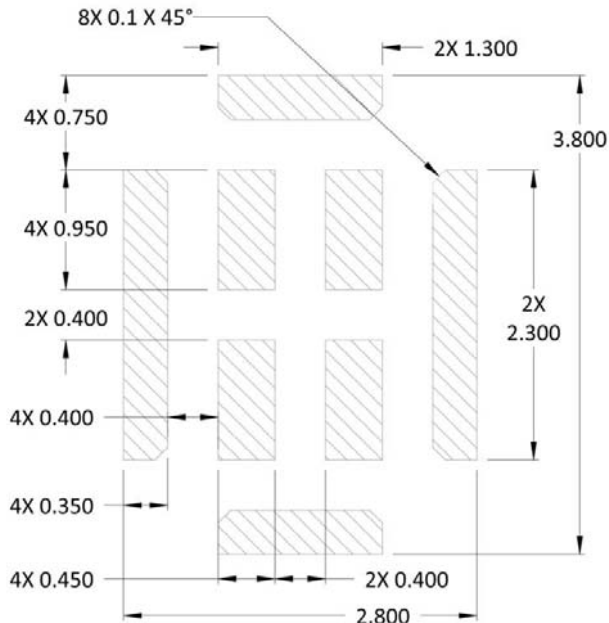
Tolerance is  $\pm 0.15$ mm unless otherwise specified



## 9. EXAMPLE LAND PATTERN



## 10. EXAMPLE SOLDER STENCIL PATTERN

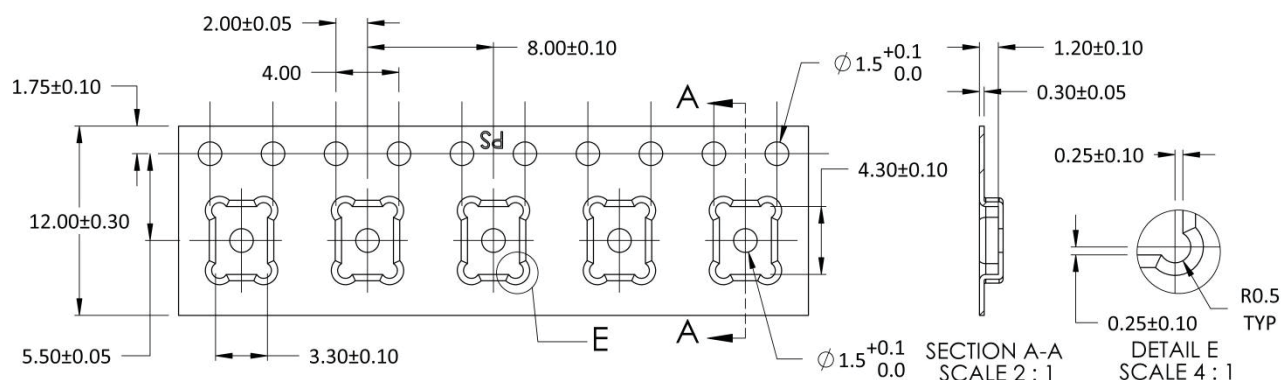


Notes: Dimensions are in millimeters unless otherwise specified.

Detailed information on AP size considerations can be found in the latest *SiSonic™ Design Guide* application note.

Further optimizations based on application should be performed.

## 11. PACKAGING & MARKETING DETAIL



| Model Number  | Suffix | Reel Diameter | Quantity Per Reel |
|---------------|--------|---------------|-------------------|
| SPK0838HT4H-B | -7     | 13"           | 5,700             |

YWW: Year/Week date code

Letter: "K"

Alpha Character A:

"S": Manufactured in China

“M”: Manufactured in Malaysia

"I": Manufactured in Itasca

Version Number Vx:

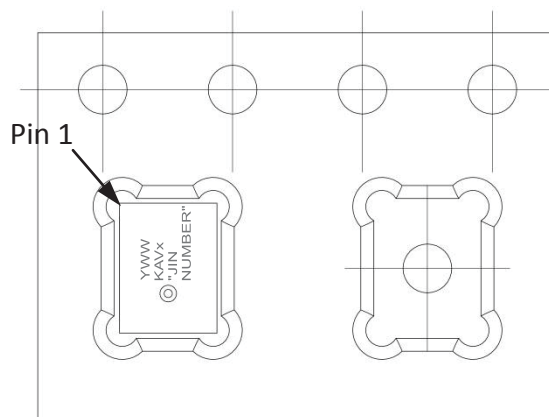
Ex: Pre-proto version number x

Px: Proto & EVT version number x

Mx: Mass production version number x

“JIN NUMBER”:

### Unique Job Identification Number for product traceability



Notes: Dimensions are in millimeters unless otherwise specified.

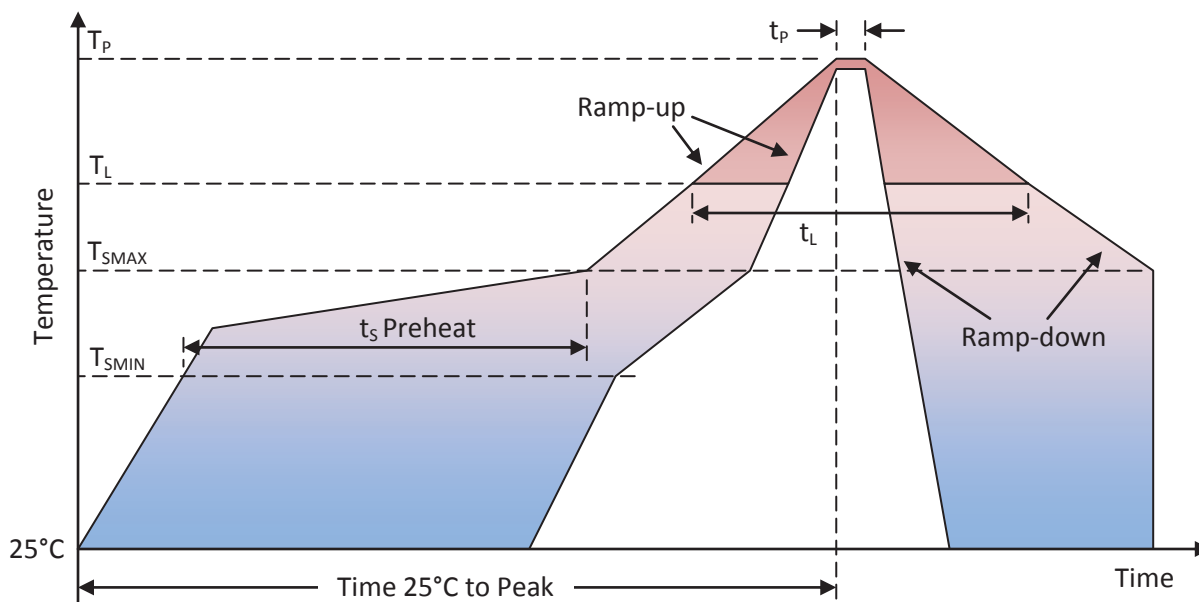
Vacuum pickup only in the pick area indicated in Mechanical Specifications.

Tape & reel per EIA-481.

Labels applied directly to reel and external package.

Shelf life: Twelve (12) months when devices are to be stored in factory supplied, unopened ESD moisture sensitive bag under maximum environmental conditions of 30°C, 70% R.H.

## 12. RECOMMENDED REFLOW PROFILE



| Profile Feature                                                                                                                                                                                               | Pb-Free                          |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------------|
| Average Ramp-up rate (T <sub>S</sub> MAX to T <sub>P</sub> )                                                                                                                                                  | 3°C/second max.                  |
| Preheat <ul style="list-style-type: none"> <li>Temperature Min (T<sub>S</sub>MIN)</li> <li>Temperature Max (T<sub>S</sub>MAX)</li> <li>Time (T<sub>S</sub>MIN to T<sub>S</sub>MAX) (t<sub>s</sub>)</li> </ul> | 150°C<br>200°C<br>60-180 seconds |
| Time maintained above: <ul style="list-style-type: none"> <li>Temperature (T<sub>L</sub>)</li> <li>Time (t<sub>L</sub>)</li> </ul>                                                                            | 217°C<br>60-150 seconds          |
| Peak Temperature (T <sub>P</sub> )                                                                                                                                                                            | 260°C                            |
| Time within 5°C of actual Peak Temperature (t <sub>p</sub> )                                                                                                                                                  | 20-40 seconds                    |
| Ramp-down rate (T <sub>P</sub> to T <sub>S</sub> MAX)                                                                                                                                                         | 6°C/second max                   |
| Time 25°C to Peak Temperature                                                                                                                                                                                 | 8 minutes max                    |

Notes: Based on IPC/JDEC J-STD-020 Revision C.

All temperatures refer to topside of the package, measured on the package body surface.



### 13. ADDITIONAL NOTES

(A) Shelf life: Twelve (12) months when devices are to be stored in factory supplied, unopened ESD moisture sensitive bag under maximum environmental conditions of 30°C, 70% R.H.

(B) MSL (moisture sensitivity level) Class 1.

(C) Maximum of 3 reflow cycles is recommended.

(D) In order to minimize device damage:

- Do not board wash or clean after the reflow process.
- Do not brush board with or without solvents after the reflow process.
- Do not directly expose to ultrasonic processing, welding, or cleaning.
- Do not insert any object in port hole of device at any time.
- Do not apply over 30 psi of air pressure into the port hole.
- Do not pull a vacuum over port hole of the microphone.
- Do not apply a vacuum when repacking into sealed bags at a rate faster than 0.5 atm/sec.

### 14. MATERIALS STATEMENT

Meets the requirements of the European RoHS directive, 2011/65/EC as amended.

Meets the requirements of the industry-standard IEC 61249-2-21:2003 for halogenated substances and Knowles Green Materials Standards Policy section on Halogen-Free.

Ozone depleting substances are not used in the product or the processes used to make the product including compounds listed in annex A, B, and C of the “Montreal Protocol on Substances that deplete the ozone Layer.”

## 15. RELIABILITY SPECIFICATIONS

| Test                        | Description                                                                                                                                           |
|-----------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------|
| Thermal Shock               | 100 cycles air-to-air thermal shock from -40°C to +125°C with 15 minute soaks. (IEC 68-2-4)                                                           |
| High Temperature Storage    | 1,000 hours at +105°C environment (IEC 68-2-2 Test Ba)                                                                                                |
| Low Temperature Storage     | 1,000 hours at -40°C environment (IEC 68-2-2 Test Aa)                                                                                                 |
| High Temperature Bias       | 1,000 hours at +105°C under bias (IEC 68-2-2 Test Ba)                                                                                                 |
| Low Temperature Bias        | 1,000 hours at -40°C under bias (IEC 68-2-2 Test Aa)                                                                                                  |
| Temperature / Humidity Bias | 1,000 hours at +85°C/85% R.H. under bias. (JESD22-A101A-B)                                                                                            |
| Vibration                   | 4 cycles of 20 to 2,000 Hz sinusoidal sweep with 20 G peak acceleration lasting 12 minutes in X, Y, and Z directions. (Mil-Std-883E, method 2007.2 A) |
| ESD-HBM                     | 3 discharges of ±2 kV direct contact to I/O pins. (ESD STM5.2)                                                                                        |
| ESD-LID/GND                 | 3 discharges of ±8 kV direct contact to lid while unit is grounded. (IEC 61000-4-2)                                                                   |
| ESD-MM                      | 3 discharges of ±2 kV direct contact to I/O pins. (MIL 883E, Method 3015.7)                                                                           |
| Reflow                      | 5 reflow cycles with peak temperature of +260°C                                                                                                       |
| Mechanical Shock            | 3 pulses of 10,000 G in the X, Y, and Z direction (IEC 68-2-27, Test Ea)                                                                              |

Note: After reliability tests are performed, the sensitivity of the microphones shall not deviate more than 3 dB from its initial value.



## SPK0838HT4H-B

**16. SPECIFICATIONS REVISIONS**

| Revision | Specification Changes       | Date      |
|----------|-----------------------------|-----------|
| A        | Initial Release (C10115620) | 2/21/2014 |
|          |                             |           |
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