

# ANTREX Electronics

## Electronic Components Sourcing Information

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### Product Reference Information

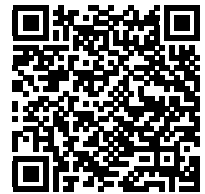
|                      |                           |
|----------------------|---------------------------|
| <b>Part Number:</b>  | BG3130RE6327BTSA1         |
| <b>Manufacturer:</b> | Infineon Technologies     |
| <b>Package:</b>      | Please confirm with sales |
| <b>Availability:</b> | Please confirm with sales |

**Product Page:**

<https://antrexco.com/product/details/infineon-technologies/bg3130re6327btsa1.html>

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### Quality & Trust

New & Original Components

Supplier verification and packaging condition review

CoC / RoHS / REACH documents available on request

Secure sourcing support for OEM / EMS projects

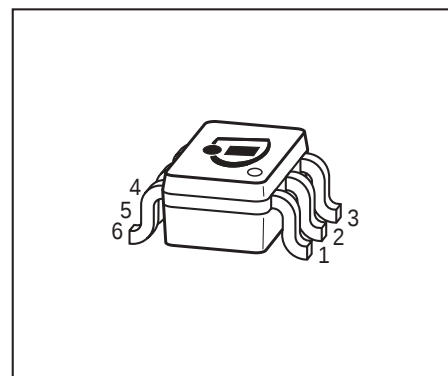
**Note:**

This PDF includes an ANTREX product reference cover page.

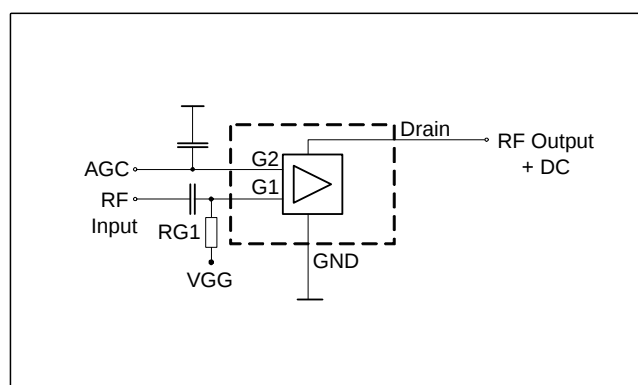
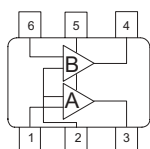
The original manufacturer datasheet follows from the next page.

## DUAL N-Channel MOSFET Tetrode

- Two gain controlled input stage for UHF and VHF -tuners e.g. (NTSC, PAL)
- Two AGC amplifiers in one single package
- Integrated gate protection diodes
- High AGC-range, low noise figure, high gain
- Improved cross modulation at gain reduction



### BG3130 BG3130R



**ESD (Electrostatic discharge) sensitive device, observe handling precaution!**

| Type    | Package | Pin Configuration |      |      |       |      |        | Marking |
|---------|---------|-------------------|------|------|-------|------|--------|---------|
| BG3130  | SOT363  | 1=G1*             | 2=G2 | 3=D* | 4=D** | 5=S  | 6=G1** | KAs     |
| BG3130R | SOT363  | 1=G1*             | 2=S  | 3=D* | 4=D** | 5=G2 | 6=G1** | KHs     |

\* For amp. A; \*\* for amp. B  
180° rotated tape loading orientation available

### Maximum Ratings

| Parameter                     | Symbol           | Value       | Unit |
|-------------------------------|------------------|-------------|------|
| Drain-source voltage          | $V_{DS}$         | 8           | V    |
| Continuous drain current      | $I_D$            | 25          | mA   |
| Gate 1/ gate 2-source current | $\pm I_{G1/2SM}$ | 1           |      |
| Gate 1/ gate 2-source voltage | $\pm V_{G1/G2S}$ | 6           | V    |
| Total power dissipation       | $P_{tot}$        | 200         | mW   |
| Storage temperature           | $T_{stg}$        | -55 ... 150 | °C   |
| Channel temperature           | $T_{ch}$         | 150         |      |

**Thermal Resistance**

| Parameter                               | Symbol      | Value      | Unit |
|-----------------------------------------|-------------|------------|------|
| Channel - soldering point <sup>1)</sup> | $R_{thchs}$ | $\leq 280$ | K/W  |

**Electrical Characteristics**

| Parameter | Symbol | Values |      |      | Unit |
|-----------|--------|--------|------|------|------|
|           |        | min.   | typ. | max. |      |

**DC Characteristics**

|                                                                                         |                 |    |     |    |         |
|-----------------------------------------------------------------------------------------|-----------------|----|-----|----|---------|
| Drain-source breakdown voltage<br>$I_D = 10 \mu A$ , $V_{G1S} = 0 V$ , $V_{G2S} = 0 V$  | $V_{(BR)DS}$    | 12 | -   | -  | V       |
| Gate1-source breakdown voltage<br>$+I_{G1S} = 10 mA$ , $V_{G2S} = 0 V$ , $V_{DS} = 0 V$ | $+V_{(BR)G1SS}$ | 6  | -   | 15 |         |
| Gate2-source breakdown voltage<br>$+I_{G2S} = 10 mA$ , $V_{G1S} = 0 V$ , $V_{DS} = 0 V$ | $+V_{(BR)G2SS}$ | 6  | -   | 15 |         |
| Gate1-source leakage current<br>$V_{G1S} = 6 V$ , $V_{G2S} = 0 V$                       | $+I_{G1SS}$     | -  | -   | 50 | $\mu A$ |
| Gate2-source leakage current<br>$V_{G2S} = 8 V$ , $V_{G1S} = 0 V$ , $V_{DS} = 0 V$      | $+I_{G2SS}$     | -  | -   | 50 | nA      |
| Drain current<br>$V_{DS} = 5 V$ , $V_{G1S} = 0 V$ , $V_{G2S} = 4.5 V$                   | $I_{DSS}$       | -  | -   | 10 | $\mu A$ |
| Drain-source current<br>$V_{DS} = 5 V$ , $V_{G2S} = 4 V$ , $R_{G1} = 120 k\Omega$       | $I_{DSX}$       | -  | 10  | -  | mA      |
| Gate1-source pinch-off voltage<br>$V_{DS} = 5 V$ , $V_{G2S} = 4 V$ , $I_D = 20 \mu A$   | $V_{G1S(p)}$    | -  | 0.7 | -  | V       |
| Gate2-source pinch-off voltage<br>$V_{DS} = 5 V$ , $I_D = 20 \mu A$                     | $V_{G2S(p)}$    | -  | 0.6 | -  |         |

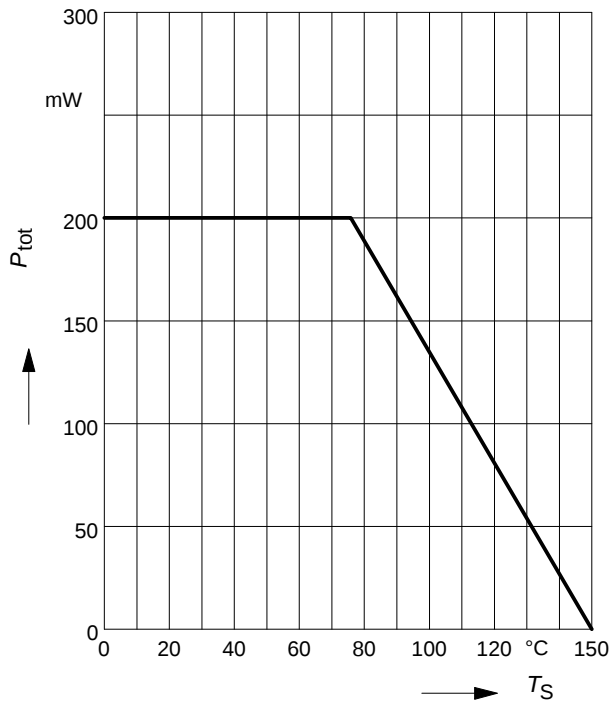
<sup>1)</sup>For calculation of  $R_{thJA}$  please refer to Application Note Thermal Resistance

**Electrical Characteristics**

| Parameter                                                                                                          | Symbol       | Values        |                |             | Unit |
|--------------------------------------------------------------------------------------------------------------------|--------------|---------------|----------------|-------------|------|
|                                                                                                                    |              | min.          | typ.           | max.        |      |
| <b>AC Characteristics</b> $V_{DS} = 5V$ , $V_{G2S} = 4V$ , ( $I_D = 14\text{ mA}$ ) (verified by random sampling)  |              |               |                |             |      |
| Forward transconductance                                                                                           | $g_{fs}$     | -             | 33             | -           | mS   |
| Gate1 input capacitance<br>$f = 10\text{ MHz}$                                                                     | $C_{g1ss}$   | -             | 1.9            | -           | pF   |
| Output capacitance<br>$f = 10\text{ MHz}$                                                                          | $C_{dss}$    | -             | 1.1            | -           |      |
| Power gain<br>$f = 800\text{ MHz}$<br>$f = 45\text{ MHz}$                                                          | $G_p$        | -<br>-        | 24<br>31       | -<br>-      | dB   |
| Noise figure<br>$f = 800\text{ MHz}$<br>$f = 45\text{ MHz}$                                                        | $F$          | -<br>-        | 1.3<br>1.7     | -<br>-      |      |
| Gain control range<br>$V_{G2S} = 4 \dots 0\text{ V}$ , $f = 800\text{ MHz}$                                        | $\Delta G_p$ | 45            | -              | -           |      |
| Cross-modulation $k=1\%$ , $f_w=50\text{MHz}$ , $f_{unw}=60\text{MHz}$<br>AGC = 0 dB<br>AGC = 10 dB<br>AGC = 40 dB | $X_{mod}$    | 90<br>-<br>96 | -<br>87<br>100 | -<br>-<br>- | -    |

**Total power dissipation**  $P_{\text{tot}} = f(T_S)$

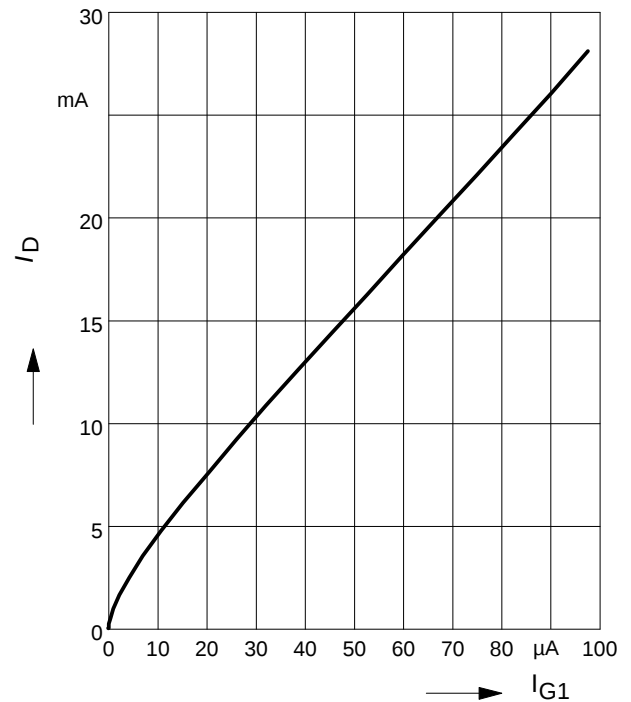
amp. A = amp. B



**Drain current**  $I_D = f(I_{G1})$

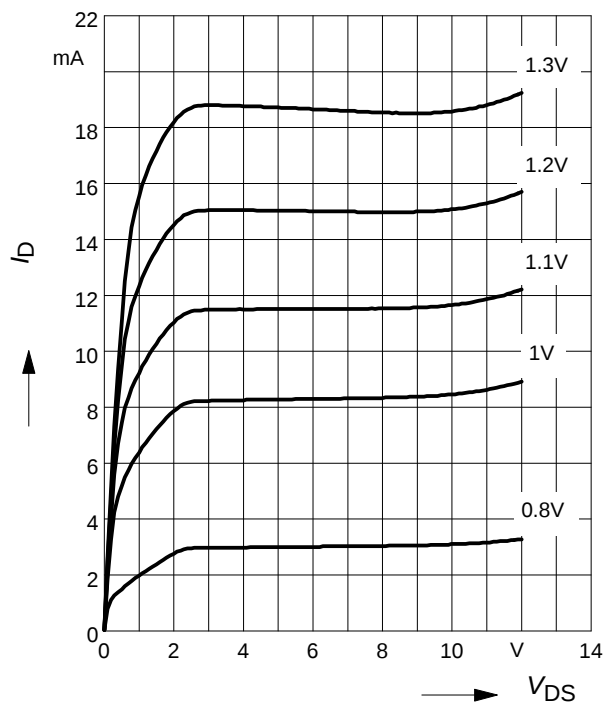
$V_{G2S} = 4V$

amp. A = amp. B



**Output characteristics**  $I_D = f(V_{DS})$

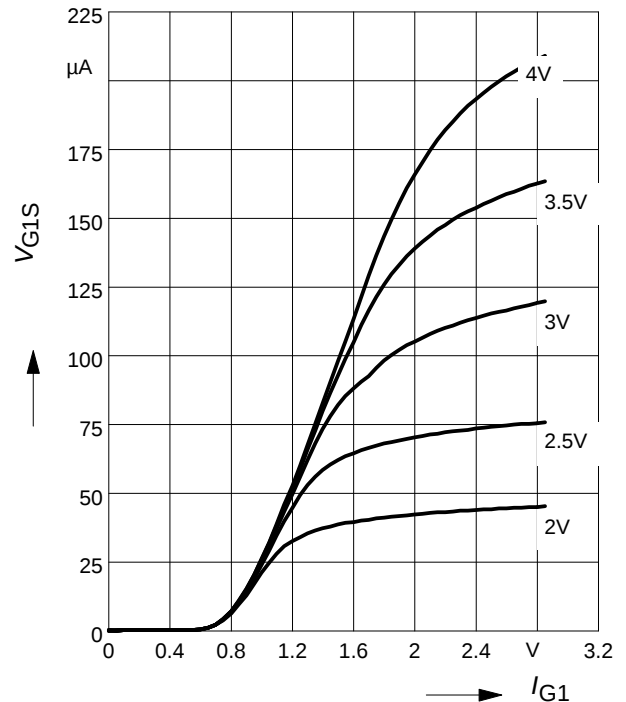
amp. A = amp. B



**Gate 1 current**  $I_{G1} = f(V_{G1S})$

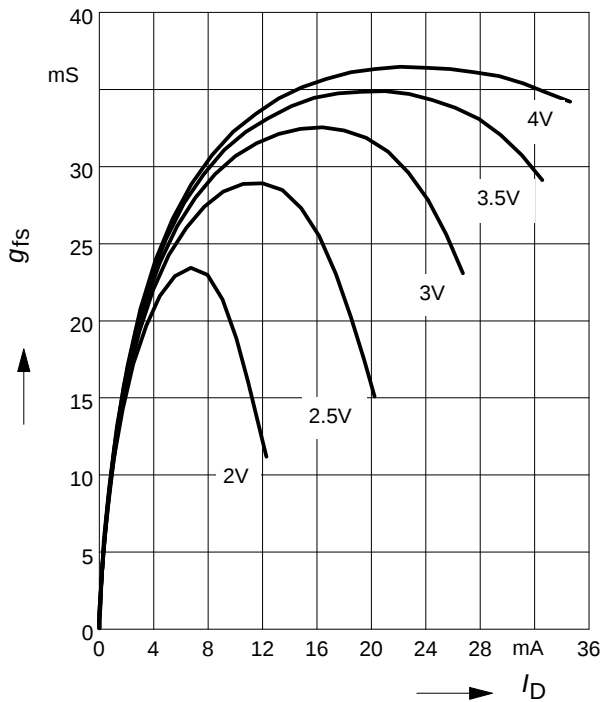
$V_{DS} = 5V$ ,  $V_{G2S} = \text{Parameter}$

amp. A = amp. B

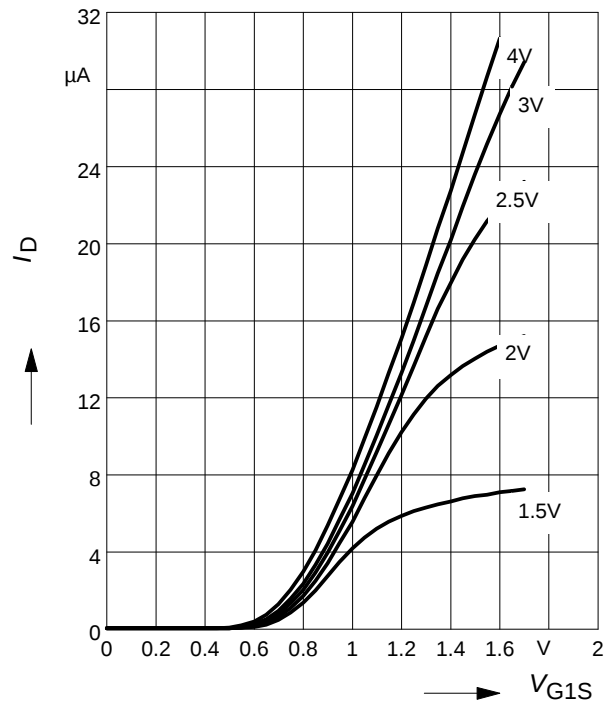


**Gate 1 forward transconductance**

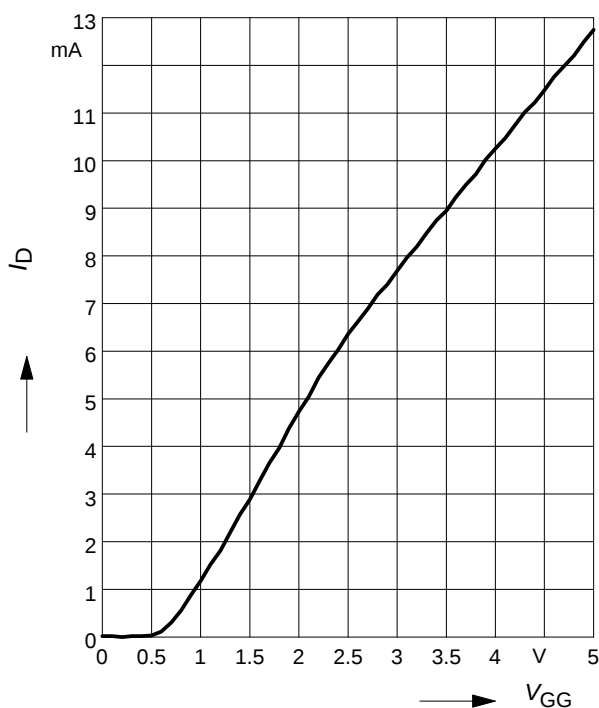
$g_{fs} = f(I_D)$ ,  $V_{DS} = 5V$ ,  $V_{G2S} = \text{Parameter}$   
amp. A = amp. B


**Drain current  $I_D = f(V_{G1S})$** 

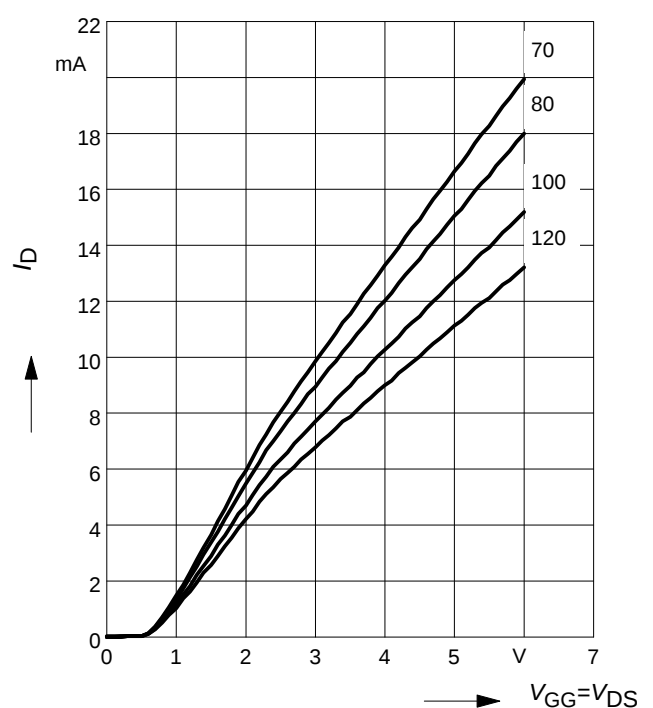
$V_{DS} = 5V$ ,  $V_{G2S} = \text{Parameter}$   
amp. A = amp. B

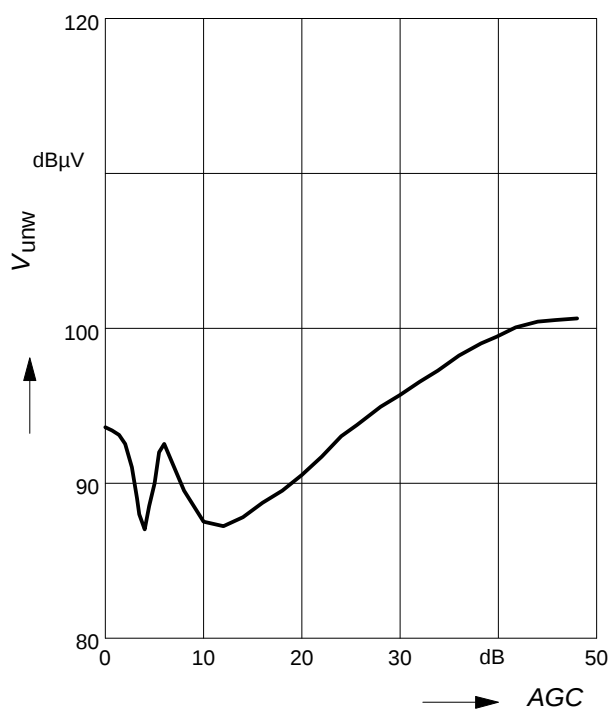
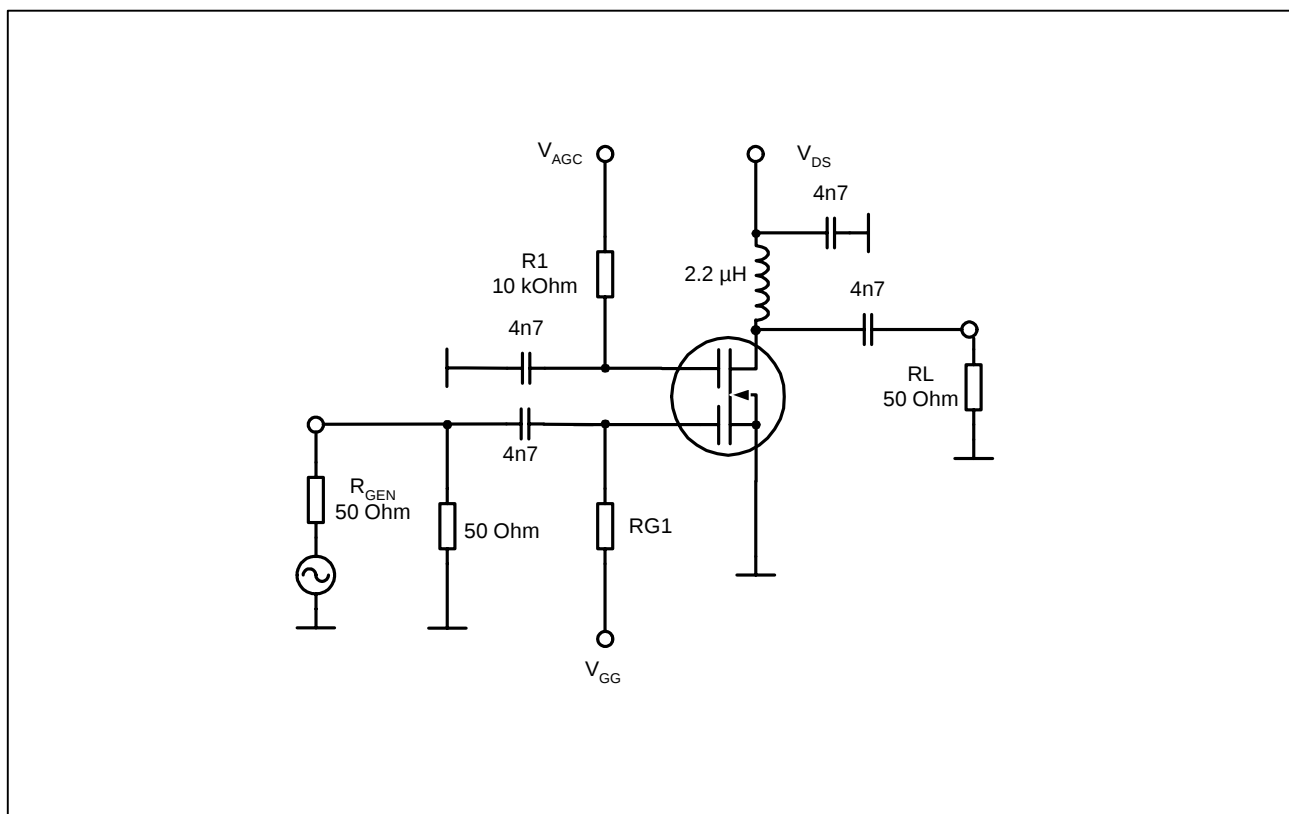

**Drain current  $I_D = f(V_{GG})$  amp.A=amp.B**

$V_{DS} = 5V$ ,  $V_{G2S} = 4V$ ,  $R_{G1} = 120k\Omega$   
(connected to  $V_{GG}$ ,  $V_{GG} = \text{gate1 supply voltage}$ )


**Drain current  $I_D = f(V_{GG})$** 

$V_{G2S} = 4V$ ,  $R_{G1} = \text{Parameter in } k\Omega$   
amp. A = amp. B



**Crossmodulation  $V_{unw} = (AGC)$** 
 $V_{DS} = 5\text{ V}$ ,  $R_{g1} = 68\text{ k}\Omega$ 

**Crossmodulation test circuit**


The diagram illustrates the marking scheme for BCR108S components. It shows two examples of component markings.

**Example 1 (Left):** The marking consists of '12 s' followed by a date code box (Year/Month) and a type code box. Labels indicate the Manufacturer, Date code (Year/Month), Type code, Laser marking, and Pin 1 marking.

**Example 2 (Right):** The marking consists of 'WH s' followed by '56' and 'BCR108S'. Labels indicate the Manufacturer, Date code (Year/Month), Type code, Laser marking, and Pin 1 marking.

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