

# ANTREX Electronics

## Electronic Components Sourcing Information

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### Product Reference Information

**Part Number:** TBU-DT065-200-WH

**Manufacturer:** Bourns Inc.

**Package:** 4-DFN (5x5)

**Availability:** Please confirm with sales

**Product Page:**

<https://antrexco.com/product/details/bourns-inc/tbu-dt065-200-wh.html>

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### Contact Information

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New & Original Components

Supplier verification and packaging condition review

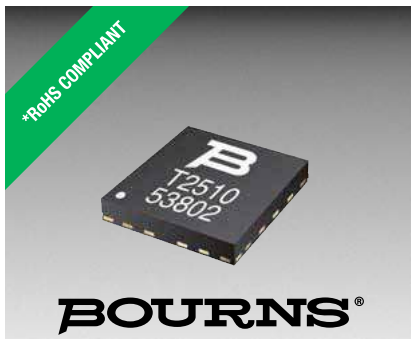
CoC / RoHS / REACH documents available on request

Secure sourcing support for OEM / EMS projects

**Note:**

This PDF includes an ANTREX product reference cover page.

The original manufacturer datasheet follows from the next page.



## Features

- Superior circuit protection
- Overcurrent and overvoltage protection
- Blocks surges up to rated limits
- High-speed performance
- Small SMT package
- RoHS compliant\*
- Agency listing: 

## Applications

- Ethernet ports
- Protection modules and dongles
- Process control equipment
- Test and measurement equipment
- General electronics

# TBU-DT Series - TBU® High-Speed Protectors

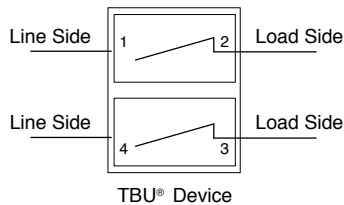
## General Information

The TBU-DT Series of Bourns® TBU® (Transient Blocking Unit) products are very low capacitance dual unidirectional high-speed surge protection components designed to protect against faults caused by short circuits, AC power cross, induction and lightning surges.

The TBU-DT series is a unidirectional TBU® device; the TBU® protector will trip in less than 1  $\mu$ s when the current reaches the maximum value in one direction only, that is when Pin 1 is positive in voltage with respect to Pin 2, and Pin 4 is positive with respect to Pin 3. No current limiting exists in the opposite polarity, and the TBU® device appears as resistive in nature. The reverse current should not exceed the maximum trigger current level of the TBU® device. An external diode may be used to prevent reverse current in DC biased applications.

The TBU® protector blocks surges and provides an effective barrier behind which sensitive electronics will not be exposed to large voltages or currents during surge events. After the surge, the TBU® device resets when the voltage across the TBU® device falls to the  $V_{reset}$  level. The TBU® device will automatically reset on lines which have no DC bias or have DC bias below  $V_{reset}$  (such as unpowered signal lines).

The TBU® device is provided in a surface mount DFN package and meets industry standard requirements such as RoHS and Pb Free solder reflow profiles.



## Additional Information

Click these links for more information:



## Agency Listing

| Description |                                      |
|-------------|--------------------------------------|
| UL          | File Number: <a href="#">E315805</a> |



**WARNING**  
Cancer and Reproductive Harm  
[www.P65Warnings.ca.gov](http://www.P65Warnings.ca.gov)

\*RoHS Directive 2015/863, Mar 31, 2015 and Annex. Specifications are subject to change without notice. Users should verify actual device performance in their specific applications. The products described herein and this document are subject to specific legal disclaimers as set forth on the last page of this document, and at [www.bourns.com/docs/legal/disclaimer.pdf](http://www.bourns.com/docs/legal/disclaimer.pdf).

## Absolute Maximum Ratings (@ $T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

| Symbol    | Parameter                                                    | Part Number                          | Value       | Unit             |
|-----------|--------------------------------------------------------------|--------------------------------------|-------------|------------------|
| $V_{imp}$ | Peak impulse voltage withstand with duration less than 10 ms | TBU-DT065-xxx-WH<br>TBU-DT085-xxx-WH | 650<br>850  | V                |
| $V_{rms}$ | Continuous A.C. RMS voltage                                  | TBU-DT065-xxx-WH<br>TBU-DT085-xxx-WH | 300<br>425  | V                |
| $T_{op}$  | Operating temperature range                                  |                                      | -40 to +125 | $^\circ\text{C}$ |
| $T_{stg}$ | Storage temperature range                                    |                                      | -65 to +150 | $^\circ\text{C}$ |

## Electrical Characteristics (@ $T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

| Symbol        | Parameter                                                                               | Part Number                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      | Min.                                                                                                                                                             | Typ.                                          | Max.                                                      | Unit               |
|---------------|-----------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------|-----------------------------------------------------------|--------------------|
| $I_{trigger}$ | Current required for the device to go from operating state to protected state           | TBU-DTxxx-100-WH<br>TBU-DTxxx-200-WH<br>TBU-DTxxx-300-WH<br>TBU-DTxxx-500-WH                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     | 100<br>200<br>300<br>500                                                                                                                                         | 150<br>300<br>450<br>750                      | 200<br>400<br>600<br>1000                                 | mA                 |
| $R_{device}$  | Series resistance of the TBU® device                                                    | $V_{imp} = 650\text{ V } I_{trigger}(\text{min.}) = 100\text{ mA}$<br>$V_{imp} = 650\text{ V } I_{trigger}(\text{min.}) = 200\text{ mA}$<br>$V_{imp} = 650\text{ V } I_{trigger}(\text{min.}) = 300\text{ mA}$<br>$V_{imp} = 650\text{ V } I_{trigger}(\text{min.}) = 500\text{ mA}$<br><br>$V_{imp} = 850\text{ V } I_{trigger}(\text{min.}) = 100\text{ mA}$<br>$V_{imp} = 850\text{ V } I_{trigger}(\text{min.}) = 200\text{ mA}$<br>$V_{imp} = 850\text{ V } I_{trigger}(\text{min.}) = 300\text{ mA}$<br>$V_{imp} = 850\text{ V } I_{trigger}(\text{min.}) = 500\text{ mA}$ | TBU-DT065-100-WH<br>TBU-DT065-200-WH<br>TBU-DT065-300-WH<br>TBU-DT065-500-WH<br><br>TBU-DT085-100-WH<br>TBU-DT085-200-WH<br>TBU-DT085-300-WH<br>TBU-DT085-500-WH | <br><br><br><br><br>10.3<br>7.4<br>6.5<br>5.8 | 8.5<br>5.6<br>4.6<br>4.0<br><br>12.1<br>8.7<br>7.7<br>6.9 | $\Omega$           |
| $R_{match}$   | Package resistance matching of the TBU® device #1 - TBU® device #2                      |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | -0.5                                                                                                                                                             |                                               | +0.5                                                      | $\Omega$           |
| $t_{block}$   | Time for the device to go from normal operating state to protected state                |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |                                                                                                                                                                  |                                               | 1                                                         | $\mu\text{s}$      |
| $I_Q$         | Current through the triggered TBU® device with 50 Vdc circuit voltage                   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | 0.25                                                                                                                                                             | 0.50                                          | 1.00                                                      | mA                 |
| $V_{reset}$   | Voltage below which the triggered TBU® device will transition to normal operating state |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | 10                                                                                                                                                               | 14                                            | 18                                                        | V                  |
| $R_{th(j-l)}$ | Junction to package pads - FR4 using recommended pad layout                             |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |                                                                                                                                                                  | 116                                           |                                                           | $^\circ\text{C/W}$ |
| $R_{th(j-l)}$ | Junction to package pads - FR4 using heat sink on board (6 cm <sup>2</sup> )            |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |                                                                                                                                                                  | 96                                            |                                                           | $^\circ\text{C/W}$ |

# TBU-DT Series - TBU® High-Speed Protectors

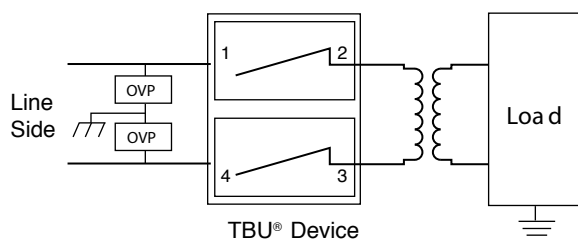
**BOURNS®**

## Environmental Characteristics

| Parameter                  | Value |
|----------------------------|-------|
| Moisture Sensitivity Level | 1     |
| ESD Classification (HBM)   | 1B    |

## Reference Application

The TBU® device can be used to protect against excessive voltage surges in transformer coupled equipment, as shown in the figure below. The TBU® protector prevents any surges from causing damage. An overvoltage protection device, such as an MOV or GDT, may be used to provide additional overvoltage protection if the surge voltage is likely to be above the maximum rating of the TBU® device.



## Basic TBU Operation

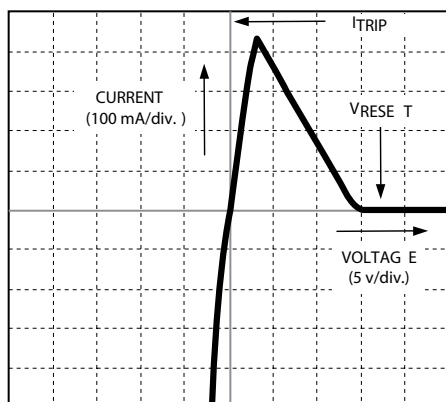
The TBU® device is a silicon-based, solid-state, resettable device which is placed in series with a signal path. The TBU® device operates in approximately 1  $\mu$ s - once line current exceeds the TBU® device's trigger current  $I_{\text{trigger}}$ . When operated, the TBU® device will limit the current to less than the  $I_{\text{trigger}}$  value within the  $t_{\text{block}}$  duration. If voltage above  $V_{\text{reset}}$  is continuously sustained, the TBU® device will subsequently reduce the current to a quiescent current level within a period of time that is dependent upon the applied voltage.

After the surge, the TBU® device resets when the voltage across the TBU® device falls to the  $V_{\text{reset}}$  level. The TBU® device will automatically reset on lines which have no DC bias or have DC bias below  $V_{\text{reset}}$  (such as unpowered signal lines).

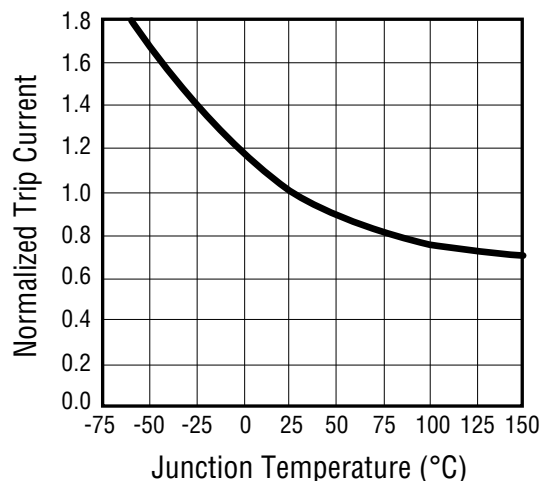
If the line has a normal DC bias above  $V_{\text{reset}}$ , the voltage across the TBU® device may not fall below  $V_{\text{reset}}$  after the surge. In such cases, special care needs to be taken to ensure that the TBU® device will reset, otherwise an automatic or manual power down will be required. Bourns application engineers can provide further assistance.

## Performance Graphs

### V-I Characteristic - TBU-DT085-300-WH (Pin 2-1 & Pin 3-4)



### Typical Trigger Current vs. Temperature



Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

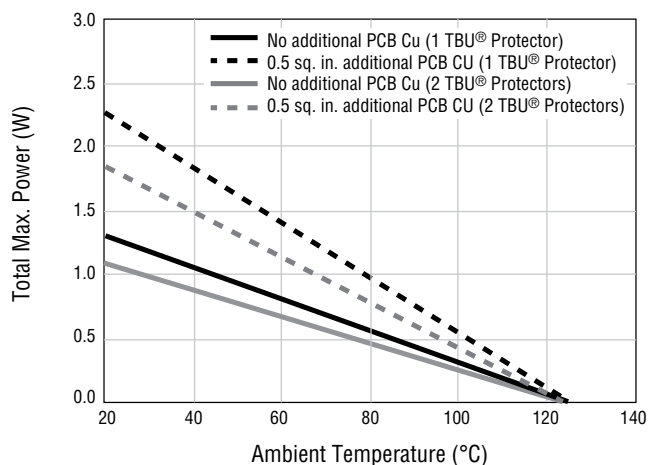
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# TBU-DT Series - TBU® High-Speed Protectors

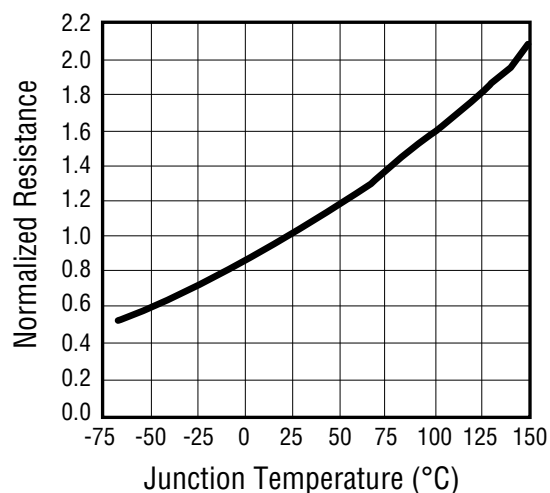
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## Performance Graphs (Continued)

### Power Derating Curve

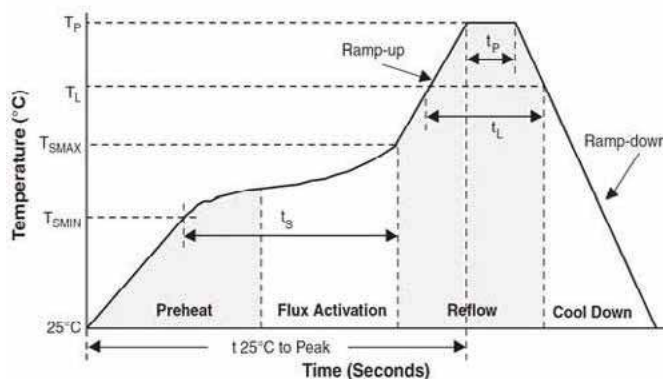


### Typical Resistance vs. Temperature



## Reflow Profile

| Profile Feature                                           | Pb-Free Assembly |
|-----------------------------------------------------------|------------------|
| Average Ramp-Up Rate (T <sub>sm</sub> to T <sub>p</sub> ) | 3 °C/sec. max.   |
| Preheat                                                   |                  |
| - Temperature Min. (T <sub>sm</sub> )                     | 150 °C           |
| - Temperature Max. (T <sub>sm</sub> )                     | 200 °C           |
| - Time (t <sub>sm</sub> to t <sub>sm</sub> )              | 60-180 sec.      |
| Time maintained above:                                    |                  |
| - Temperature (T <sub>L</sub> )                           | 217 °C           |
| - Time (t <sub>L</sub> )                                  | 60-150 sec.      |
| Peak/Classification Temperature (T <sub>p</sub> )         | 260 °C           |
| Time within 5 °C of Actual Peak Temp. (t <sub>p</sub> )   | 20-40 sec.       |
| Ramp-Down Rate                                            | 6 °C/sec. max.   |
| Time 25 °C to Peak Temperature                            | 8 min. max.      |



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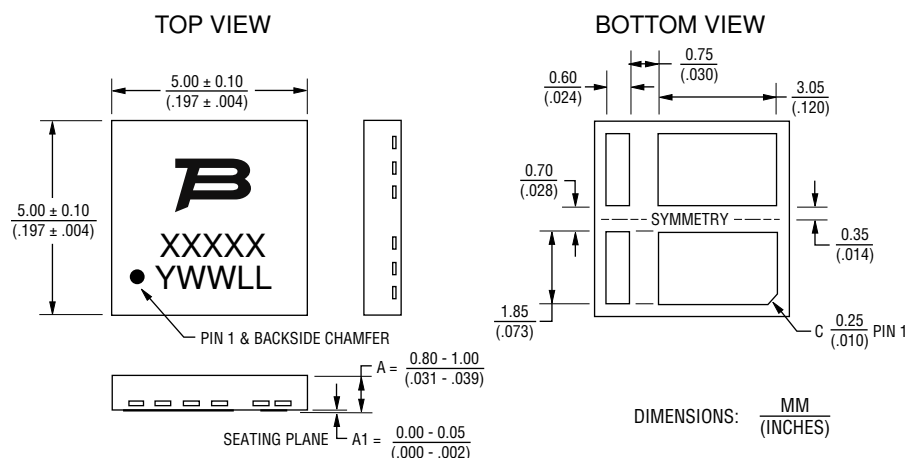
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# TBU-DT Series - TBU® High-Speed Protectors

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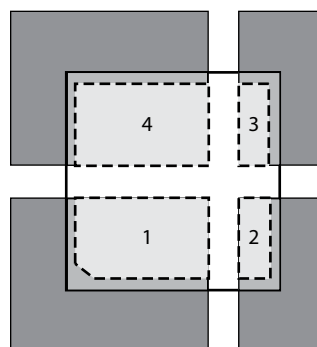
## Product Dimensions



## Recommended Pad Layout

TBU® High-Speed Protectors have a 100 % matte-tin termination finish. For improved thermal dissipation, the recommended layout uses PCB copper areas which extend beyond the exposed solder pad. The exposed solder pads should be defined by a solder mask which matches the pad layout of the TBU® device in size and spacing. It is recommended that they should be the same dimension as the TBU® pads but if smaller solder pads are used, they should be centered on the TBU® package terminal pads and not more than 0.10-0.12 mm (0.004-0.005 in.) smaller in overall width or length. Solder pad areas should not be larger than the TBU® pad sizes to ensure adequate clearance is maintained. The

recommended stencil thickness is 0.10-0.12 mm (0.004-0.005 in.) with a stencil opening size 0.025 mm (0.0010 in.) less than the solder pad size. Extended copper areas beyond the solder pad significantly improve the junction to ambient thermal resistance, resulting in operation at lower junction temperatures with a corresponding benefit of reliability. All pads should soldered to the PCB, including pads marked as NC or NU but no electrical connection should be made to these pads. For minimum parasitic capacitance, it is recommended that signal, ground or power signals are not routed beneath any pad.

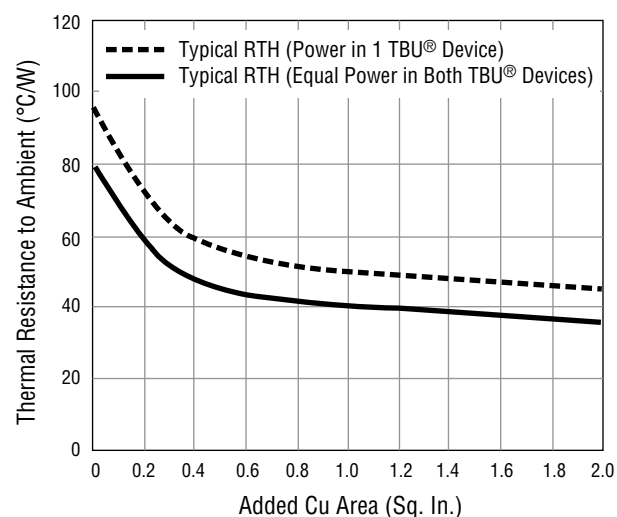


**Pad Designation**

| Pad # | Pin Out     |
|-------|-------------|
| 1     | Line Side 1 |
| 2     | Load Side 1 |
| 3     | Load Side 2 |
| 4     | Line Side 2 |

Dark grey areas show added PCB copper area for better thermal resistance.

## Thermal Resistance vs. Additional PCB Cu Area



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# TBU-DT Series - TBU® High-Speed Protectors

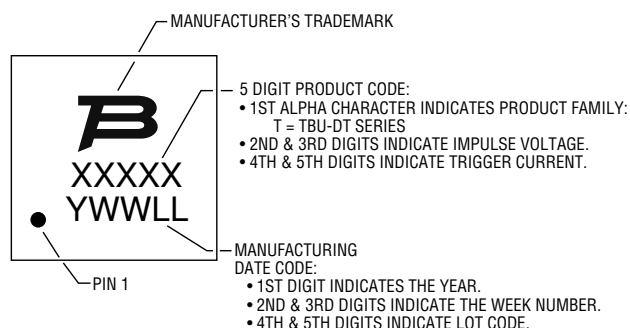
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## How to Order

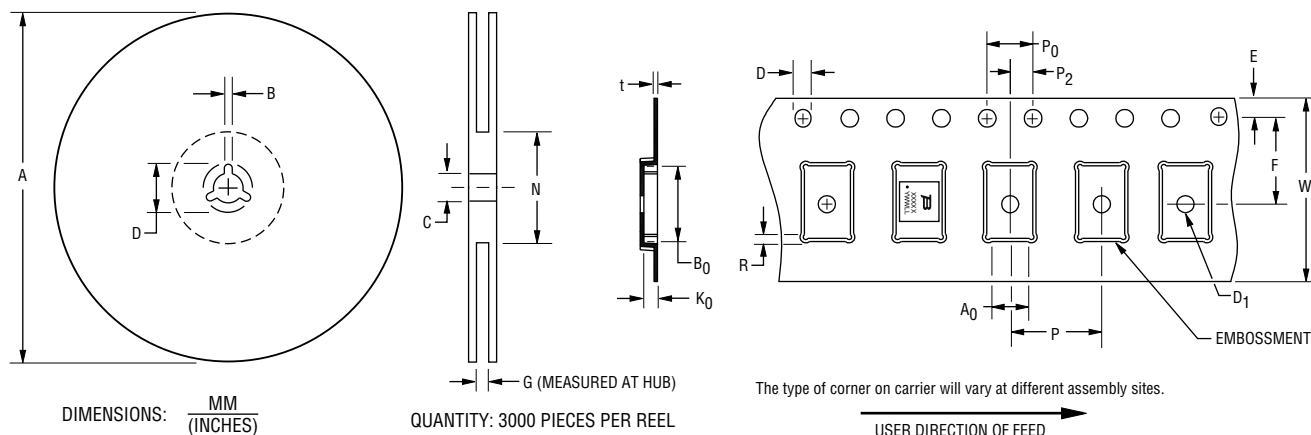
**TBU - DT 065 - 500 - WH**

TBU® Product \_\_\_\_\_  
 Series \_\_\_\_\_  
 DT = Dual Uni-Series  
 Impulse Voltage Rating \_\_\_\_\_  
 065 = 650 V  
 085 = 850 V  
 Trigger Current \_\_\_\_\_  
 100 = 100 mA    300 = 300 mA  
 200 = 200 mA    500 = 500 mA  
 Hold to Trip Ratio Suffix \_\_\_\_\_  
 W = Hold to Trip Ratio  
 Package Suffix \_\_\_\_\_  
 H = DFN Package

## Typical Part Marking



## Packaging Specifications



| A        |          | B      |        | C      |        | D      |      | G      | N       |
|----------|----------|--------|--------|--------|--------|--------|------|--------|---------|
| Min.     | Max.     | Min.   | Max.   | Min.   | Max.   | Min.   | Max. | Ref.   | Ref.    |
| 326      | 330      | 1.5    | 2.5    | 12.8   | 13.5   | 20.2   | —    | 16.5   | 102     |
| (12.835) | (13.002) | (.059) | (.098) | (.504) | (.531) | (.795) |      | (.650) | (4.016) |

| A0     |        | B0     |        | D      |        | D1     |      | E      |        | F      |        |
|--------|--------|--------|--------|--------|--------|--------|------|--------|--------|--------|--------|
| Min.   | Max.   | Min.   | Max.   | Min.   | Max.   | Min.   | Max. | Min.   | Max.   | Min.   | Max.   |
| 5.15   | 5.35   | 5.15   | 5.35   | 1.5    | 1.6    | 1.5    | —    | 1.65   | 1.85   | 5.45   | 5.55   |
| (.203) | (.211) | (.203) | (.211) | (.059) | (.063) | (.059) |      | (.065) | (.073) | (.214) | (.218) |

| K0     |        | P      |        | P0     |        | P2     |        | R    |        | t      |        |
|--------|--------|--------|--------|--------|--------|--------|--------|------|--------|--------|--------|
| Min.   | Max.   | Min.   | Max.   | Min.   | Max.   | Min.   | Max.   | Min. | Max.   | Min.   | Max.   |
| 1.0    | 1.2    | 7.9    | 8.1    | 3.8    | 4.2    | 1.95   | 2.05   | 0    | 0.5    | 0.25   | 0.35   |
| (.039) | (.047) | (.311) | (.319) | (.150) | (.165) | (.077) | (.081) | (0)  | (.020) | (.010) | (.014) |

| W      |        |
|--------|--------|
| Min.   | Max.   |
| 11.7   | 12.3   |
| (.461) | (.484) |

DIMENSIONS:  $\frac{\text{MM}}{(\text{INCHES})}$

REV. 02/19

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