

# ANTREX Electronics

Electronic Components Sourcing Information

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## Product Reference Information

**Part Number:** TBU-CA065-050-WH-Q  
**Manufacturer:** Bourns Inc.  
**Package:** 3-SMD  
**Availability:** Please confirm with sales

**Product Page:**

<https://antrexco.com/product/details/bourns-inc/tbu-ca065-050-wh-q.html>

**Request a Quote:**

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## Contact Information

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## Quality & Trust

New & Original Components

Supplier verification and packaging condition review

CoC / RoHS / REACH documents available on request

Secure sourcing support for OEM / EMS projects

**Note:**

This PDF includes an ANTREX product reference cover page.

The original manufacturer datasheet follows from the next page.

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## Features

- Superior circuit protection
- Overcurrent and overvoltage protection
- Blocks surges up to rated limits
- High-speed performance
- Small SMT package
- Agency listing: 
- RoHS\* and AEC-Q101 compliant\*\*

## Applications

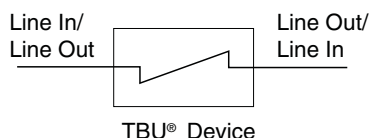
- Voice / VDSL cards
- Protection modules and dongles
- Process control equipment
- Test and measurement equipment
- General electronics

# TBU-CA-Q Series - TBU® High-Speed Protectors

## General Information

The TBU-CA-Q Series of Bourns® TBU® products are low capacitance single bidirectional high-speed protection components, constructed using MOSFET semiconductor technology, and designed to protect against faults caused by short circuits, AC power cross, induction and lightning surges.

The TBU® high-speed protector placed in the system circuit will monitor the current with the MOSFET detection circuit triggering to provide an effective barrier behind which sensitive electronics will not be exposed to large voltages or currents during surge events up to the device's specified maximum limits. The TBU® device is provided in a halogen free\*\*\*, surface mount DFN package and meets industry standard requirements such as RoHS and Pb Free solder reflow profiles.



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## Agency Listing

| Description |                                      |
|-------------|--------------------------------------|
| UL          | File Number: <a href="#">E315805</a> |

## Absolute Maximum Ratings (@ T<sub>A</sub> = 25 °C Unless Otherwise Noted)

| Symbol            | Parameter                                                    | Part Number                                                    | Value             | Unit |
|-------------------|--------------------------------------------------------------|----------------------------------------------------------------|-------------------|------|
| V <sub>imp</sub>  | Peak impulse voltage withstand with duration less than 10 ms | TBU-CA025-xxx-WH-Q<br>TBU-CA065-xxx-WH-Q<br>TBU-CA085-xxx-WH-Q | 250<br>650<br>850 | V    |
| V <sub>rms</sub>  | Continuous A.C. RMS voltage                                  | TBU-CA025-xxx-WH-Q<br>TBU-CA065-xxx-WH-Q<br>TBU-CA085-xxx-WH-Q | 100<br>300<br>425 | V    |
| T <sub>op</sub>   | Operating temperature range                                  |                                                                | -55 to +125       | °C   |
| T <sub>stg</sub>  | Storage temperature range                                    |                                                                | -65 to +150       | °C   |
| T <sub>jmax</sub> | Maximum junction temperature                                 |                                                                | +125              | °C   |
| ESD               | HBM ESD protection per IEC 61000-4-2                         |                                                                | ±2                | kV   |

## Environmental Characteristics

| Parameter                  | Value |
|----------------------------|-------|
| Moisture Sensitivity Level | 1     |
| ESD Classification (HBM)   | 1A    |



**WARNING Cancer and Reproductive Harm** - [www.P65Warnings.ca.gov](http://www.P65Warnings.ca.gov)

\* RoHS Directive 2015/863, Mar 31, 2015 and Annex.

\*\* "Q" part number suffix for automotive and other applications requiring appropriate AEC-Q101 compliance.

\*\*\* Bourns considers a product to be "halogen free" if (a) the Bromine (Br) content is 900 ppm or less; (b) the Chlorine (Cl) content is 900 ppm or less; and (c) the total Bromine (Br) and Chlorine (Cl) content is 1500 ppm or less.

Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

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# TBU-CA-Q Series - TBU® High-Speed Protectors

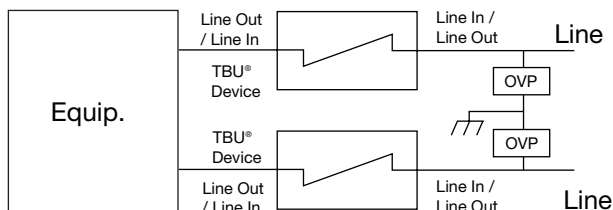
**BOURNS®**

## Electrical Characteristics (@ T<sub>A</sub> = 25 °C Unless Otherwise Noted)

| Symbol               | Parameter                                                                                         | Part Number                                                                         | Min. | Typ. | Max. | Unit |
|----------------------|---------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------|------|------|------|------|
| I <sub>trigger</sub> | Current required for the device to go from operating state to protected state                     | TBU-CAxxx-050-WH-Q                                                                  | 50   | 75   | 100  | mA   |
|                      |                                                                                                   | TBU-CAxxx-100-WH-Q                                                                  | 100  | 150  | 200  |      |
|                      |                                                                                                   | TBU-CAxxx-300-WH-Q                                                                  | 300  | 450  | 600  |      |
|                      |                                                                                                   | TBU-CAxxx-500-WH-Q                                                                  | 500  | 750  | 1000 |      |
| R <sub>device</sub>  | Series resistance of the TBU® device                                                              | V <sub>imp</sub> = 250 V I <sub>trigger</sub> (min.) = 50 mA<br>TBU-CA025-050-WH-Q  |      | 13.3 | 15.3 | Ω    |
|                      |                                                                                                   | V <sub>imp</sub> = 650 V I <sub>trigger</sub> (min.) = 50 mA<br>TBU-CA065-050-WH-Q  |      | 17.7 | 20.3 |      |
|                      |                                                                                                   | V <sub>imp</sub> = 650 V I <sub>trigger</sub> (min.) = 100 mA<br>TBU-CA065-100-WH-Q |      | 11.5 | 13.2 |      |
|                      |                                                                                                   | V <sub>imp</sub> = 650 V I <sub>trigger</sub> (min.) = 300 mA<br>TBU-CA065-300-WH-Q |      | 7.6  | 8.8  |      |
|                      |                                                                                                   | V <sub>imp</sub> = 850 V I <sub>trigger</sub> (min.) = 100 mA<br>TBU-CA085-100-WH-Q |      | 15.2 | 17.4 |      |
|                      |                                                                                                   | V <sub>imp</sub> = 850 V I <sub>trigger</sub> (min.) = 500 mA<br>TBU-CA085-500-WH-Q |      | 10.7 | 12.2 |      |
| t <sub>block</sub>   | Time for the device to go from normal operating state to protected state                          |                                                                                     |      |      | 1    | μs   |
| I <sub>Q</sub>       | Current through the triggered TBU® device with 50 Vdc circuit voltage                             |                                                                                     | 0.25 | 0.50 | 1.00 | mA   |
| V <sub>reset</sub>   | Voltage below which the triggered TBU® device will transition to normal operating state           |                                                                                     | 12   | 16   | 20   | V    |
| R <sub>th(j-l)</sub> | Junction to package pads - FR4 using recommended pad layout                                       |                                                                                     |      | 129  |      | °C/W |
| R <sub>th(j-l)</sub> | Junction to package pads - FR4 using heat sink on board (6 cm <sup>2</sup> ) (1 in <sup>2</sup> ) |                                                                                     |      | 40   |      | °C/W |

## Reference Application

The TBU® devices are general use protectors used in a wide variety of applications. The maximum voltage rating of the TBU® device should never be exceeded. Where necessary, an OVP should be employed to limit the maximum voltage. A cost-effective protection solution combines Bourns® TBU® protection devices with a pair of Bourns® MOVs. For bandwidth sensitive applications, a Bourns® GDT may be substituted for the MOV.



## Basic TBU Operation

The TBU® device, constructed using MOSFET semiconductor technology, placed in the system circuit will monitor the current with the MOSFET detection circuit triggering to provide an effective barrier behind which sensitive electronics are not exposed to large voltages or currents during surge events up to the device's specified maximum limits. The TBU® device operates in approximately 1 μs - once line current exceeds the TBU® device's trigger current I<sub>trigger</sub>. When operated, the TBU® device will limit the current to less than the I<sub>trigger</sub> value within the t<sub>block</sub> duration. If voltage above V<sub>reset</sub> is continuously sustained, the TBU® device will subsequently reduce the current to a quiescent current level within a period of time that is dependent upon the applied voltage.

After the surge, the TBU® device resets when the voltage across the TBU® device falls to the V<sub>reset</sub> level. The TBU® device will automatically reset on lines which have no DC bias or have DC bias below V<sub>reset</sub> (such as unpowered signal lines).

If the line has a normal DC bias above V<sub>reset</sub>, the voltage across the TBU® device may not fall below V<sub>reset</sub> after the surge. In such cases, special care needs to be taken to ensure that the TBU® device will reset, with software monitoring as one method used to accomplish this. Bourns application engineers can provide further assistance.

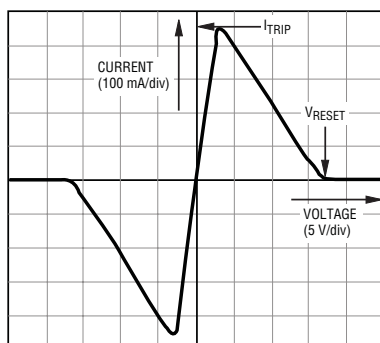
Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

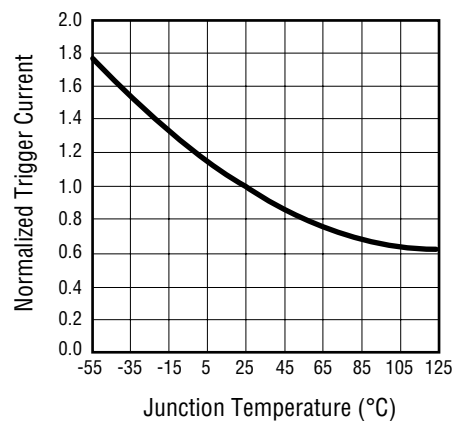
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## Performance Graphs

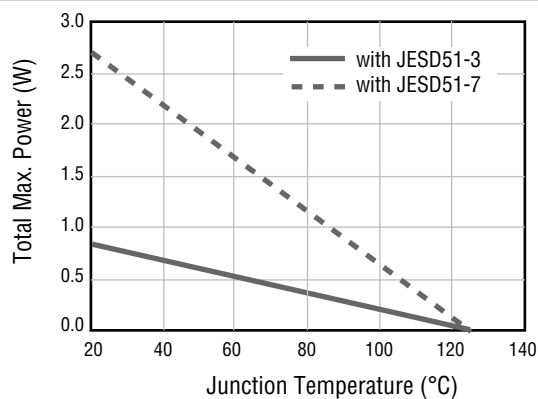
### Typical V-I Characteristics



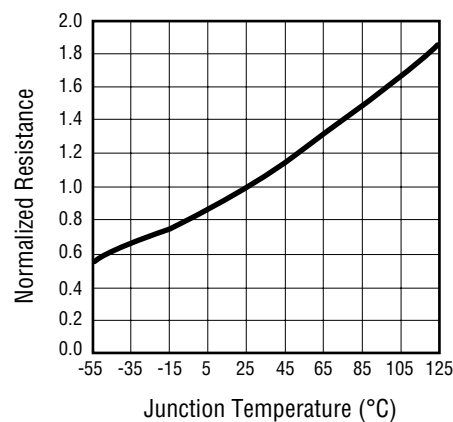
### Typical Trigger Current vs. Temperature



### Power Derating Curve



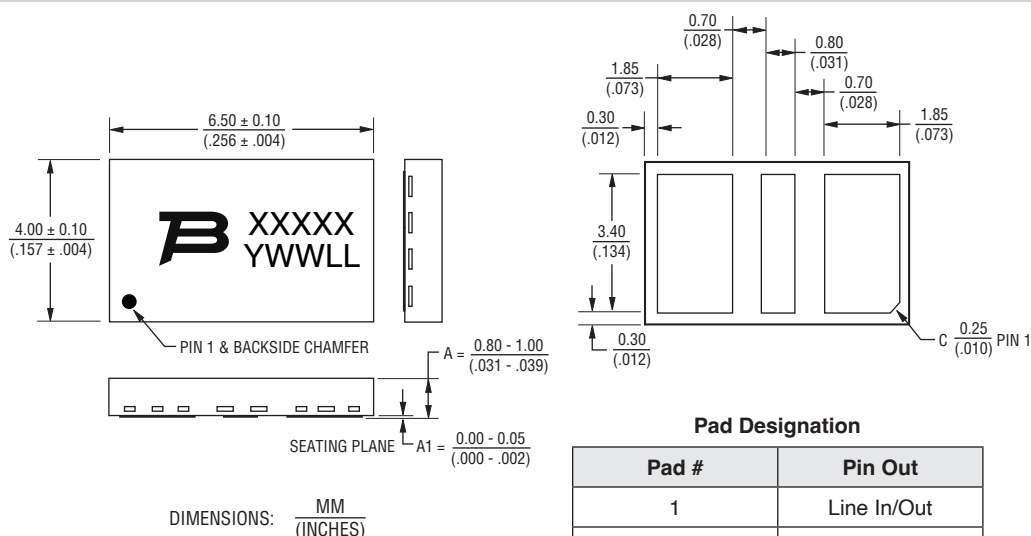
### Typical Resistance vs. Temperature



# TBU-CA-Q Series - TBU® High-Speed Protectors

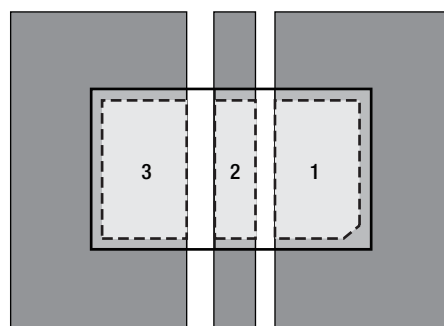
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## Product Dimensions



## Recommended Pad Layout

TBU® High-Speed Protectors have a 100 % matte-tin termination finish. For improved thermal dissipation, the recommended layout uses PCB copper areas which extend beyond the exposed solder pad. The exposed solder pads should be defined by a solder mask which matches the pad layout of the TBU® device in size and spacing. For best performance, Bourns recommends that solder pads be the same dimension as the TBU® pads, but if smaller solder pads are used, they should be centered on the TBU® package terminal pads and not be more than 0.10-0.12 mm (0.004-0.005 in.) smaller in overall width or length. Solder pad areas should not be larger than the TBU® pad sizes to ensure adequate clearance is maintained. The recommended stencil thickness is 0.10-0.12 mm (0.004-0.005 in.) with a stencil opening size 0.025 mm (0.0010 in.) less than the solder pad size. Extended copper areas beyond the solder pad significantly improve the junction to ambient thermal resistance, resulting in operation at lower junction temperatures with a corresponding benefit of reliability. All pads should be soldered to the PCB, including pads marked as NC or NU but no electrical connection should be made to these pads. For minimum parasitic capacitance, Bourns recommends that ground or power signals not be routed beneath any pad.



*Dark grey areas show added PCB copper area for better thermal resistance.*

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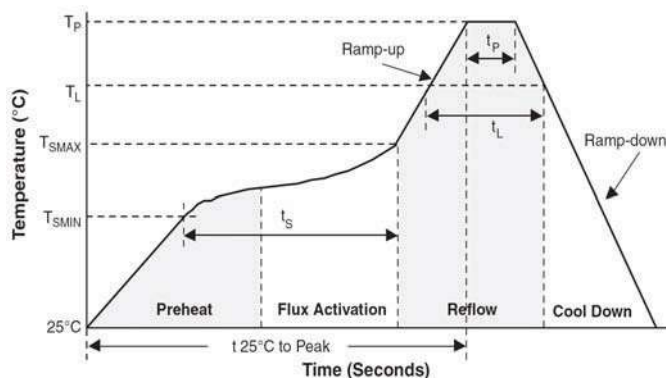
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# TBU-CA-Q Series - TBU® High-Speed Protectors

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## Reflow Profile

| Profile Feature                                                                                                                                                                                   | Pb-Free Assembly                |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------|
| Average Ramp-Up Rate (T <sub>smax</sub> to T <sub>p</sub> )                                                                                                                                       | 3 °C/sec. max.                  |
| Preheat <ul style="list-style-type: none"> <li>- Temperature Min. (T<sub>sm</sub>)</li> <li>- Temperature Max. (T<sub>smax</sub>)</li> <li>- Time (t<sub>sm</sub> to t<sub>smax</sub>)</li> </ul> | 150 °C<br>200 °C<br>60-180 sec. |
| Time maintained above: <ul style="list-style-type: none"> <li>- Temperature (T<sub>L</sub>)</li> <li>- Time (t<sub>L</sub>)</li> </ul>                                                            | 217 °C<br>60-150 sec.           |
| Peak/Classification Temperature (T <sub>p</sub> )                                                                                                                                                 | 260 °C                          |
| Time within 5 °C of Actual Peak Temp. (t <sub>p</sub> )                                                                                                                                           | 20-40 sec.                      |
| Ramp-Down Rate                                                                                                                                                                                    | 6 °C/sec. max.                  |
| Time 25 °C to Peak Temperature                                                                                                                                                                    | 8 min. max.                     |

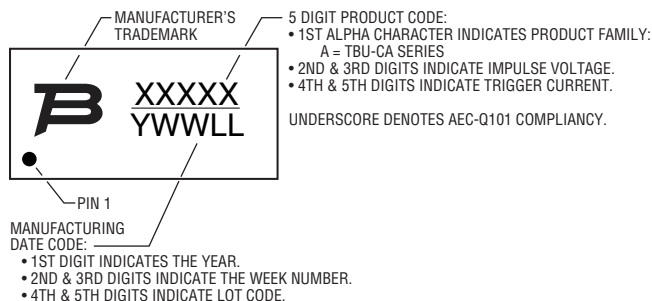


## How to Order

**TBU - CA xxx - yyy - WH - Q**

TBU® Product \_\_\_\_\_  
Series \_\_\_\_\_  
CA = Bi-Series  
Impulse Voltage Rating \_\_\_\_\_  
025 = 250 V  
065 = 650 V  
085 = 850 V  
Trigger Current \_\_\_\_\_  
050 = 50 mA  
100 = 100 mA  
300 = 300 mA  
500 = 500 mA  
Hold to Trip Ratio Suffix \_\_\_\_\_  
W = Hold to Trip Ratio = 2  
Package Suffix \_\_\_\_\_  
H = DFN Package  
AEC-Q101 Suffix \_\_\_\_\_  
Q = AEC-Q101 Compliant

## Typical Part Marking



Specifications are subject to change without notice.

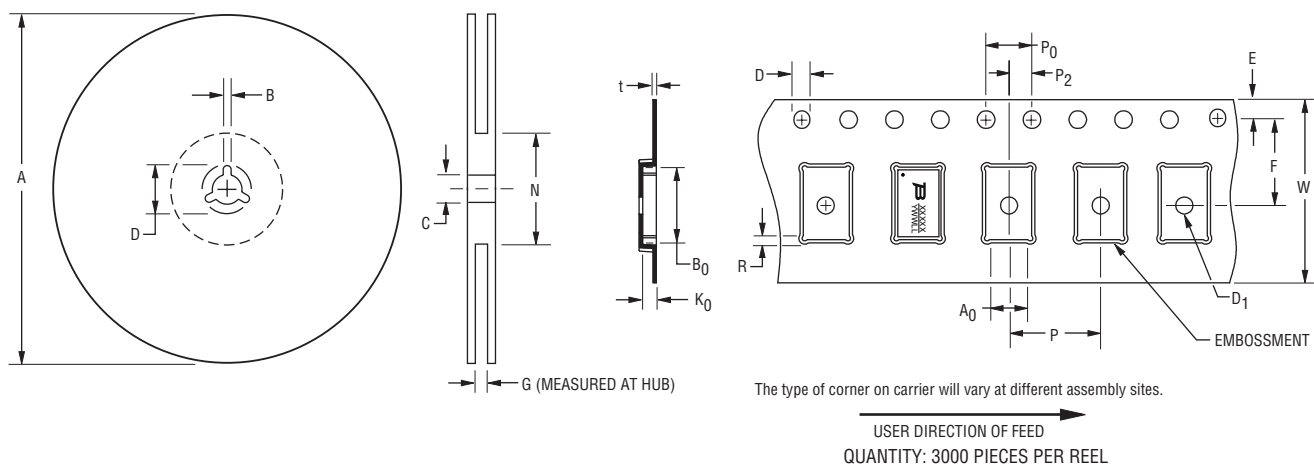
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# TBU-CA-Q Series - TBU® High-Speed Protectors

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## Packaging Specifications



| A               |                 | B             |               | C              |                | D              |      | G              | N              |
|-----------------|-----------------|---------------|---------------|----------------|----------------|----------------|------|----------------|----------------|
| Min.            | Max.            | Min.          | Max.          | Min.           | Max.           | Min.           | Max. | Ref.           | Ref.           |
| 326<br>(12.835) | 330<br>(13.002) | 1.5<br>(.059) | 2.5<br>(.098) | 12.8<br>(.504) | 13.5<br>(.531) | 20.2<br>(.795) | —    | 16.5<br>(.650) | 102<br>(4.016) |

| A0            |               | B0            |               | D             |               | D1            |      | E              |                | F             |               |
|---------------|---------------|---------------|---------------|---------------|---------------|---------------|------|----------------|----------------|---------------|---------------|
| Min.          | Max.          | Min.          | Max.          | Min.          | Max.          | Min.          | Max. | Min.           | Max.           | Min.          | Max.          |
| 4.3<br>(.169) | 4.5<br>(.177) | 6.7<br>(.264) | 6.9<br>(.272) | 1.5<br>(.059) | 1.6<br>(.063) | 1.5<br>(.059) | —    | 1.65<br>(.065) | 1.85<br>(.073) | 7.4<br>(.291) | 7.6<br>(.299) |

| K0            |               | P             |               | P0            |               | P2            |               | R        |               | t              |                |
|---------------|---------------|---------------|---------------|---------------|---------------|---------------|---------------|----------|---------------|----------------|----------------|
| Min.          | Max.          | Min.          | Max.          | Min.          | Max.          | Min.          | Max.          | Min.     | Max.          | Min.           | Max.           |
| 1.0<br>(.039) | 1.2<br>(.047) | 7.9<br>(.311) | 8.1<br>(.319) | 3.9<br>(.159) | 4.1<br>(.161) | 1.9<br>(.075) | 2.1<br>(.083) | 0<br>(0) | 0.5<br>(.020) | 0.25<br>(.010) | 0.35<br>(.014) |

| W              |                |
|----------------|----------------|
| Min.           | Max.           |
| 15.7<br>(.618) | 16.3<br>(.642) |

DIMENSIONS:  $\frac{\text{MM}}{(\text{INCHES})}$

**BOURNS®**

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- Bourns considers implantable/invasive devices and devices/procedures designed as life-supporting or life-sustaining by the U.S. Food and Drug Administration or equivalent organizations outside of the United States as "life-critical" medical applications. Bourns expressly identifies those Bourns® standard products that are suitable for use in typical medical applications that are not life-critical in its publication entitled "Bourns Medical Grade Component Guide."
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