# ANTREX Electronics

Electronic Components Sourcing Information

## Product Reference Information

| | |
|---|---|
| **Part Number:** | 0ZAF0008FF28 |
| **Manufacturer:** | Bel Fuse Inc. |
| **Package:** | 2920 (7351 Metric), Concave |
| **Availability:** | Please confirm with sales |

**Product Page:**

https://antrexco.com/product/details/bel-fuse-inc/0zaf0008ff28.html

**Request a Quote:**

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### Contact Information

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### Quality & Trust

New & Original Components

Supplier verification and packaging condition review

CoC / RoHS / REACH documents available on request

Secure sourcing support for OEM / EMS projects

**Note:**

This PDF includes an ANTREX product reference cover page.

The original manufacturer datasheet follows from the next page.

# Surface Mount PTC
## 0ZAF Series

HF Pb 0ZAF Series – 2920 Chip RoHS Compliant

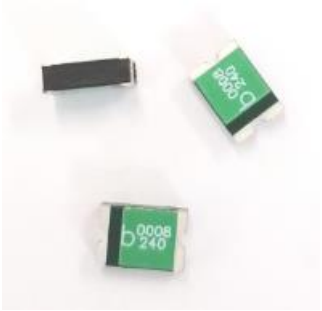
### Product Features
- 2920 Dimension, Surface mountable, Solid state, Faster time to trip than standard SMD devices.
- Full compliance with EU Directive 2011/65/EU and amending directive 2015/863
- AEC-Q Compliant
- Meets Bel automotive qualification*
  * - Largely based on internal AEC-Q test plan

### Operating (Hold Current) Range
50mA – 160mA

### Maximum Voltage
240VAC/250VAC (per table)

### Temperature Range
-40°C to 85°C

### Agency Approval
TUV (Std. EN/IEC 60738-1/60738-1-1 and EN/IEC 60730-1, Cert. R50102117)
UL Recognized Component (Std. UL1434, File E305051)



AEC-Q Compliant

LEAD FREE = Pb

HALOGEN FREE = HF

### Electrical Characteristics (23°C)

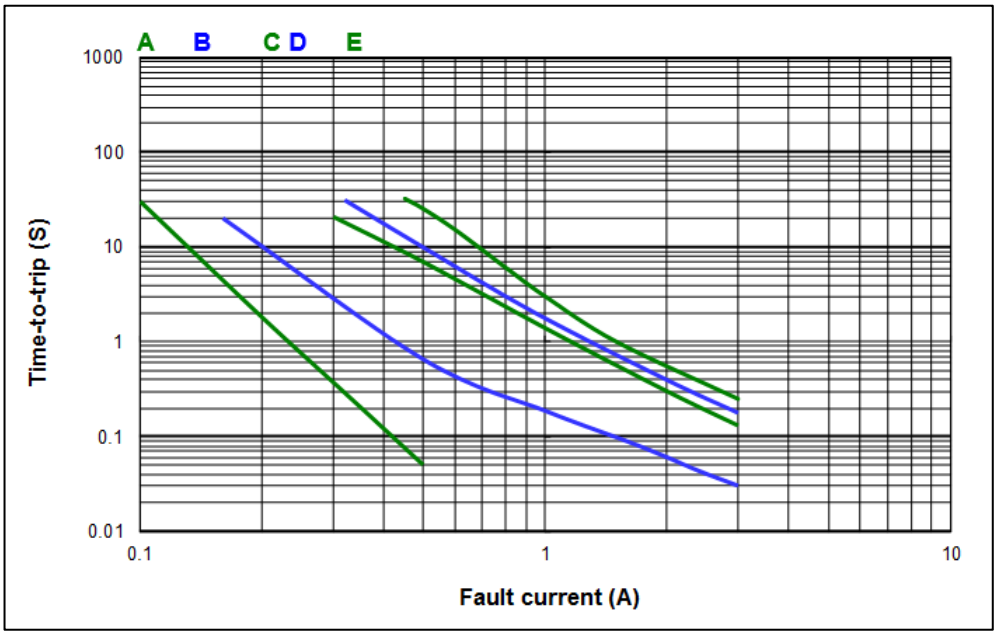
| | Part Number | Hold Current | Trip Current | Max. Current | Max. Oper. Voltage | Max. Int. Voltage | Typ. Power | Max Time to Trip | | Resistance Tolerance | | | Agency Approvals | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | | | | | | | | Current | Time | Rmin | Rmax | R1max | | |
| | | $I_H$, A | $I_T$, A | $I_{max}$, A | $V_{max}$, Vac | $V_{I-max}$, Vac | $P_d$, W | A | Sec | Ohms | Ohms | Ohms | | |
| A | 0ZAF0005FF28 | 0.05 | 0.12 | 1.0 | 240 | 250 | 1.5 | 0.25 | 15.00 | 10.0 | 55.0 | 70.0 | Y | Y |
| B | 0ZAF0008FF28 | 0.08 | 0.19 | 1.2 | 240 | 250 | 1.5 | 0.40 | 15.00 | 6.0 | 16.0 | 25.0 | Y | Y |
| C | 0ZAF0012FF28 | 0.12 | 0.30 | 1.2 | 240 | 250 | 1.5 | 0.60 | 15.00 | 6.0 | 14.0 | 20.0 | Y | Y |
| D | 0ZAF0013FF28 | 0.13 | 0.32 | 1.2 | 240 | 250 | 1.5 | 0.65 | 15.00 | 2.0 | 6.0 | 12.0 | Y | Y |
| E | 0ZAF0016FF28 | 0.16 | 0.37 | 2.0 | 240 | 250 | 1.5 | 0.80 | 15.00 | 2.0 | 6.0 | 11.0 | Y | Y |

IH Hold Current- The maximum current at which the device will not trip in still air at 23°C.
IT Trip current- The minimum current at which the device will trip in still air at 23°C.
Vmax Maximum voltage device can withstand at its rated current without suffering damage.
Imax Maximum fault current device can withstand at rated voltage (Vmax) without damage.
Pd Typical power dissipated-type amount of power dissipated by device when in tripped state in 23°C still air environment.
Rmin Minimum device resistance at 23℃ prior to tripping.
Rmax Maximum device resistance at 23℃.
R1max Maximum device resistance at 23℃ measured 1 hour after tripping or reflow soldering of 260℃ for 20 seconds.

## PTC's – Basic Theory of Operation / "Tripped" Resistance Explanation

A Bel PTC consists of a block of polymeric material containing conductive carbon granules which is sandwiched between two conductive metal plates. When this polymer block reaches approximately 125C, either due to current passing through it via conductive chains of carbon particles or due to an external heat source; it swells volumetrically. This expansion breaks apart a majority of the chains of carbon granules that run randomly between the two conductive plates. This behavior results in a sharp increase in resistance across the two plates which all but eliminates current flow through the device, allowing just enough residual current flow to maintain the block's internal temperature at 125C. Once this "tripped" state current is cut off, the polymer brick cools and shrinks to its original size, thereby allowing its broken carbon chains to reestablish themselves and permit the part to return to its low resistance state. Once cooled to room ambient, the PTC will once again exhibit a resistance less than its "R1max" rating.
At currents below the device IHOLD rating, AND at temperatures below 100C, the PTC maintains a resistance value below its R1 MAX rating.
The catalog data for each device specifies a "Typical Power" value. This is the power required to exactly match the heat lost by the tripped device to its ambient surroundings at 23C. By Ohm's Law, power can be stated as: $W = E^2/R$. Thus the approximate resistance of a "Tripped" PTC can be determined by: $R = E^2/W$, where "E" is the voltage appearing across the PTC (usually the supply's open circuit voltage), and "W" is the Typical Power value for the particular PTC.
Since the PPTC acts to maintain a constant internal temperature, its apparent resistance will change based upon applied voltage and, to a lesser degree, ambient conditions. Consider the following example....
A PTC with a Typical Power of 1 watt protecting a circuit using a 60V supply will demonstrate an apparent, tripped resistance "R" of:
$R = 60^2/1 = 3{,}600$ ohms
This same tripped device when used to protect a 12V circuit would now present an apparent resistance of:
$R = 12^2/1 = 144$ ohms
The value for Typical Power is "typical" because any physical factors that affect heat loss (such as ambient temperature or air convection) will somewhat alter the level of power that the PTC needs to maintain its internal temperature. In short, PTCs do not exhibit a constant, quantifiable tripped resistance value.

## Average Time Current Characteristic Curve at 23°C




The Average Time Current Characteristic Curve and Temperature Rerating Curve are affected by a number of variables and these curves are provided for guidance only.



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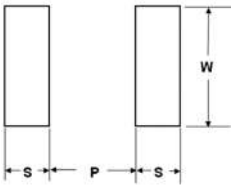
## Pad Layout

The dimensions in the table below provide the Recommended pad layout.

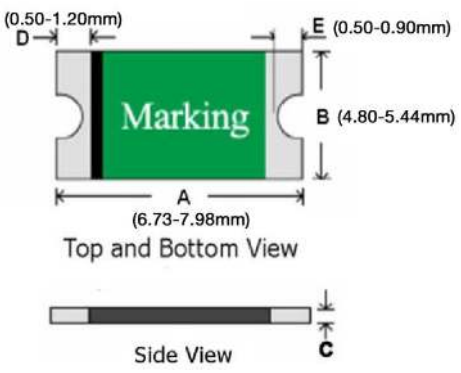



| P | | S | | W | |
|---|---|---|---|---|---|
| Nominal | | Nominal | | Nominal | |
| mm | Inch | mm | Inch | mm | Inch |
| 5.10 | 0.201 | 2.30 | 0.091 | 5.60 | 0.221 |

## Termination Pad Materials

Pure Tin

## Mechanical Dimensions and Marking



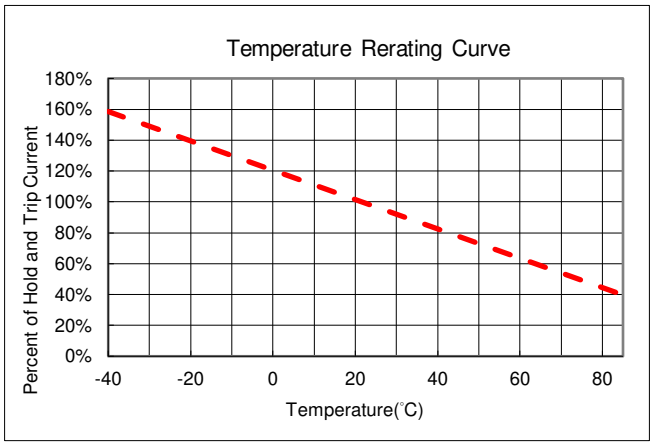

All dimensions in mm.

| Part Number | Dimensions | | Marking Code |
|---|---|---|---|
| | C | | "b", $I_H$ code |
| | Min | Max | b XXXX XXX |
| 0ZAF0005FF28 | 2.00 | 2.60 | 0005 240 |
| 0ZAF0008FF28 | 2.00 | 2.60 | 0008 240 |
| 0ZAF0012FF28 | 2.00 | 2.60 | 0012 240 |
| 0ZAF0013FF28 | 2.00 | 2.60 | 0013 240 |
| 0ZAF0016FF28 | 2.00 | 2.60 | 0016 240 |

## Temperature Derating Table

| | Temperature Derating Table | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|
| **I Hold Value** | **-40** | **-20** | **0** | **23** | **30** | **40** | **50** | **60** | **70** | **85** |
| 0ZAF | 158% | 140% | 120% | 100% | 93% | 82% | 72% | 63% | 54% | 40% |

## Thermal Derating Curve




## Cautionary Notes

1. Operation beyond the specified maximum ratings or improper use may result in damage and possible electrical arcing and/or flame.
2. These Polymer PTC (PPTC) devices are intended for protection against occasional overcurrent/overtemperature fault conditions and may not be suitable for use in applications where repeated and/or prolonged fault conditions are anticipated.
3. Avoid contact of PTC device with chemical solvent. Prolonged contact may adversely impact the PTC performance.
4. These PTC devices may not be suitable for use in circuits with a large inductance, as the PTC trip can generate circuit voltage spikes above the PTC rated voltage.
5. If potting is mandated, avoid rigid potting compounds as they will encase the PTC and prevent it from volumetrically expanding to properly respond to a trip event.
6. MSL: 2a (According to IPC J-Std-020).



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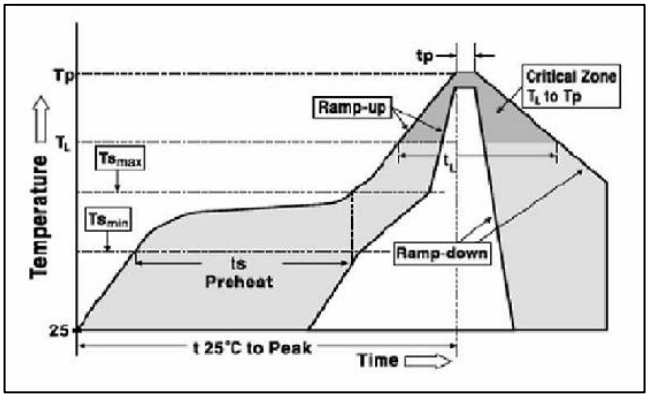
## Environmental Specifications

| | |
|---|---|
| Temperature cycling | JESD22 Method JA-104 |
| Biased humidity | MIL-STD-202 Method 103 |
| Operational life | MIL-STD-202 Method 108 |
| Resistance to solvents | MIL-STD-202 Method 215 |
| Mechanical shock | MIL-STD-202 Method 213 |
| Vibration | MIL-STD-202 Method 204 |
| Resistance to soldering heat | MIL-STD-202 Method 210 |
| Thermal shock | MIL-STD-202 Method 107 |
| Solderability | ANSI/J-STD-002 |
| Board flex(SMD) | AEC-Q200-005 |
| Terminal strength | AEC-Q200-006 |

## Solder Reflow and Rework Recommendations

| Profile Feature | Pb-Free Assembly |
|---|---|
| Average Ramp-Up Rate (Tsmax to Tp) | 3°C/second max |
| Preheat : | |
| Temperature Min (Tsmin) | 150°C |
| Temperature Max (Tsmax) | 200°C |
| Time (tsmin to tsmax) | 60-180 seconds |
| Time maintained above: | |
| Temperature(TL) | 217°C |
| Time (tL) | 60-150 seconds |
| Peak/Classification Temperature(Tp) : | 260°C |
| Time within 5°C of actual Peak : | |
| Temperature (tp) | 20-40 seconds |
| Ramp-Down Rate : | 6°C/second max. |
| Time 25°C to Peak Temperature : | 8 minutes max |




### Solder Reflow

Due to “lead free / RoHS compliant ” construction of these PTC devices , the required Temperature and Dwell Time in the “ Soldering ” zone of the reflow profile are greater than those used for non-RoHS devices.

1. Recommended reflow methods; IR, vapor phase oven, hot air oven.
2. Not Recommended For Wave Solder / Direct Immersion.
3. Recommended paste thickness range – 0.20 – 0.25mm.
4. Devices are compatible with standard industry cleaning solvents and methods.
5. MSL: 2a (According to IPC J-Std-020).

### Caution

If reflow temperature / dwell times exceed the recommended profile, the electrical performance of the PTC may be affected.

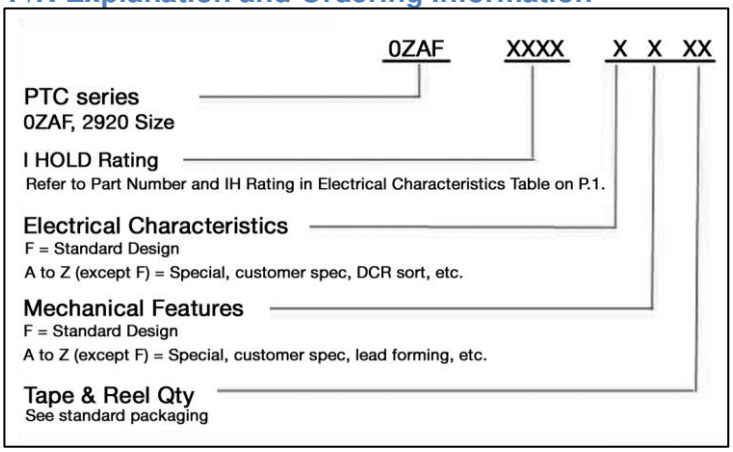
Rework: MIL-STD-202G Method 210F, Test Condition A.

## Standard Packaging

| Part Number | Tape/Reel Qty |
|---|---|
| 0ZAF0005FF28 Thru 0ZAF0016FF28 | 800 |

800 fuses in 7 inches dia. Reel, 8mm wide tape, 4mm pitch, per EIA-481(equivalent IEC-286 part 3).

## P/N Explanation and Ordering Information




Specifications subject to change without notice

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