

ANTREX Electronics

Electronic Components Sourcing Information

Product Reference Information

Part Number: BLS9G3135L-115U
Manufacturer: Ampleon USA Inc.
Package: Please confirm with sales
Availability: Please confirm with sales

Product Page:

<https://antrexco.com/product/details/ampleon-usa-inc/bls9g3135l-115u.html>

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Note:

This PDF includes an ANTREX product reference cover page.

The original manufacturer datasheet follows from the next page.

BLS9G3135L-115; BLS9G3135LS-115

LDMOS S-band radar power transistor

Rev. 1 — 25 July 2019

AMPLEON

Product data sheet

1. Product profile

1.1 General description

115 W LDMOS power transistor for S-band radar applications in the frequency range from 3.1 GHz to 3.5 GHz.

Table 1. Typical performance

Typical RF performance at $T_{case} = 25\text{ °C}$; $t_p = 300\text{ }\mu\text{s}$; $\delta = 10\%$; $I_{Dq} = 200\text{ mA}$; in a class-AB demo circuit.

Test signal	f (GHz)	V _{DS} (V)	P _L (W)	G _p (dB)	η_D (%)
pulsed RF	3.1 to 3.5	32	115	14	49

1.2 Features and benefits

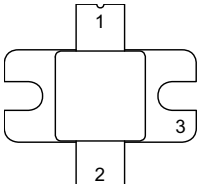
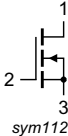
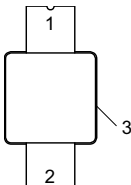
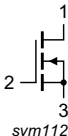
- High efficiency
- Excellent ruggedness
- Excellent thermal stability
- Easy power control
- Integrated dual sided ESD protection enables excellent off-state isolation
- High flexibility with respect to pulse formats
- Internally matched for broadband matching in 3.1 GHz to 3.5 GHz S-band operation
- For RoHS compliance see the product details on the Ampleon website

1.3 Applications

- S-band radar applications in the frequency range 3.1 GHz to 3.5 GHz

2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
BLS9G3135L-115 (SOT1135A)			
1	drain		 <i>sym112</i>
2	gate		
3	source [1]		
BLS9G3135LS-115 (SOT1135B)			
1	drain		 <i>sym112</i>
2	gate		
3	source [1]		

[1] Connected to flange.

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BLS9G3135L-115	-	flanged ceramic package; 2 mounting holes; 2 leads	SOT1135A
BLS9G3135LS-115	-	earless flanged ceramic package; 2 leads	SOT1135B

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Min	Max	Unit
V_{DS}	drain-source voltage	-	65	V
V_{GS}	gate-source voltage	-6	+13	V
T_{stg}	storage temperature	-65	+150	°C
T_j	junction temperature ^[1]	-	225	°C

[1] Continuous use at maximum temperature will affect the reliability. For details refer to the online MTF calculator.

5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Typ	Unit
$Z_{th(j-mb)}$	transient thermal impedance from junction to mounting base	$T_{case} = 85\text{ °C}; P_L = 115\text{ W}$		
		$t_p = 100\text{ }\mu\text{s}; \delta = 10\text{ }\%$	0.31	K/W
		$t_p = 200\text{ }\mu\text{s}; \delta = 10\text{ }\%$	0.35	K/W
		$t_p = 300\text{ }\mu\text{s}; \delta = 10\text{ }\%$	0.38	K/W
		$t_p = 500\text{ }\mu\text{s}; \delta = 10\text{ }\%$	0.44	K/W
		$t_p = 100\text{ }\mu\text{s}; \delta = 20\text{ }\%$	0.37	K/W

6. Characteristics

Table 6. DC characteristics

$T_j = 25\text{ °C}$ unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	$V_{GS} = 0\text{ V}; I_D = 1.1\text{ mA}$	65	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$V_{DS} = 10\text{ V}; I_D = 110\text{ mA}$	1.5	2	2.5	V
I_{DSS}	drain leakage current	$V_{GS} = 0\text{ V}; V_{DS} = 32\text{ V}$	-	-	1.4	μA
I_{DSX}	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75\text{ V}; V_{DS} = 10\text{ V}$	-	21	-	A
I_{GSS}	gate leakage current	$V_{GS} = 11\text{ V}; V_{DS} = 0\text{ V}$	-	-	140	nA
g_{fs}	forward transconductance	$V_{DS} = 10\text{ V}; I_D = 110\text{ mA}$	-	1	-	S
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75\text{ V}; I_D = 3.85\text{ A}$	-	0.12	-	Ω

Table 7. RF characteristics

Test signal: pulsed RF; $t_p = 300\text{ }\mu\text{s}; \delta = 10\text{ }\%$; RF performance at $V_{DS} = 32\text{ V}; I_{DQ} = 200\text{ mA}; T_{case} = 25\text{ °C}$; unless otherwise specified, in a class-AB production circuit.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
G_p	power gain	$P_L = 115\text{ W}$	12.5	14	-	dB
η_D	drain efficiency	$P_L = 115\text{ W}$	45	49	-	%
RL_{in}	input return loss	$P_L = 115\text{ W}$	-	-8	-	dB
$P_{droop(pulse)}$	pulse droop power	$P_L = 115\text{ W}$	-	0.0	0.3	dB
t_r	rise time	$P_L = 115\text{ W}$	-	6	50	ns
t_f	fall time	$P_L = 115\text{ W}$	-	6	50	ns
$P_{L(2dB)}$	output power at 2 dB gain compression		-	110	-	W

7. Test information

7.1 Ruggedness in class-AB operation

The BLS9G3135L-115 and BLS9G3135LS-115 are capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions: $V_{DS} = 32\text{ V}; I_{DQ} = 200\text{ mA}; P_L = 115\text{ W}; t_p = 300\text{ }\mu\text{s}; \delta = 10\text{ }\%$.

7.2 Impedance information

Table 8. Typical impedance

f (GHz)	Z_S (Ω)	Z_L (Ω)
3.1	$3.6 - j11.9$	$2.7 - j7.7$
3.2	$5.3 - j14.4$	$3.2 - j6.8$
3.3	$10.6 - j12.9$	$3.8 - j6.6$
3.4	$10.1 - j4.3$	$4.7 - j5.6$
3.5	$5.5 - j3.9$	$5.7 - j3.9$

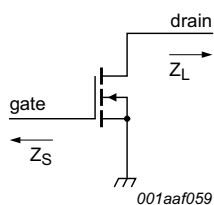


Fig 1. Definition of transistor impedance

7.3 Test circuit information

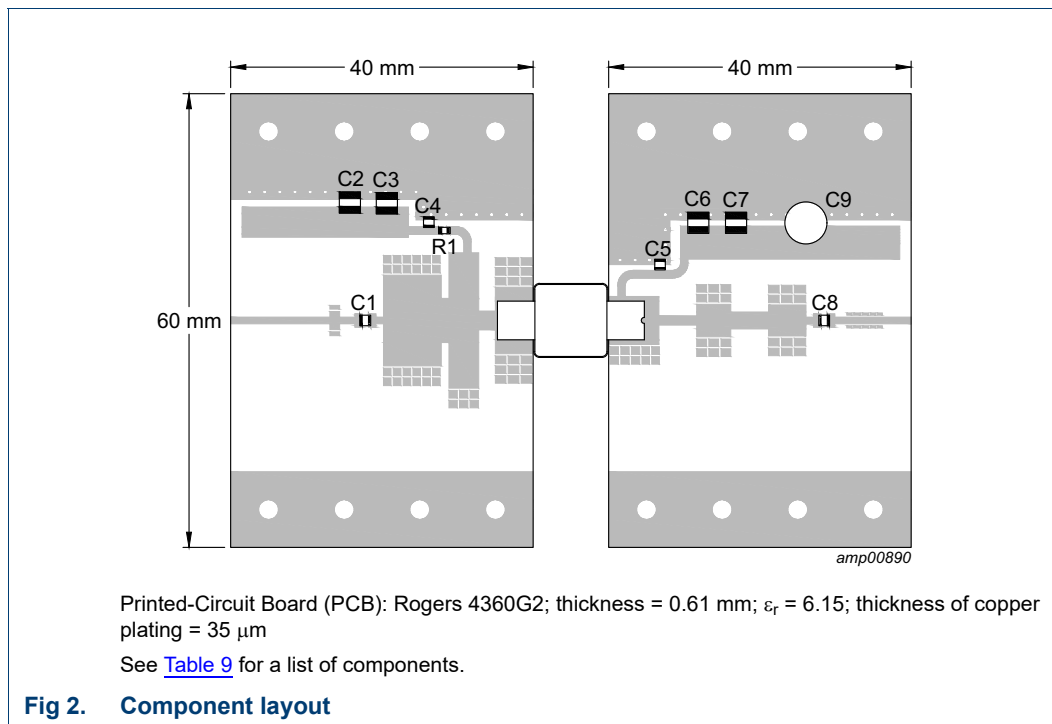
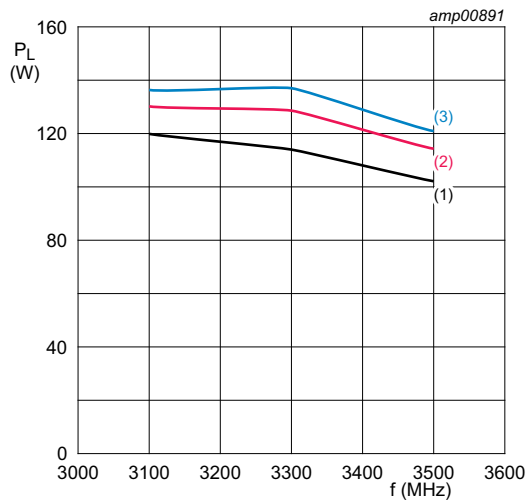


Table 9. List of components

For test circuit see [Figure 2](#).

Component	Description	Value	Remarks
C1	multilayer ceramic chip capacitor	10 pF	ATC 800A
C2, C7	multilayer ceramic chip capacitor	10 μF	Murata: GRM55DR61H106KA88L
C3, C6	multilayer ceramic chip capacitor	1 nF	ATC 100B
C4, C5	multilayer ceramic chip capacitor	10 pF	ATC 800A
C8	multilayer ceramic chip capacitor	10 pF	ATC 100A
C9	electrolytic capacitor	100 μF , 63 V	
R1	SMD resistor	5 Ω	0603

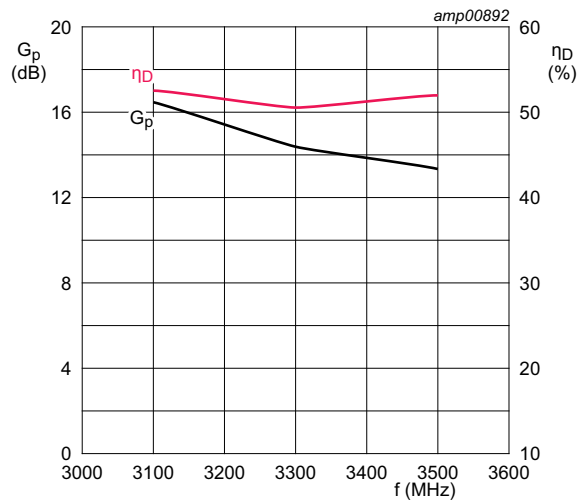
7.4 Graphical data



$V_{DS} = 32 \text{ V}$; $I_{DQ} = 200 \text{ mA}$; $t_p = 300 \text{ } \mu\text{s}$; $\delta = 10 \text{ } \%$.

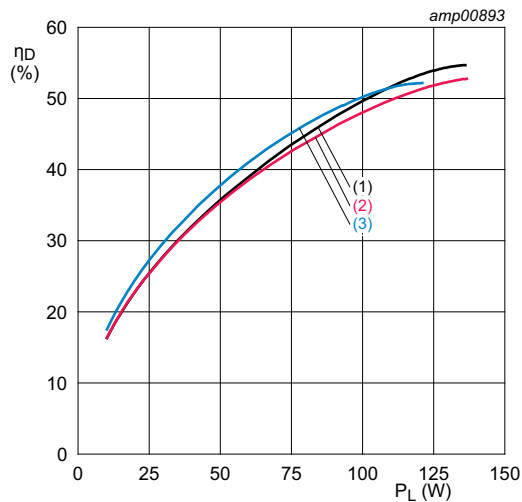
- (1) at $P_{L(1dB)}$
- (2) at $P_{L(2dB)}$
- (3) at $P_{L(3dB)}$

Fig 3. Output power as a function of frequency; typical values



$V_{DS} = 32 \text{ V}$; $I_{DQ} = 200 \text{ mA}$; $P_L = 115 \text{ W}$; $t_p = 300 \text{ } \mu\text{s}$; $\delta = 10 \text{ } \%$.

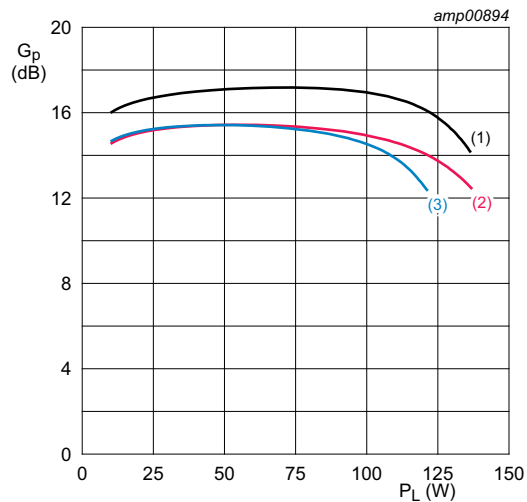
Fig 4. Power gain and drain efficiency as function of frequency; typical values



$V_{DS} = 32 \text{ V}$; $I_{DQ} = 200 \text{ mA}$; $t_p = 300 \text{ } \mu\text{s}$; $\delta = 10 \text{ } \%$.

- (1) $f = 3100 \text{ MHz}$
- (2) $f = 3300 \text{ MHz}$
- (3) $f = 3500 \text{ MHz}$

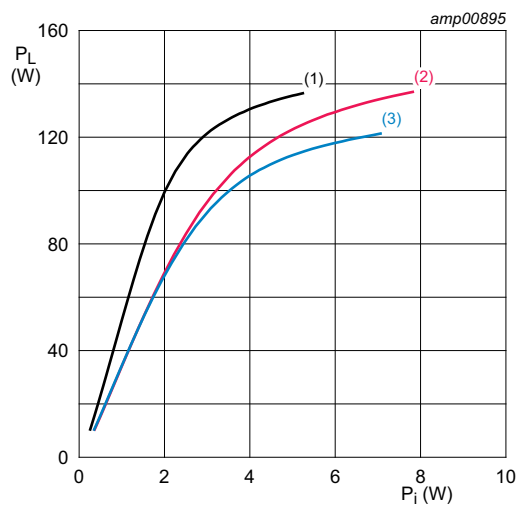
Fig 5. Drain efficiency as a function of output power; typical values



$V_{DS} = 32 \text{ V}$; $I_{DQ} = 200 \text{ mA}$; $t_p = 300 \text{ } \mu\text{s}$; $\delta = 10 \text{ } \%$.

- (1) $f = 3100 \text{ MHz}$
- (2) $f = 3300 \text{ MHz}$
- (3) $f = 3500 \text{ MHz}$

Fig 6. Power gain as a function of output power; typical values



$V_{DS} = 32 \text{ V}$; $I_{DQ} = 200 \text{ mA}$; $t_p = 300 \text{ } \mu\text{s}$; $\delta = 10 \text{ } \%$.

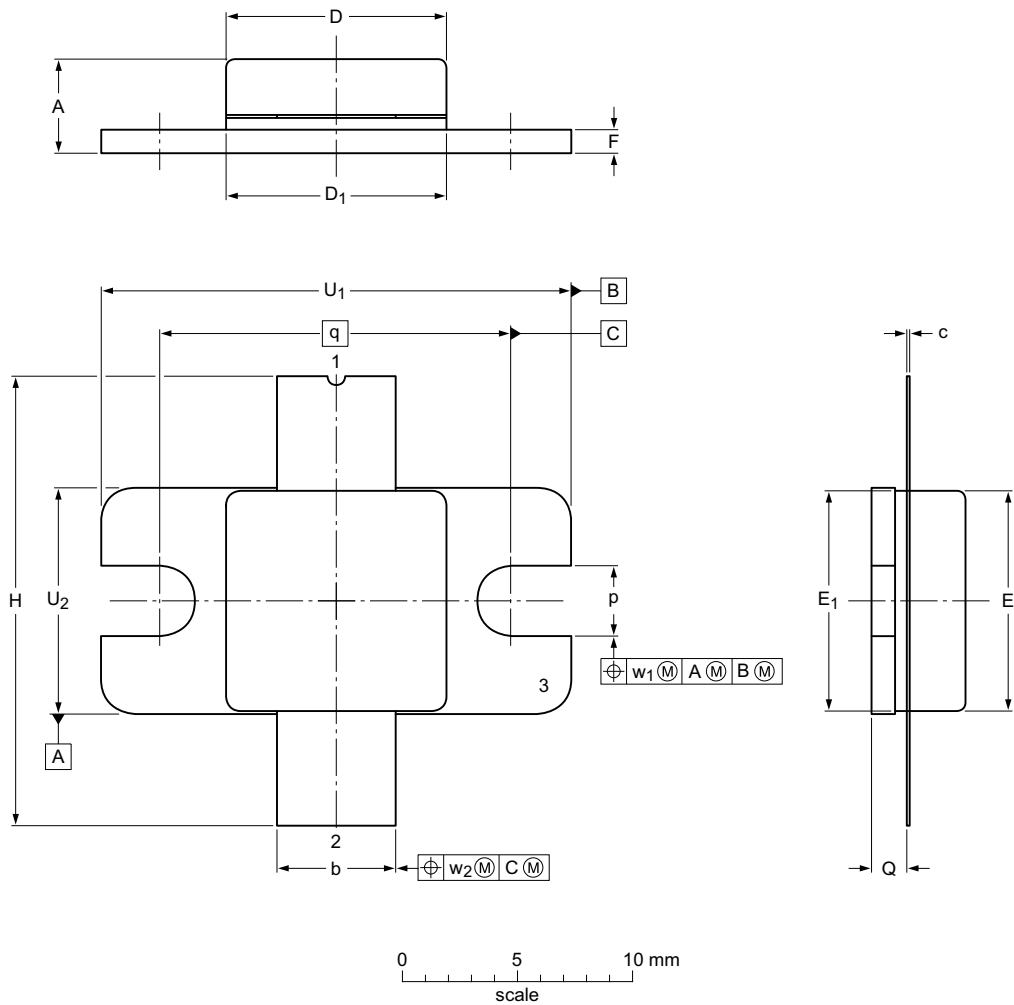
- (1) $f = 3100 \text{ MHz}$
- (2) $f = 3300 \text{ MHz}$
- (3) $f = 3500 \text{ MHz}$

Fig 7. Output power as a function of input power; typical values

8. Package outline

Flanged ceramic package; 2 mounting holes; 2 leads

SOT1135A



Dimensions

Unit ⁽¹⁾	A	b	c	D	D ₁	E	E ₁	F	H	p	Q	q	U ₁	U ₂	w ₁	w ₂
mm	max 4.65	5.26	0.18	9.65	9.65	9.65	9.65	1.14	19.94	3.30	1.70	15.24	20.45	9.91	0.25	0.51
	nom 3.76	5.00	0.10	9.40	9.40	9.40	9.40	0.89	18.92	2.92	1.45		20.19	9.65		
	min 0.183	0.207	0.007	0.38	0.38	0.38	0.38	0.045	0.785	0.130	0.067		0.805	0.39		
inches	nom 0.183	0.207	0.007	0.38	0.38	0.38	0.38	0.045	0.785	0.130	0.067	0.6	0.805	0.39	0.01	0.02
	min 0.148	0.197	0.004	0.37	0.37	0.37	0.37	0.035	0.745	0.115	0.057		0.795	0.38		

Note

1. millimeter dimensions are derived from the original inch dimensions.
2. dimension is measured 0.030 inch (0.76 mm) from the body.

sot1135a_po

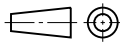
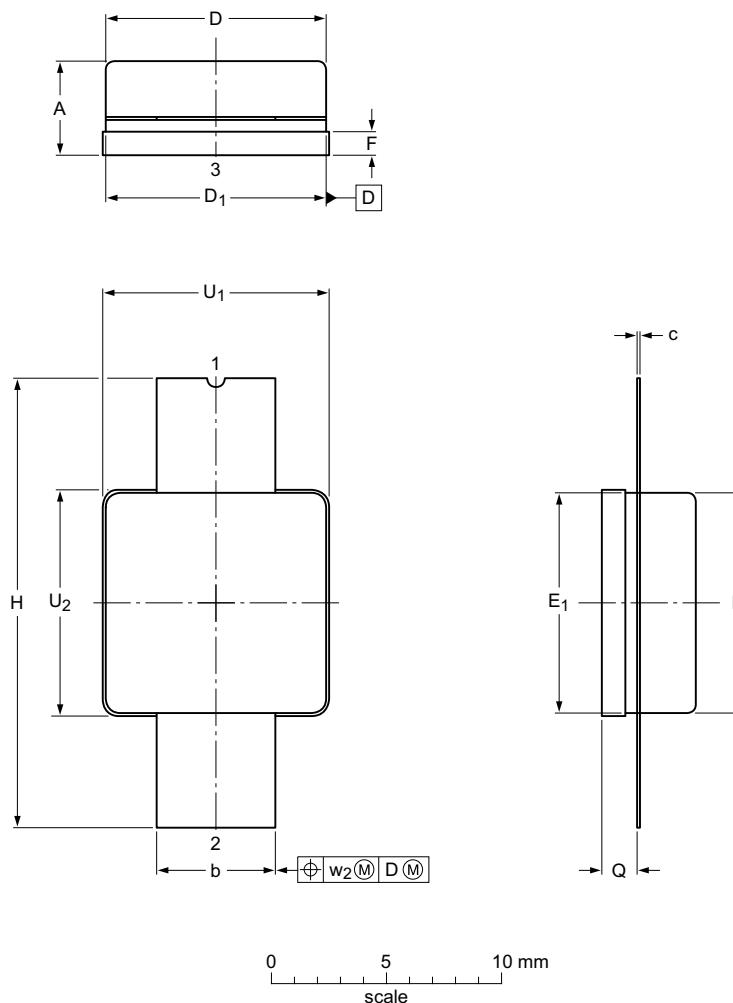
Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT1135A						-09-10-12- 09-12-14

Fig 8. Package outline SOT1135A

Earless flanged ceramic package; 2 leads

SOT1135B



Dimensions

Unit ⁽¹⁾	A	b	c	D	D ₁	E	E ₁	F	H	Q	U ₁	U ₂	w ₂
mm	max 4.65	5.26	0.18	9.65	9.65	9.65	9.65	1.14	19.94	1.70	9.91	9.91	0.51
	nom 3.76	5.00	0.10	9.40	9.40	9.40	9.40	0.89	18.92	1.45	9.65	9.65	
	min 0.183	0.207	0.007	0.38	0.38	0.38	0.38	0.045	0.785	0.067	0.39	0.39	
inches	max 0.183	0.207	0.007	0.38	0.38	0.38	0.38	0.045	0.785	0.067	0.39	0.39	0.02
	nom 0.148	0.197	0.004	0.37	0.37	0.37	0.37	0.035	0.745	0.057	0.38	0.38	
	min 0.148	0.197	0.004	0.37	0.37	0.37	0.37	0.035	0.745	0.057	0.38	0.38	

Note

1. millimeter dimensions are derived from the original inch dimensions.

2. dimension is measured 0.030 inch (0.76 mm) from the body.

sot1135b_po

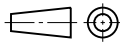
Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT1135B						09-10-12 09-12-14

Fig 9. Package outline SOT1135B

9. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the *ANSI/ESD S20.20*, *IEC/ST 61340-5*, *JESD625-A* or equivalent standards.

Table 10. ESD sensitivity

ESD model	Class
Charged Device Model (CDM); According to ANSI/ESDA/JEDEC standard JS-002	C2A [1]
Human Body Model (HBM); According to ANSI/ESDA/JEDEC standard JS-001	2 [2]

[1] CDM classification C2A is granted to any part that passes after exposure to an ESD pulse of 500 V.

[2] HBM classification 2 is granted to any part that passes after exposure to an ESD pulse of 2000 V.

10. Abbreviations

Table 11. Abbreviations

Acronym	Description
ESD	ElectroStatic Discharge
LDMOS	Laterally Diffused Metal-Oxide Semiconductor
MTF	Median Time to Failure
S-band	Short wave Band
RoHS	Restriction of Hazardous Substances
SMD	Surface Mounted Device
VSWR	Voltage Standing-Wave Ratio

11. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLS9G3135L-115_3135LS-115 v.1	20190725	Product data sheet	-	-

12. Legal information

12.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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[1] Please consult the most recently issued document before initiating or completing a design.

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13. Contact information

For more information, please visit: <http://www.ampleon.com>

For sales office addresses, please visit: <http://www.ampleon.com/sales>

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